PRELIMINARY

SDR705 thru SDR720

SOLID STATE DEVICES, INC

14849 Firestone Boulevard · La Mirada,CA 90638
Phone: (714) 670-SSDI (7734) · Fax: (714) 522-7424

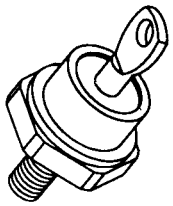
70 AMP 50-200 VOLTS 50 nsec ULTRA FAST RECTIFIER

Designer's Data Sheet

FEATURES:

- Ultra Fast Recovery: 50 nsec Maximum
- Low Forward Voltage Drop
- Low Reverse Leakage
- High Surge Current Capability
- Hermetically Sealed
- Single Chip Construction
- For High Efficiency Applications
- TX, TXV and Space Level Screening Available

DO-5



MAXIMUM RATINGS

RATING	SYMBOL	VALUE	UNIT
Peak Repetitive Reverse and DC Blocking Voltage **SDR705** **SDR710** **SDR715** **SDR720**	V_{RRM} V_{RWM} V_R	50 100 150 200	**Volts**
Average Rectified Forward Current (Resistive Load, 60Hz, Sine Wave, TA=25°C)	I_O	70	**Amps**
Peak Surge Current (8.3 ms Pulse, Half Sine Wave, TA=25°C)	I_{FSM}	750	**Amps**
Operating and storage temperature	**Top & Tstg**	-55 to +175	**°C**
Maximum Thermal Resistance Junction to Case	$R_{\theta JC}$	1.0	**°C/W**

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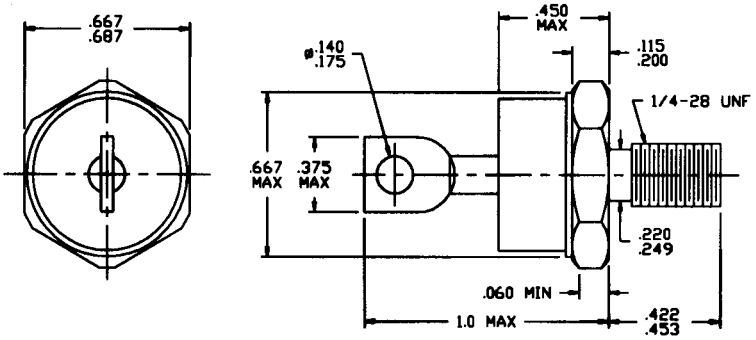
ELECTRICAL CHARACTERISTICS

CHARACTERISTICS	SYMBOL	MAXIMUM	UNIT
Instantaneous Forward Voltage Drop (I_F = 70 Adc, T_A=25°C, 300µs Pulse)	VF	0.975	Vdc
Instantaneous Forward Voltage Drop (I_F = 70 Adc, T_A=-55°C, 300µs Pulse)	VF	1.075	Vdc
Reverse Leakage Current (Rated VR, T_A=25°C, 300µs pulse minimum)	IR	25	µA
Reverse Leakage Current (Rated V_R, T_A=100°C, 300µs pulse minimum)	IR	6	mA
Junction Capacitance (V_R = 10 Vdc, T_A=25°C, f= 1 MHz)	CJ	700	pf
Reverse Recovery Time (I_F =500mA, I_R = 1A, I_{RR}=250mA, T_A=25°C)	trr	50	nsec

CASE OUTLINE: DO-5



Dimensions prior to solder dipping.

TYPICAL OPERATING CURVES

TA=25°C Unless otherwise specified