

Features

- 2.488 Gbps SONET/SDH OC-48 Compliant
- LVTTL Signal Detect Output
- LVTTL Transmitter Disable Input
- Tx Bias monitor
- Tx Power monitor
- Optional Rx Power monitor
- Low profile fits Mezzanine Card Applications
- Single +3.3V Power Supply
- LVPECL or CML outputs
- Wave Solderable / Aqueous Washable

PRODUCT OVERVIEW

The SLC210-7-X-XXX Small Form Factor MSA optical transceivers are high performance integrated duplex data links for bi-directional communication over optical fiber. The SLC210-7-X-XXX module is specifically designed to be used in single mode SONET/SDH OC-3 applications. Stratos SFF 2x10 transceivers are provided with optional receiver power monitor which enable monitoring of photodetector current. The SLC210-7-X-XXX transceiver is provided with an LC receptacle which is compatible with the industry standard LC connector. The Stratos Lightwave SFF transceivers measure 0.532 inches in width. These transceivers provide double port densities by fitting twice the number of transceivers into the same board space as a 1x9 transceiver. This saves on system costs and can reduce overall design time. The SLC210-7-X-XXX operates at +3.3V.

This optoelectronic transceiver module is a class 1 laser product compliant with FDA Radiation Performance Standards, 21 CFR Subchapter J. This component is also class 1 laser compliant according to International Safety Standard IEC-825-1.

LONG WAVELENGTH LASER

The SLC210-7-X-XXX is provided with single mode optics. The single mode laser provides highly reliable single mode communications which meets or exceeds SONET/SDH OC-48 distance requirements.

ABSOLUTE MAXIMUM RATINGS

| PARAMETER | SYMBOL | MIN | MAX | UNITS | NOTES |
|-----------------------|--------|-----|------|-------|--------------------------|
| Storage Temperature | Tstg | -40 | 85 | °C | |
| Soldering Temperature | | | 260 | °C | 10 seconds on leads only |
| Supply Voltage | Vcc | | 6.0 | V | Vcc - ground |
| Supply Current | lcc | | 330 | ma | |
| Power Dissapation | | | 1.15 | W | |

RECOMMENDED OPERATING CONDITIONS

| REGUINILITIES OF ENATING GONDITIONS | | | | | | | | |
|-------------------------------------|--------|-------|-----|-------|-------|-------|--|--|
| PARAMETER | SYMBOL | MIN | TYP | MAX | UNITS | NOTES | | |
| Ambient Operating Temperature | Та | 0 | | 70 | °C | | | |
| Supply Voltage | Vcc | 3.135 | 3.3 | 3.465 | VDC | | | |



PERFORMANCE SPECIFICATIONS -- ELECTRICAL

 $Ta = 25^{\circ} C$, Vcc = 3.3V

| SYMBOL | MIN | TYP | MAX | UNITS | NOTES | | | |
|--------|------------------------------------|--|--------------------------|---|--|--|--|--|
| Icc | | | 200 | mA | 0°C <ta<70°c, 3.135v<="" <3.465="" td="" v<="" vcc=""></ta<70°c,> | | | |
| | | • | • | • | | | | |
| | 0.7 | | 2 | Vpp | DC coupled inputs, External Bias and Termination | | | |
| Zin | | 100 | | Ohms | Externally Terminated | | | |
| lbias | | 10 | | Ohms | 3K Standoff: See Fig. 1A & 1B | | | |
| lpwr | | 200 | | Ohms | 3K Standoff; See Fig. 1A & 1B | | | |
| VIH | 2.2 | | | V | | | | |
| VIL | | | 0.6 | V | | | | |
| • | | | | | | | | |
| | 500 | | 1570 | mVpp | DC coupled outputs, External Bias and Termination | | | |
| | 200 | | 1200 | mVpp | DC coupled outputs, External Bias and Termination | | | |
| t(SDa) | | | 100 | μs | Measured on transition - Low to High | | | |
| t(SDd) | | | 350 | μs | Measured on transition - High to Low | | | |
| | SYMBOL Icc Zin Ibias Ipwr VIH VIL | SYMBOL MIN Icc 0.7 Zin Ibias Ipwr VIH 2.2 VIL 500 200 t(SDa) | SYMBOL MIN TYP Icc | SYMBOL MIN TYP MAX lcc 200 2 2 Zin 100 lbias 10 lpwr 200 VIH 2.2 VIL 0.6 500 1570 200 1200 t(SDa) 100 | SYMBOL MIN TYP MAX UNITS Icc 200 mA 0.7 2 Vpp Zin 100 Ohms Ibias 10 Ohms Ipwr 200 Ohms VIH 2.2 V VIL 0.6 V 500 1570 mVpp 200 1200 mVpp t(SDa) 100 µs | | | |

SLC210-7-2-XXX PERFORMANCE SPECIFICATIONS -- OPTICAL Single Mode Laser

Ta = 25° C, Vcc = 3.3V

| PARAMETER | SYMBOL | MIN | TYP | MAX | UNITS | NOTES | | |
|------------------------|--------|------|------|------|-------|---------------------------------|--|--|
| TRANSMITTER | | | | | | | | |
| Optical Center | λ | 1266 | 1310 | 1360 | nm | | | |
| RMS Spectral Width | Δλ | | | 4 | nm | RMS | | |
| Optical Transmit Power | Popt | -10 | | -3 | dBm | average @ 1310 nm | | |
| Extinction Ratio | Er | 8.2 | | | dB | P1/P0 | | |
| RECEIVER | | | | | | | | |
| Optical Center | λ | 1266 | 1310 | 1360 | nm | | | |
| Optical Input Power | Pr | -18 | | -3 | dBm | average power for BER < 1.0E-10 | | |



Optoelectronic Products

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TERMINATION CIRCUITS

Inputs to the SLC210 transmitter are internally AC coupled and terminated with 100 ohms differential. These transceivers can operate with PECL logic levels. The input signal must have at least a 0.35 V peak-to-peak (single ended) signal swing. Output from the receiver section of the module is also AC coupled and is expected to drive into a 100 ohm differential load. Different termination strategies may be required depending on the particular MUX/DEMUX chip set used.

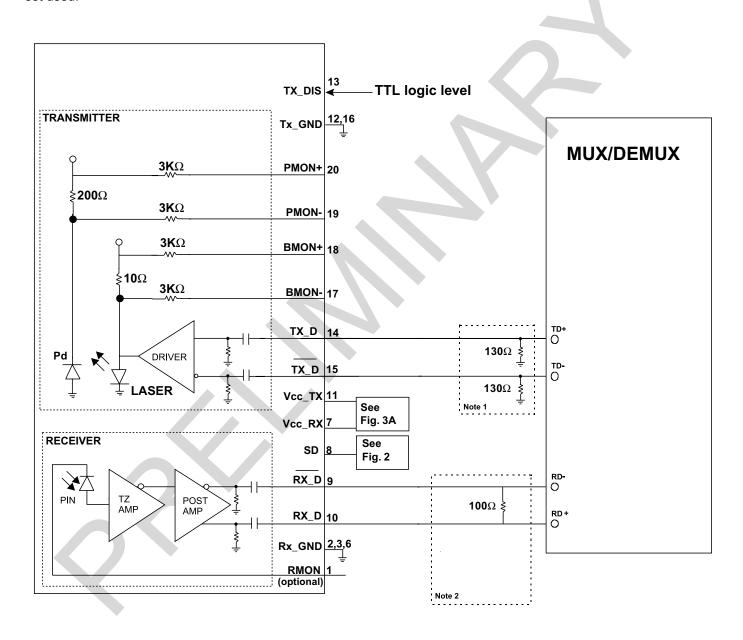


Figure 1A: Recommended TRANSMIT and RECEIVE (PECL) Data Terminations

Notes

- 1. Consult the MUX/DEMUX manufacturer's applications information for biasing required for TD+, TD- outputs. Some multiplexer outputs are internally biased and may not need external bias resistors.
- 2. Consult MUX/DEMUX manufacturer's data sheet and application data for appropriate receiver input biasing network.



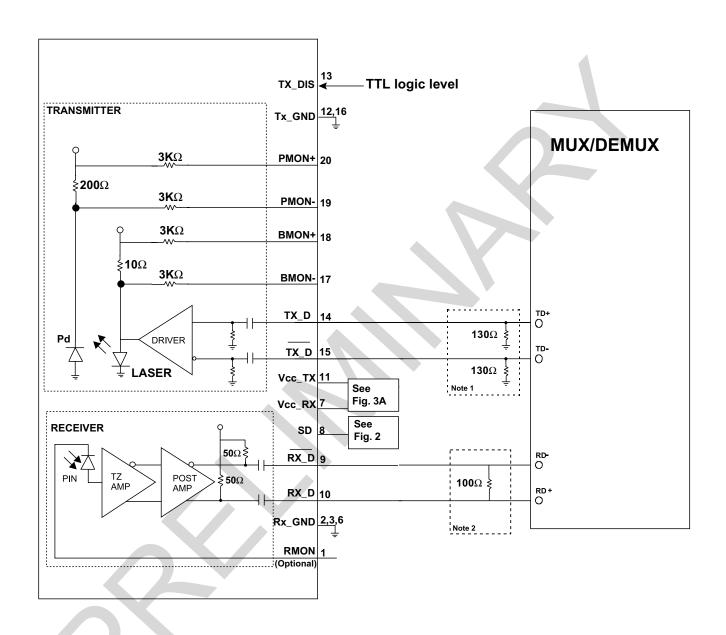


Figure 1B: Recommended TRANSMIT and RECEIVE (CML) Data Terminations

Notes

- 1. Consult the MUX/DEMUX manufacturer's applications information for biasing required for TD+, TD- outputs. Some multiplexer outputs are internally biased and may not need external bias resistors.
- 2. Consult MUX/DEMUX manufacturer's data sheet and application data for appropriate receiver input biasing network.



SIGNAL DETECT

The SLC210 transceivers are equipped with TTL signal detect outputs. The TTL option eliminates the need for a PECL to TTL level shifter in most applications. The SFF adhoc industry standard provides for a TTL level SIgnal Detect output.

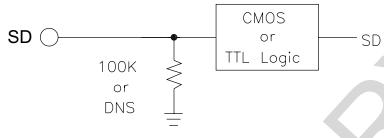


Figure 2. Suggested Signal Detect Coupling

POWER COUPLING

A suggested layout for power and ground connections is given in figure 4B below. Connections are made via separate voltage and ground planes. The mounting posts are at case ground and should not be connected to circuit ground. The ferrite bead should provide a real impedance of 50 to 100 ohms at 100 to 1000 MHz. Bypass capacitors should be placed as close to the 10-pin connector as possible.

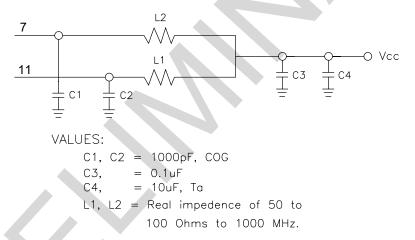


Figure 3A. Suggested Power Coupling - Electrical Schematic

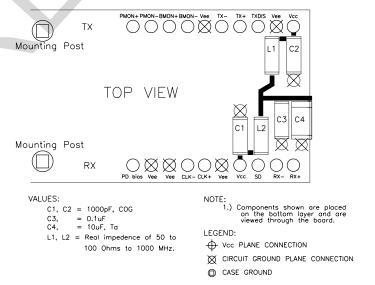


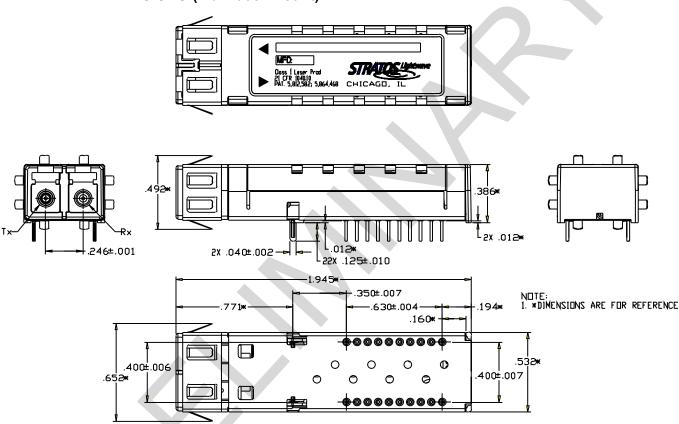
Figure 3B. Suggested Power Coupling - Component Placement



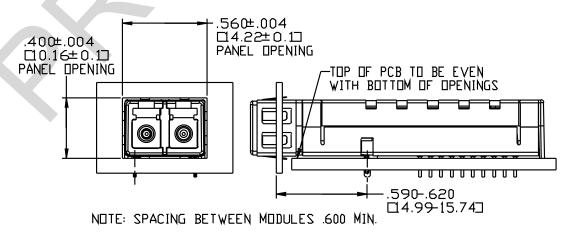
EMI and ESD CONSIDERATIONS

Stratos Lightwave optoelectronic transceivers offer a metal case and a special chassis grounding clip. As shown in the drawing, this clip connects the module case to chassis ground when installed flush through the panel cutout. The grounding clip in this way brushes the edge of the cutout in order to make a proper contact. The use of a grounding clip also provides increased electrostatic protection and helps reduce radiated emissions from the module or the host circuit board through the chassis faceplate. The attaching posts are at case potential and may be connected to chassis ground. They should not be connected to circuit ground.

MECHANICAL DIMENSIONS (Individual Mount) -

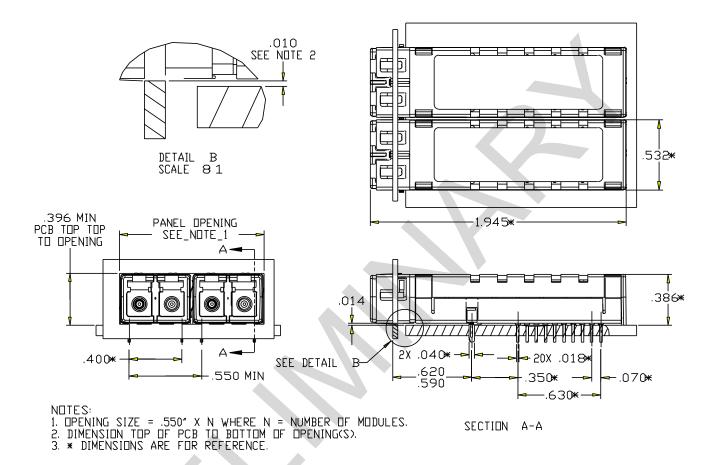


PANEL CUTOUT DIMENSIONS

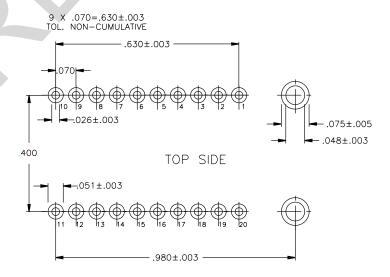




MECHANICAL DIMENSIONS (Gang Mount) -



SUGGESTED PCB LAND PATTERN





PHYSICAL DESCRIPTION

The SLC210 features a compact design with a standard LC duplex connector for fiber optic connections. The 10-pin connector (70 mil spacing) provides the electrical connection for all operation. With a height of 9.8 mm the SLC210 fits mezzanine card applications. An epoxy encapsulation provides excellent protection from environmental hazards and assists in heat dissipation for all components. Two wave-solderable posts are provided for attaching the package to the circuit board without the need for multiple attachment operations.

ELECTRICAL INTERFACE, PIN DESCRIPTIONS

| PIN | SYMBOL | NOTES | PIN | SYMBOL | NOTES | | |
|-----|--|--|-----|--------|-----------------------------|--|--|
| 1 | RMON | Receiver power monitor (optional) ¹ | 11 | Vcc | Transmitter 3.3V | | |
| 2 | Vee | Receiver Ground | 12 | Vee | Transmitter Ground | | |
| 3 | Vee | Receiver Ground | 13 | TDis | Transmitter Disable | | |
| 4 | NIC | No Internal Connection | 14 | TD+ | Transmitter Data+ | | |
| 5 | NIC | No Internal Connection | 15 | TD- | Transmitter Data- | | |
| 6 | Vee | Receiver Ground | 16 | Vee | Transmitter Ground | | |
| 7 | Vcc | Receiver 3.3V | 17 | BMON- | Transmitter Bias Monitor- | | |
| 8 | SD | Signal Detect | 18 | BMON+ | Transmitter Bias Monitor+ | | |
| 9 | RX- | Receiver data- | 19 | PMON - | Transmitter Power Monitor - | | |
| 10 | RX+ | Receiver data+ 20 | | PMON + | Transmitter Power Monitor + | | |
| | ATTACHING The attaching posts are at case potential and may be connected to chassis ground. They are isolated from circuit ground. | | | | | | |

^{*} If this option is used, connect to Rx monitor circuit or Vcc. Otherwise, leave pin 1 open.



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