TOSHIBA BI-DIRECTIONAL TRIODE THYRISTOR SILICON PLANAR TYPE

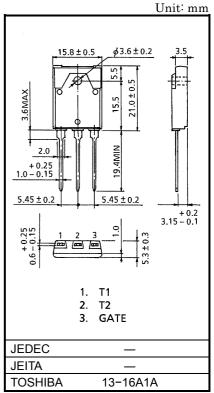
# SM16GZ51,SM16JZ51

### AC POWER CONTROL APPLICATIONS

 $\begin{array}{lll} \bullet & Repetitive\ Peak\ off-State\ Voltage & :\ V_{DRM} = 400,\ 600\ V \\ \bullet & R.M.S\ On-State\ Current & :\ I_{T\ (RMS)} = 16\ A \\ \bullet & High\ Commutating\ (dv\ /\ dt) & :\ (dv\ /\ dt)\ c = 10\ V\ /\ \mu s \\ \bullet & Isolation\ Voltage & :\ V_{ISOL} = 1500\ V\ AC \\ \end{array}$ 

### **MAXIMUM RATINGS**

CHARACTER	ISTIC	SYMBOL	RATING	UNIT	
Repetitive Peak Off-State Voltage	SM16GZ51	$V_{DRM}$	400	V	
	SM16JZ51	▼DRM	600	V	
R. M. S. On-tate Curre (Full Sine Waveform Ta		I <sub>T (RMS)</sub>	16	А	
Peak One Cylce Surge On-State Current (Non-Repetitive)		I <sub>TSM</sub>	150 (50 Hz)	Α	
			165 (60 Hz)		
I <sup>2</sup> t Limit Value		1 <sup>2</sup> t	112.5	A <sup>2</sup> s	
Critical Rate of Rise of Current	On-State (Note 1)	di / dt	50	A / μs	
Peak Gate Power Dissi	pation	P <sub>GM</sub>	5	W	
Average Gate Power D	issipation	P <sub>G (AV)</sub>	0.5	W	
Peak Gate Voltage		$V_{GM}$	10	V	
Peak Gate Current		I <sub>GM</sub>	2	Α	
Junction Temperature		Tj	-40~125	°C	
Storage Temperature R	lange	T <sub>stg</sub>	-40~125	°C	
Isolation Voltage (AC, t	= 1 min.)	V <sub>ISOL</sub>	1500	V	



Weight: 2.0g

Note 1: di / dt test condition

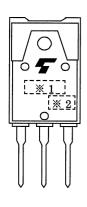
 $V_{DRM}$  = 0.5 × Rated,  $I_{TM} \le 25$  A,  $t_{gw} \ge 10$   $\mu$ s,  $t_{gr} \le 250$  ns,  $i_{gp}$  =  $I_{GT}$  × 2.0



## **ELECTRICAL CHARACTERISTICS (Ta = 25°C)**

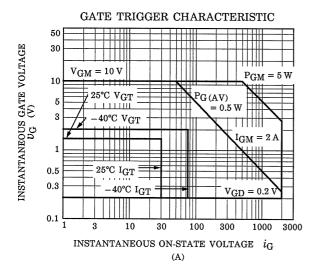
CHARACTERISTIC		SYMBOL	TEST CONDITION		MIN	TYP.	MAX	UNIT
Repetitive Peak Off-State Current		I <sub>DRM</sub>	V <sub>DRM</sub> = Rated		-	_	20	μA
Gate Trigger Voltage	I	- V <sub>GT</sub>	V <sub>D</sub> = 12 V, R <sub>L</sub> = 20 Ω	T2 (+) , Gate (+)	1	_	1.5	V
	Ш			T2 (+) , Gate (-)	_	_	1.5	
	Ш			T2 (-) , Gate (-)	_	_	1.5	
	IV			T2 (-) , Gate (+)	_	_	_	
Gate Trigger Current	I	l <sub>GT</sub>	V <sub>D</sub> = 12 V, R <sub>L</sub> = 20 Ω	T2 (+), Gate (+)	_	_	30	- mA
	Ш			T2 (+) , Gate (-)	_	_	30	
	III			T2 (-) , Gate (-)	_	_	30	
	IV			T2 (-) , Gate (+)	_	_	_	
Peak On-State Voltage		V <sub>TM</sub>	I <sub>TM</sub> = 25 A		_	_	1.5	V
Gate Non-Trigger Voltage		$V_{GD}$	V <sub>D</sub> = Rated, Tc = 125°C		0.2	_	_	V
Holding Current		lΗ	V <sub>D</sub> = 12 V, I <sub>TM</sub> = 1 A		_	_	50	mA
Thermal Resistance		R <sub>th (j-c)</sub>	Junction to Case, AC		_	_	1.8	°C/W
Critical Rate of Rise of Off-State Voltage		dv / dt	V <sub>DRM</sub> = Rated, T <sub>j</sub> = 125°C Exponential Rise		_	300	_	V / µs
Critical Rate of Rise of Off-State Voltage at Commutation (dv		(dv / dt) c	$V_{DRM} = 400 \text{ V}, T_j = 125^{\circ}\text{C}$ (di / dt) c = -8.7 Å / ms		10	_	_	V / µs

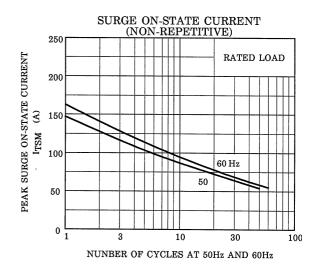
### **MARKING**

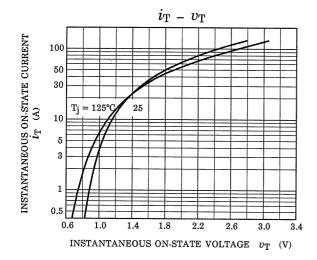


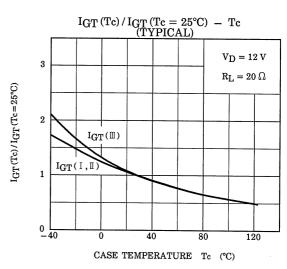
*NUMBER	SYMBOL		MARK	
*1	TYPE	M16GZ51	SM16GZ51	
		M16JZ51	SM16JZ51	
*2	Lot Number  Month (Starting from Alphabet A)  Year (Last Decimal Digit of the Current Year )		Example 8A : January 1998 8B : February 1998 8L : December 1998	

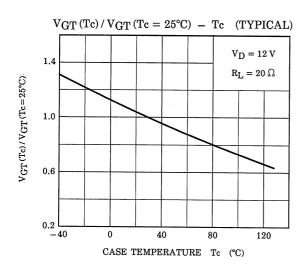
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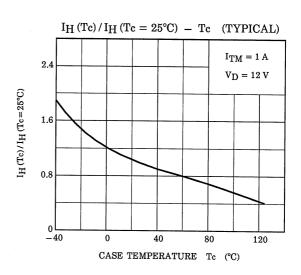




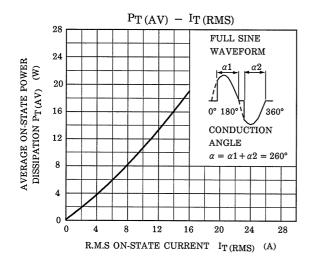


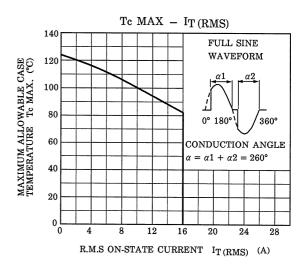


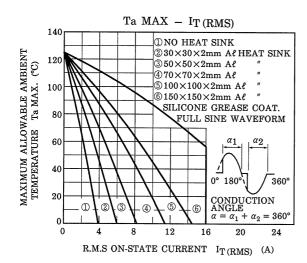


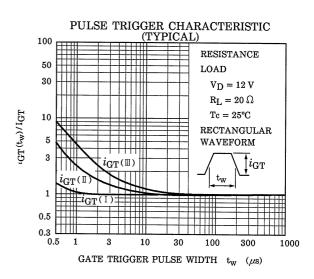


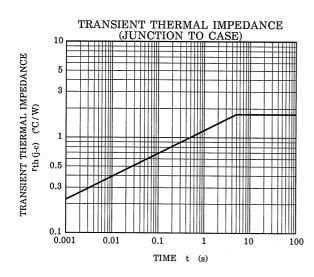
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