

8M X 36 BITS DRAM SIMM Memory Module

FEATURES

- Performance range:

	t_{RAC}	t_{CAC}	t_{RC}
SL...-A60	60ns	15ns	110ns
SL...-A70	70ns	20ns	130ns

- Fast Page Mode operation
- $\overline{\text{CAS}}$ -before- $\overline{\text{RAS}}$ refresh capability
- $\overline{\text{RAS}}$ -only and Hidden refresh capability
- TTL compatible inputs and outputs
- +5V $\pm$ 10% power supply
- 2048 cycles/32ms refresh
- JEDEC standard pinout
- Tin or gold edge-connections

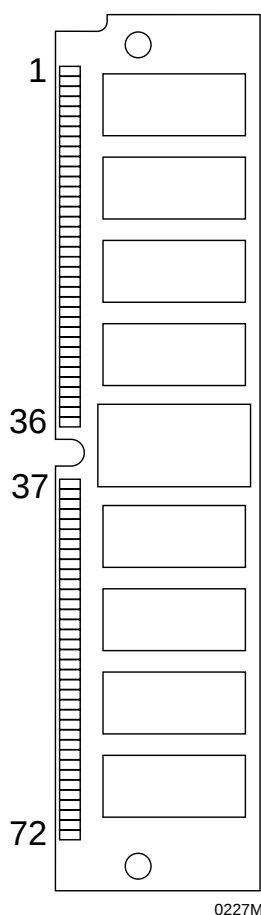
GENERAL DESCRIPTION

The SiliconTech SL36(S/T)4B8M2C-Axx is a 8MB x 36 bits Dynamic RAM high density memory module. This module consists of sixteen CMOS 4M x 4 bits DRAMs in 24-pin SOJ package and two CMOS 4M x 4 bits Quad-CAS DRAM in a 28-pin SOJ package mounted on a 72-pin glass epoxy substrate. A 0.1mF decoupling capacitor is mounted for each DRAM.

The module is a Single In-line Memory Module with tin (SL36T4B8M2C-Axx) or gold (SL36S4B8M2C-Axx) edge-connections and is intended for mounting into 72-pin SIMM edge connector sockets.

PIN CONFIGURATION

Pin	Symbol	Pin	Symbol	Pin	Symbol
1	V_{SS}	25	DQ_{24}	49	DQ_9
2	DQ_0	26	DQ_7	50	DQ_{27}
3	DQ_{18}	27	DQ_{25}	51	DQ_{10}
4	DQ_1	28	A_7	52	DQ_{28}
5	DQ_{19}	29	NC	53	DQ_{11}
6	DQ_2	30	V_{CC}	54	DQ_{29}
7	DQ_{20}	31	A_8	55	DQ_{12}
8	DQ_3	32	A_9	56	DQ_{30}
9	DQ_{21}	33	$\overline{\text{RAS}}_1$	57	DQ_{13}
10	V_{CC}	34	$\overline{\text{RAS}}_0$	58	DQ_{31}
11	NC	35	PQ_{26}	59	V_{CC}
12	A_0	36	PQ_8	60	DQ_{32}
13	A_1	37	PQ_{17}	61	DQ_{14}
14	A_2	38	PQ_{35}	62	DQ_{33}
15	A_3	39	V_{SS}	63	DQ_{15}
16	A_4	40	$\overline{\text{CAS}}_0$	64	DQ_{34}
17	A_5	41	$\overline{\text{CAS}}_2$	65	DQ_{16}
18	A_6	42	$\overline{\text{CAS}}_3$	66	NC
19	A_{10}	43	$\overline{\text{CAS}}_1$	67	PD_1
20	DQ_4	44	$\overline{\text{RAS}}_0$	68	PD_2
21	DQ_{22}	45	$\overline{\text{RAS}}_1$	69	PD_3
22	DQ_5	46	NC	70	PD_4
23	DQ_{23}	47	$\overline{W}$	71	NC
24	DQ_6	48	NC	72	V_{SS}



Pin Names

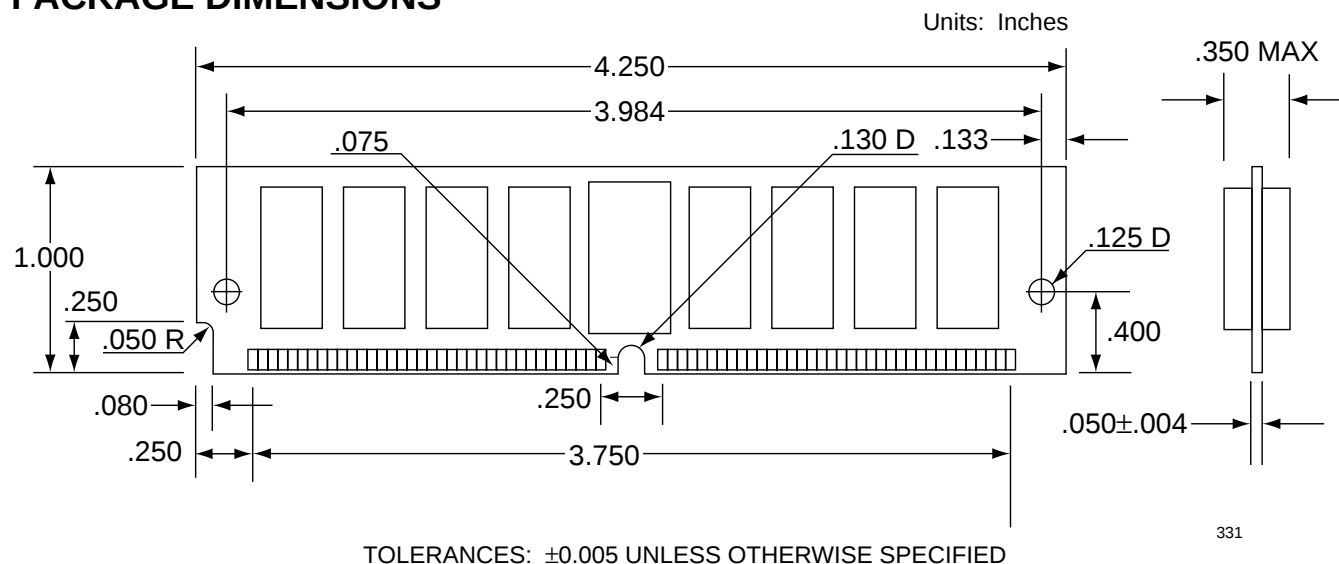
Pin Name	Pin Function
A_0 - A_{10}	Address Inputs
DQ_x	Data In/Out
$\text{PQ}_{8, 17, 26, 35}$	
$\overline{W}$	Read/Write Input
$\overline{\text{RAS}}_0, \overline{\text{RAS}}_1$	Row Address Strobe
$\overline{\text{CAS}}_0$ - $\overline{\text{CAS}}_3$	Column Address Strobe
PD_1 - PD_4	Presence Detect
V_{CC}	Power (+5V)
V_{SS}	Ground
NC	No Connection

Presence Detect Pins* (Optional)

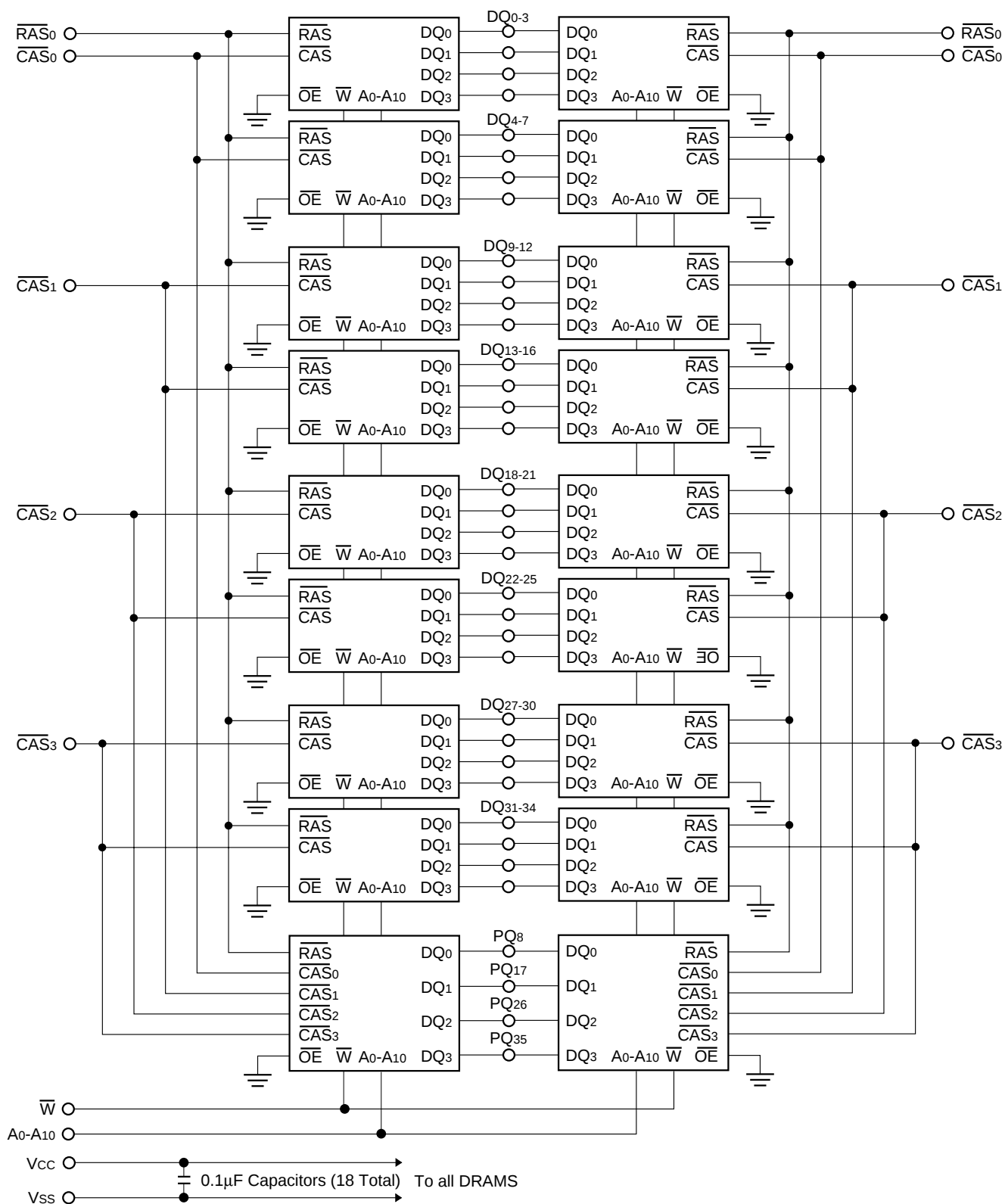
Pin	60ns	70ns
PD_1	NC	NC
PD_2	V_{SS}	V_{SS}
PD_3	NC	V_{SS}
PD_4	NC	NC

* Pin Connection Changing Available

PACKAGE DIMENSIONS



FUNCTIONAL BLOCK DIAGRAM



0240M