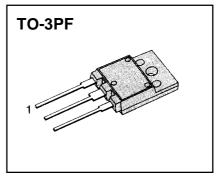
SGF40N60UFD

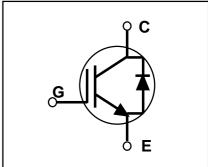
FEATURES

- * High Speed Switching
- * Low Saturation Voltage
 - $: V_{CF}(sat) = 2.0 \text{ V } (@ \text{ Ic}=20\text{A})$
- * High Input Impedance
- *CO-PAK, IGBT with FRD
 - : Trr = 42nS (typ.)

APPLICATIONS

- * AC & DC Motor controls
- * General Purpose Inverters
- * Robotics, Servo Controls
- * Power Supply
- * Lamp Ballast





ABSOLUTE MAXIMUM RATINGS

Symbol	Characteristics	Rating	Units
V _{CES}	Collector-Emitter Voltage	600	V
V_{GES}	Gate-Emitter Voltage	±20	V
I _C	Collector Current @ Tc = 25°C	40	А
	Collector Current @ Tc = 100°C	20	Α
I _{CM (1)}	Pulsed Collector Current	160	Α
I _F	Diode Continuous Forward Current @ Tc = 100°C	15	Α
I _{FM}	Diode Maximum Forward Current	160	Α
P_{D}	Maximum Power Dissipation @Tc = 25°C 96		W
	Maximum Power Dissipation @Tc = 100°C	38	W
Tj	Operating Junction Temperature	-55 ~ 150	°C
Tstg	Storage Temperature Range -55 ~ 150		°C
TL	Maximum Lead Temp. For Soldering 300		°C
	Purposes, 1/8" from case for 5 seconds		

Notes:(1) Repetitive rating : Pulse width limited by max. junction temperature



ELECTRICAL CHARACTERISTICS (IGBT PART)

(Tc=25°C,Unless Otherwise Specified)

Symbol	Characteristics	Test Conditions	Min	Тур	Max	Units
BV _{CES}	C - E Breakdown Voltage	V _{GE} = 0V , I _C = 250uA	600	-	-	V
$\Delta V_{\text{CES}/}$	Temperature Coeff. of	$V_{GE} = 0V$, $I_C = 1mA$	-	0.6	-	V/°C
ΔT_J	Breakdown Voltage					
$V_{GE(th)}$	G - E threshold voltage	$I_C = 20$ mA , $V_{CE} = V_{GE}$	4.5	5.5	7.5	V
I _{CES}	Collector cutoff Current	$V_{CE} = V_{CES}$, $V_{GE} = 0V$	-	-	250	uA
I _{GES}	G - E leakage Current	$V_{GE} = V_{GES}$, $V_{CE} = 0V$	-	-	100	nA
V _{CE} (sat)	Collector to Emitter	Ic=20A, V _{GE} = 15V	-	2.0	2.6	V
	saturation voltage	Ic=40A, V _{GE} = 15V	-	2.6	-	٧
Cies	Input capacitance	V _{GE} = 0V , f = 1MHz	-	1430	-	pF
Coes	Output capacitance	V _{CE} = 30V	-	168	-	pF
Cres	Reverse transfer capacitance		-	50	-	pF
td(on)	Turn on delay time	V _{CC} = 300V , I _C = 20A	-	12	-	ns
tr	Turn on rise time	V _{GE} = 15V	-	20	-	ns
td(off)	Turn off delay time	$R_G = 10\Omega$	-	68	100	ns
tf	Turn off fall time	Inductive Load	-	50	100	ns
Eon	Turn on Switching Loss		-	0.08	-	mJ
Eoff	Turn off Switching Loss		-	0.19	-	mJ
Ets	Total Switching Loss		-	0.27	0.47	mJ
Qg	Total Gate Charge	Vcc = 300V	-	92	138	nC
Qge	Gate-Emitter Charge	V _{GE} = 15V	-	21	31	nC
Qgc	Gate-Collector Charge	Ic = 20A	-	28	42	nC
Le	Internal Emitter Inductance	Measured 5mm from PKG	-	14	-	nH



ELECTRICAL CHARACTERISTICS (DIODE PART)

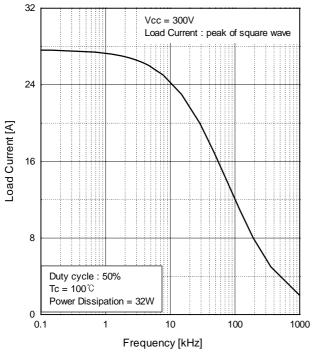
(Tc=25°C,Unless Otherwise Specified)

Symbol	Characteristics	Test Conditions		Min	Тур	Max	Units
VFM	Diode Forward Voltage	IF=15A	Tc =25°C	-	1.4	1.7	٧
			Tc =100°C	1	1.3	-	
Trr	Diode Reverse		Tc =25°C	-	42	60	nS
	Recovery Time		Tc =10 °C	-	74	-	
Irr	Diode Peak Reverse	IF=15A, VR=200V	Tc =25°C	-	4.0	6.0	Α
	Recovery Current	-di/dt=200A/uS	Tc =10 °C	-	6.5	-	
Qrr	Diode Reverse		Tc =25°C	ı	80	180	nC
	Recovery Charge		Tc =100°C	-	220	-	

THERMAL RESISTANCE

Symbol	Characteristics	Min	Тур	Max	Units
R_{θ} JC	Junction-to-Case (IGBT)	-	-	1.3	°C/W
R _e JC	Junction-to-Case (DIODE)	-	-	2.5	°C/W
$R_{\theta}JA$	Junction-to-Ambient	-	-	45	°C/W





200

150

Tc = 25℃

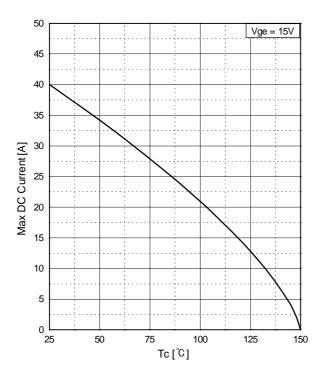
Tc = 100℃

Tc = 100℃

Vce [V]

Fig.1 Typical Load Current vs. Frequency

Fig.2 Typical Output Characteristics





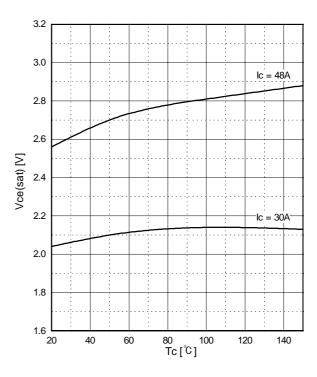


Fig.4 Collector to Emitter Voltage vs. Case Temperature



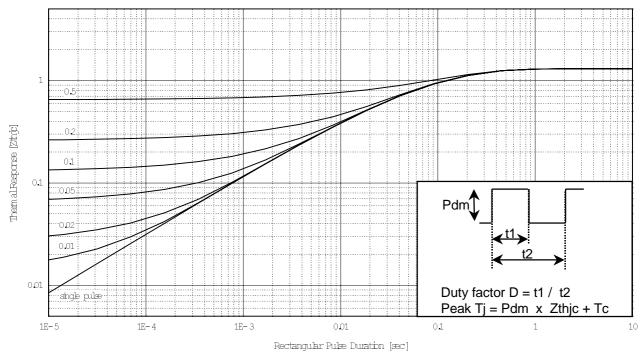


Fig.5 Maximum Effective Transient Thermal Impedance, Junction to Case

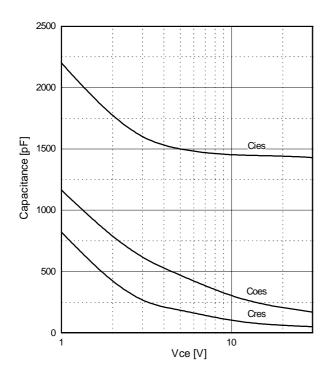


Fig.6 Typical Capacitance vs.
Collector to Emitter Voltage

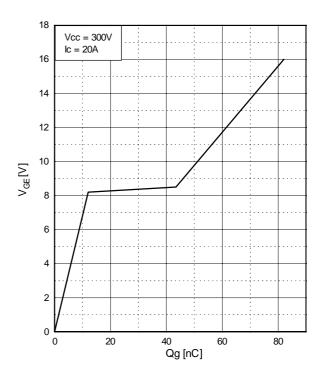


Fig.7 Typical Gate Charge vs. Gate to Emitter Voltage



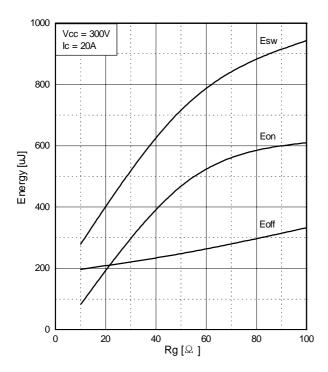


Fig.8 Typical Switching Loss vs. Gate Resistance

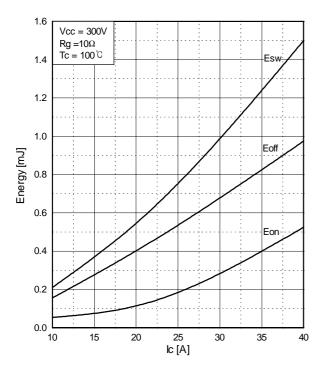


Fig.10 Typical Switching loss vs.
Collector to Emitter Current

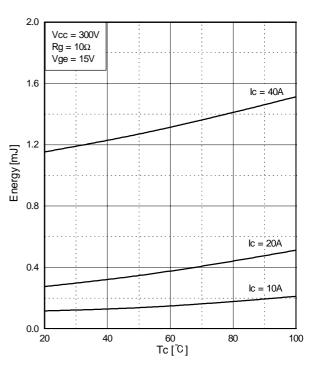


Fig.9 Typical Switching Loss vs. Case Temperature

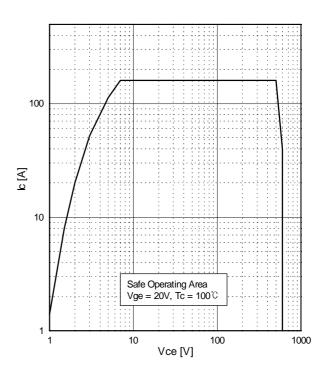


Fig.11 Turn-off SOA



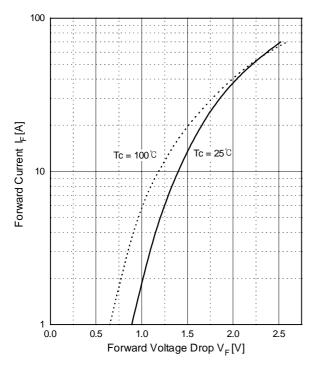


Fig.12 Typical Forward Voltage Drop vs. Forward Current

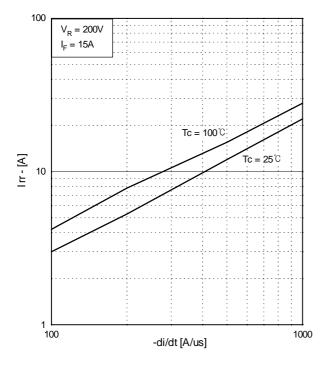


Fig.14 Typical Reverse Recovery Current vs. di/dt

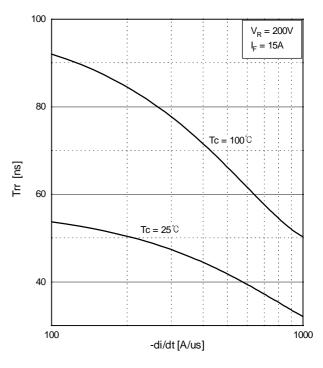


Fig.13 Typical Reverse Recovery Time vs. di/dt

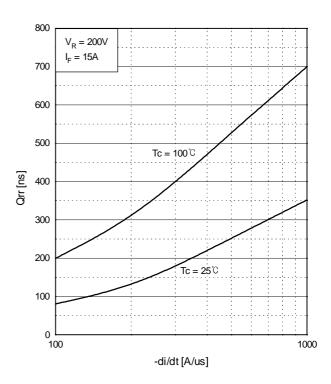


Fig.15 Typical Stored Charge vs. di/dt



TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEXTMISOPI ANAR™ $\mathsf{CoolFET}^\mathsf{TM}$ MICROWIRETM CROSSVOLT™ **POPTM** E2CMOS™ PowerTrench™ QSTM FACT™ $QuietSeries^{TM}\\$ FACT Quiet Series™ FAST® SuperSOTTM-3 FASTr™ SuperSOTTM-6 GTO^TM SuperSOTTM-8 HiSeC™ TinyLogicTM

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVER ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or © whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
- 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

LIFE SUPPORT POLICY

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later data. Fairchild Semiconductor reserves the right to make changes at any time without notices in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.