

### FEATURES

- Small 8-pin DIP package
- 200ns max. acquisition time to  $\pm 0.01\%$
- 100ns max. sample-to-hold settling time to  $\pm 0.01\%$
- 16MHz small signal bandwidth
- 74dB feedthrough attenuation
- $\pm 25$  picoseconds aperture uncertainty
- 415mW maximum power dissipation



### GENERAL DESCRIPTION

DATEL's SHM-49 is a high-speed, highly accurate sample/hold designed for precision, high-speed analog signal processing applications. The SHM-49 features excellent dynamic specifications including a maximum acquisition time of only 200 nanoseconds for a 10V step to  $\pm 0.01\%$ .

Sample-to-hold settling time, to  $\pm 0.01\%$  accuracy, is 100 nanoseconds maximum with an aperture uncertainty of  $\pm 25$  picoseconds.

The SHM-49 is a complete sample/hold circuit, containing a precision MOS hold capacitor and a MOSFET switching configuration which results in faster switching and better feedthrough attenuation. Additionally, a FET input amplifier design allows faster acquisition and settling times while maintaining a considerably lower droop rate.

### INPUT/OUTPUT CONNECTIONS

PIN	FUNCTION
1	+5V SUPPLY
2	S/H CONTROL
3	ANALOG INPUT
4	ANALOG RETURN
5	-15V SUPPLY
6	ANALOG OUTPUT
7	+15V SUPPLY
8	POWER GROUND

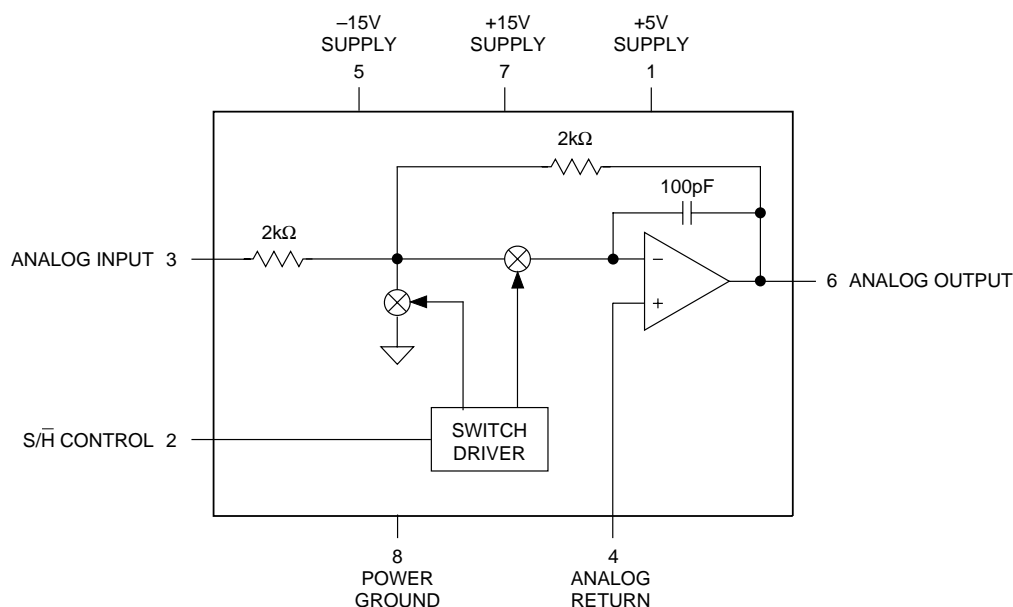


Figure 1. Functional Block Diagram

## ABSOLUTE MAXIMUM RATINGS

±15V Supply Voltage	±18V
+5V Supply Voltage	−0.5V to +7V
Analog Input	±18V
Digital Input	−0.5V to +5.5V
Output Current	±65 mA

## FUNCTIONAL SPECIFICATIONS

(Apply over the operating temperature range with ±15V and +5V supplies unless otherwise specified.)

ANALOG INPUT/OUTPUT	MIN.	TYP.	MAX.	UNITS
<b>Input/Output Voltage Range</b>				
±15V Nominal Supplies	±10	±11.5	—	Volts
±12V Nominal Supplies	±7	±8.5	—	Volts
<b>Input Impedance</b>	1.75	2	—	kΩ
<b>Output Current</b>	—	—	±40	mA
<b>Output Impedance</b>	—	0.1	—	Ω
<b>Capacitive Load</b>	100	250	—	pF
<b>DIGITAL INPUT</b>				
<b>Input Logic Levels</b>				
Logic 1	+2.0	—	+5.0	Volts
Logic 0	0	—	+0.8	Volts
<b>Loading</b>				
Logic 1	—	—	+5	μA
Logic 0	—	—	−5	μA
<b>TRANSFER CHARACTERISTICS</b>				
<b>Gain</b>	—	−1	—	V/V
<b>Gain Error, +25°C</b>	—	±0.05	±0.5	%
<b>Linearity Error</b>	—	±0.005	±0.01	%FS
<b>Sample Mode Offset, +25°C</b>	—	±2	±7	mV
<b>Sample-to-Hold Offset (Pedestal), +25°C</b>	—	±2.5	±25	mV
<b>Gain Drift</b>	—	±0.5	±15	ppm/°C
<b>Sample Mode Offset Drift</b>	—	±3	±15	ppm of FSR/°C
<b>Sample-to-Hold Off. (Pedestal) Drift</b>	—	±5	±20	ppm of FSR/°C
<b>DYNAMIC CHARACTERISTICS</b>				
<b>Acquisition Time</b>				
10V to ±0.01%FS (±1 mV)				
+25 °C	—	160	200	ns
−55 to +125 °C	—	—	265	ns
10V to ±0.1%FS (±10 mV)				
+25 °C	—	100	150	ns
−55 to +125 °C	—	—	215	ns
10V to ±1%FS (±100 mV)	—	90	—	ns
1V to ±1%FS (±10 mV)	—	75	—	ns
<b>Sample-to-Hold Settling Time</b>				
10V to ±0.01%FS (±1 mV)	—	60	100	ns
10V to ±0.1%FS (±10 mV)	—	40	80	ns
<b>Sample-to-Hold Transient</b>	—	100	—	mVp-p
<b>Aperture Delay Time</b>	—	10	15	ns
<b>Aperture Uncertainty (Jitter)</b>	—	±25	±50	ps
<b>Output Slew Rate</b>	±200	±300	—	V/μs
<b>Small Signal BW (−3dB)</b>	10	16	—	MHz
<b>Output Droop</b>				
+25 °C	—	±0.5	±15	μV/μs
0 to +70 °C	—	±15	±30	μV/μs
−55 to +125 °C	—	±1.2	±2.4	mV/μs
<b>Feedthrough Rejection</b>	69	74	—	dB

POWER REQUIREMENTS	MIN.	TYP.	MAX.	UNITS
<b>Voltage Range</b>				
+15V Supply	+11.5	+15.0	+15.5	Volts
−15V Supply	−11.5	−15.0	−15.5	Volts
+5V Supply	+4.75	+5.0	+5.25	Volts
<b>Power Supply Rejection Ratio</b>	—	±0.5	±1	mV/V
<b>Quiescent Current Drain</b>				
+15V Supply	—	+12	+13.5	mA
−15V Supply	—	−12	−13.5	mA
+5V Supply	—	+1	+1.5	mA
<b>Power Consumption</b>	—	365	415	mW
<b>PHYSICAL/ENVIRONMENTAL</b>				
<b>Operating Temp. Range, Case</b>				
SHM-49MC			0 to +70 °C	
SHM-49MM			−55 to +125 °C	
<b>Storage Temperature Range</b>			−65 to +150 °C	
<b>Thermal Impedance</b>				
θjc			15°C/W	
θca			35°C/W	
<b>Package Type</b>			8-pin ceramic DIP	

### Footnotes:

Full Scale (FS) = 10V. Full Scale Range (FSR) = 20V.

Sample-to-hold offset error (pedestal) is constant regardless of input/output level.

## ORDERING INFORMATION

MODEL	OPERATING TEMP. RANGE
<b>SHM-49MC</b>	0 to +70°C
<b>SHM-49MM</b>	−55 to +125°C
For availability of high-reliability versions of the SHM-49, contact DATEL.	

## TECHNICAL NOTES

1. All ground pins should be tied together and connected to system analog ground as close to the package as possible. It is recommended to use a ground plane under the device and solder ground pins directly to it. Take care to ensure that no ground potentials can exist between ground pins.
2. External 0.1 F to 1 F tantalum bypass capacitors are required in critical applications.
3. A logic 1 on S/H puts the unit in the sample mode. A logic 0 puts the unit in hold mode.
4. The maximum capacitive load to avoid oscillation is typically 250pF. Recommended resistive load is 500Ω, although values as low as 250Ω may be used. Acquisition and sample-to-hold settling times are relatively unaffected by resistive loads down to 250Ω and capacitive loads up to 50pF. Greater load capacitances will affect both acquisition and settling time.
5. Gain and offset adjusting can be accomplished using the external circuitry shown in Figure 2. Adjust offset with a 0V input. Adjust gain with a ±FS input. Adjust so that the output in the hold mode matches the input.

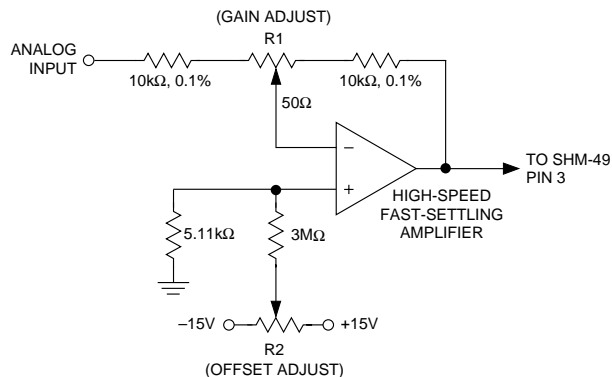


Figure 2. Offset and Gain Adjustments

### MECHANICAL DIMENSIONS INCHES (mm)

