

CMOS Compatible SJ-A1420 Series

Description

The SJ-A1420 Series of quartz crystal oscillators provide enable/disable 3-state CMOS compatible signals for bus connected systems. Supplying Pin 1 of the -1420 units with a logic "1" or open enables its Pin 3 output. In the disable mode, Pin 3 presents a high impedance to the load. All units are designed to survive standard wave soldering operations without damage.

Pin Connection

JEDEC XTAL Industry

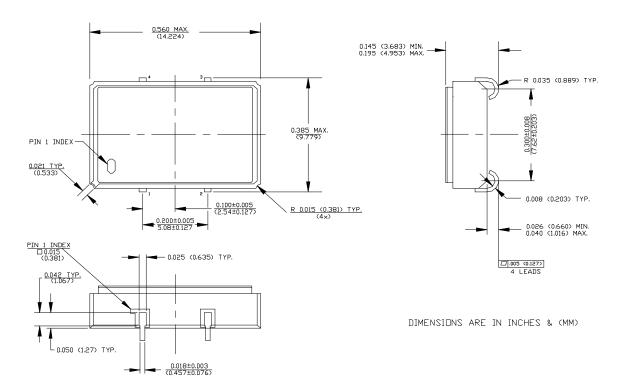
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6	1	Enable/				
		Disable Input				
10	2	Ground				
20	3	Output				
24	4	V _{DD}				
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Suggested Applications

The SJ-A1420 Series oscillators are ideally suited for applications involving more than one clock or allows ATE (Automatic Test Equipment) board testing without having to remove the oscillator. In multiplexing applications, multiplex clock signals can be made available to a system using the enable/disable 3-state feature.

Features

- Wide frequency range—40.1MHz to 70.0MHz
- User specified tolerance from ±20ppm
- Will withstand vapor phase temperatures of 253°C for 4 minutes maximum
- Low power consumption
- High shock resistance, to 3000g
- 3.3 volt operation
- Metal lid electrically connected to ground to reduce EMI
- Gold plated leads—Solder dipped leads available upon request

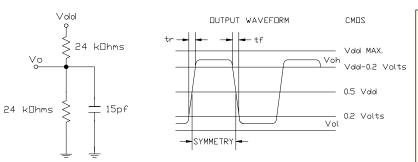




Continued CMOS Compatible SJ-A1420 Series

Operating Conditions and Output Characteristics						
PARAMETER		CONDITIONS	MINIMUM	MAXIMUM		
ristics	Supply voltage (V _{DD})	Supply	3.15V	3.45V		
		Breakdown	-0.5V	7.0V		
	Supply current (I _{DD})	V _{DD} or ground current	0.0 mA	40 mA		
÷	Output current (I _O)	Low level output	0.0 mA	±16.0 mA		
ľ		current				
General Characteristics	Tolerance	User specified	±20ppm			
	Operating temperature (T _A)		0°C	70°C		
	Storage temperature (T _S)		-55°C	125°C		
	Power dissipation (P _D)			138 mW		
Q	Lead temperature (T _L)	Soldering, 10 sec.		300°C		
Output Characteristics	Frequency		40.1MHz	70.0MHz		
	Symmetry	@.5V _{DD}	45/55%	55/45%		
	Logic 0 (V _{OL})	I _O =600μA		0.2V		
	Logic 1 (V _{OH})	I _O =600μA	V _{DD} -0.2V			
	Logic 0 (I _{OL} sink)	V _O =0.2V		600µA		
	Logic 1 (I _{OH} source)	V _O =V _{DD} -0.2V		600µA		
	Rise & fall time (t _r ,t _f)	10-90% V _O		8 ns		
	T _{pz} (Enable/Disable			25 ns		
臣	to high or low)					
) Ti	Enable/Disable					
	Logic high voltage		2.0V			
	Logic low voltage			0.8V		

(CMOS)



This information has been carefully prepared and is believed to be entirely reliable. However, no responsibility is assumed for inaccuracies. NEL reserves the right to make changes at any time in order to improve design and supply the best product possible.

Specialty Oscillators for Unique Requirements

If the characteristics listed above do not meet your specific requirements, specialty solutions are often available.

For example, if you need better stability, extended temperature range, or tighter symmetry, NEL can provide a SJ-A1429 series oscillator to serve your needs.

To let us know your special requirements, complete our Specialty Oscillator sheet. We will respond with the desired specialty oscillator, or discuss with you a solution that most closely meets your needs.

