July, 1998





Technical Specification for 2.5Gbps Fiber Optic Transmitter Module

SDT8078-T_-Q_

155.52Mb/s	622.08Mb/s	other2488.32Mbps
Short Haul Intermediate Reach	Long Haul Long Reach	other <u>STM16 L-16.2 / O</u> C48 LR-2
Single 5.0 V	Single 3.3 V	other
1.3 μm	1.55 μm	other
Transmitter	Receiver	Transceiver
,	(2R / 3R)	(
SUMITOMO	ELECTRIC INI	DUSTRIES, LTD.
Sumitomo Electric reserves t	he right to make changes in	the specification without prior notice.
	ppriate use of the product. The sy	s picture symbols to prevent possible injury to operator or mbols and definitions are as shown below. Be sure to be
⚠ Warning Wrong operation with	out following this instruction may I	ead to human death or serious injury.
▲ Caution Wrong operation without	out following this instruction may le	ead to human injury or property damage.
Example of picture symbols indicates pro	phibition of actions. Action details	s are explained thereafter.
indicates co	mpulsory actions or instructions.	Action details are explained thereafter.

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1. General

The features of SDT8078-T_-Q_ are listed below:

* SDH STM-16 L-16.2 / SONET OC-48 LR-2 Compliant

* Power Supply Voltage Single +5V (or -5V)

* Low Power Supply Current
* Compact Package Size
* Pin Configuration
150mA (typ.)
36 X 15 X 9 mm
24 pin Dual in Line

* Uncooled Laser Diode with Automatic Optical Power Control Circuit
* Laser Diode 1550nm InGaAsP / InP, DFB-LD

- * Optical Output Shut-down Function (Disable Function)
- * Laser Bias Current Alarm Function.
- * Laser Bias Monitor / Rear Facet Monitor Function.
- * Clocked / Non-clocked mode selector
- * Optical Connector Interface FC / SC connector

2. Block Diagram

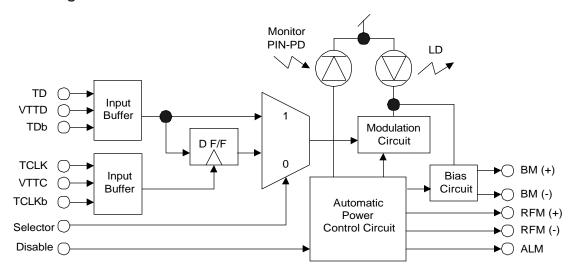


Figure 1 Block Diagram

*Clocked Operation Mode (Vs=Vslct)

H:High Level, L:Low Level, \$\oplus\$:H or L Q0:Previous optical output status before data input condition defined

*Non-Clocked Operation Mode (Vs=Vnslct)

100-01	(• 0 – • 1 10101)					
Data	Optical					
TD	TDb	Output				
Н	L	Н				
L	Н	L				

H:High Level, L:Low Level

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3. Package Dimension

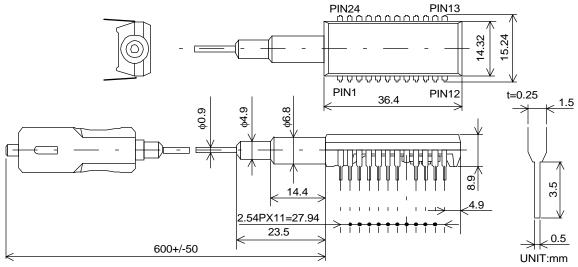


Figure 2. Package Dimension

∆ Caution

Do not disassemble this product. Otherwise, failure, electrical shock, overheating or fire may occur.

Handle the lead pins carefully. Use assisting tools or prospective aids as required. A lead pin may injure skin or human body

4. Pin Assignment

Pin	Symbol	I/O	Level	Description
1	Vcc		+5V	Positive power supply (+5V)
2	Vcc		+5V	Positive power supply (+5V)
3	RFM(+)	0	Analog	Rear facet PD current monitor voltage (positive)
4	RFM(-)	0	Analog	Rear facet PD current monitor voltage (negative)
5	TDb	ı	PECL	Differential data input (negative)
6	VTTD		Vcc-2V	Data input signal termination, Vcc-2V supply for DC PECL interface.
			(AC GND)	Or pull down to GND via a capacitor for AC coupled interface.
7	TD	ı	PECL	Differential data input (positive)
8	Omit			Connected to daughter board attached to the module.
				Must not be connected to customer's circuit board.
9	Vcc		+5V	Positive power supply (+5V)
10	Vcc		+5V	Positive power supply (+5V)
11	Vee		GND	Negative power supply (GND)
12	Vee		GND	Negative power supply (GND)
13	Vee		GND	Negative power supply (GND)
14	Omit			Connected to daughter board attached to the module.
				Must not be connected to customer's circuit board.
15	ALM	0	TTL	LD bias current alarm, normal low, active high
16	Disable	- 1	TTL	Transmitter disable input. Default (open) LOW and transmitter is enabled.
17	Selector	1		Signal input mode selector, clocked or non-clocked.
				Default (open) LOW and clocked mode.
18	TCLK	- 1	PECL	Differential clock input (positive)
19	VTTC		Vcc-2V	Clock input signal termination, Vcc-2V supply for DC PECL interface.
			(AC GND)	Or pull down to GND via a capacitor for AC coupled interface
20	TCLKb	I	PECL	Differential clock input (negative)
21	BM(-)	0	Analog	LD bias current monitor voltage (negative)
22	BM(+)	0	Analog	LD bias current monitor voltage (positive)
23	Vcc		+5V	Positive power supply (+5V)
24	Vcc		+5V	Positive power supply (+5V)

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5. Absolute Maximum Ratings

Parameter	Symbol	min.	Max	Unit	Note
Storage Case Temperature	Ts	-40	85	°C	1
Operating Case Temperature	T_C	0	70	°C	1
Supply Voltage	Vcc-Vee	0.0	6.0	V	2
Input Voltage	Vi	Vee	Vcc	V	3
Lead Soldering (Temperature)			260	°C	4
(Time)			10	sec.	

Note 1. No condensation allowed. 2. Vcc>Vee, Vcc=+5.0V for Vee=GND or Vcc=GND for Vee=-5.0V 3. Data, Clock, Disable and Selector 4. Measured on lead pins 2mm (0.079in.) off the package bottom

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Use the product with the rated voltage described in the specification. If the voltage exceeds the maximum rating, overheating or fire may occur.

▲ Caution

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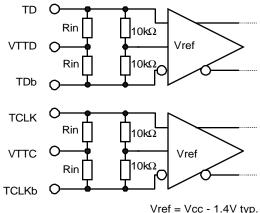
Do not store the product in the area where temperature exceeds the maximum rating, where there is too much moisture or dampness, where there is acid gas or corrosive gas, or other extreme conditions. Otherwise, failure, overheating or fire may occur.

6. Electrical Interface

(Unless otherwise specified, Vcc-Vee = 4.75 to 5.25 V @2488.32Mbps, PRBS2^23-1, 50% duty and all operating temperature shall apply.)

and an operating temperature	117						
Parameter	Symbol	Min.	Тур.	Max.	Unit	Note	
Supply Voltage	Supply Voltage			5.00	5.25	V	
Supply Current		ld		150	220	mA	1
Input Impedance (Data and Clock)		Rin		50		Ω	2
Input Voltage	High	V_{IH}	Vcc-1.00	Vcc-0.90	Vcc-0.70	V	
(Data and Clock, for ECL or PECL interface)	Low	V_{IL}	Vcc-1.90	Vcc-1.70	Vcc-1.60	V	
Differential Input Voltage Swing for AC couple	ed interface	Vin	0.45	0.80	1.20	Vp-p	
Input Signal Rise Time (20% - 80%)		Tr		100	120	ps	
Input Signal Fall Time (20% - 80%)	Tf		100	120	ps		
Set up Time (for clocked mode)	Tset	130			ps	3	
Hold Time (for clocked mode)	Hold Time (for clocked mode)					ps	3
Disable Input Voltage	Disable	Vdisbl	Vee+2.00		Vcc	V	4
	Enable	Venbl	Vee		Vee+0.8	V	
Selector Input Voltage	Clocked	Vslct	Vee		Vee+1.5	V	5
	Non Clocked	Vnclct	Vcc-1.5		Vcc	V	
LD Bias Alarm Output Voltage	Normal	Valml	Vee		Vee+0.5	V	6
	Abnormal	Valmh	Vcc-1.00		Vcc	V	
LD Bias Monitor Voltage (between pin 22 and	pin 21)	Vbm	0.01	0.10	0.50	V	7, 9
Rear Facet PD monitor Voltage (between pin	3 and pin 4)	Vrfm	0.01	0.10	0.25	V	8, 9

- 1. Termination current is not included. 2. Measured between each signal input and VTTD or VTTC. Refer to Figure 3.
- 3. Refer to Figure 4. 4. Default (Open) normal operation. 5. Default (Open) clocked mode. 6. Alarm will be launched when LD bias current exceeds 70mA typ. 7. LD bias current can be monitored by measuring the voltage difference between BM(+) Pin 22 and BM(-) Pin 21. Please refer to Figure 5. 8. Rear Facet PD current can be monitored by measuring the voltage difference between RFM(+) Pin 3 and RFM(-) Pin 4. Please refer to Figure 5. 9. High input impedance (> $1M\Omega$) device is required to measure this voltage.



VICI = VCC - 1.4V typ

TDb
TCLK
TCLKb
Tset Thold
T= 400 psec

Figure 4 Input Data and Clock Timing

Figure 3 Data and Clock Input Interface (SDT8078-T_-Q_)

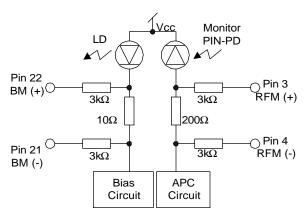


Figure 5 BM and RFM Interface

7. Optical Interface

. (Unless otherwise specified, Vcc-Vee = 4.75 to 5.25 V @2488.32Mbps, PRBS2^23-1, 50% duty and all operating temperature shall apply.)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Note
Optical Output Power	Po	-2.0		3.0	dBm	
Optical Output Power (disable)	Podis			-45.0	dBm	
Extinction Ratio	Er	8.2			dB	
Side Mode Suppression Ratio	Sr	30			dB	
Center Wavelength	λς	1500		1580	nm	
Spectral Width (-20dB)	$\Delta\lambda_{20}$			1.0	nm	
Dispersion Penalty	Dp			2.0	dB	1
Output Eye Diagram	Com	Compliant with Bellcore G-253 CORE and ITU G957				

Note1. Less than 2dB penalty is guaranteed when connected to SDT8978-R@-@@.

Note2. Refer to Figure 6 for the eye diagram mask

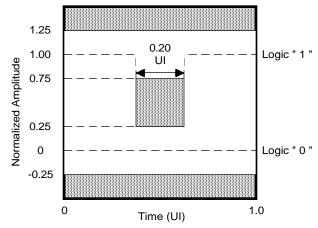


Figure 6 Eye Diagram Mask for Optical Output

▲ Warning

Do not look at the laser beam projection area (e.g. end of optical connector) with naked eyes or through optical equipment while the power is supplied to this product. Otherwise, your eyes may be injured.

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8. Recommended User Interface

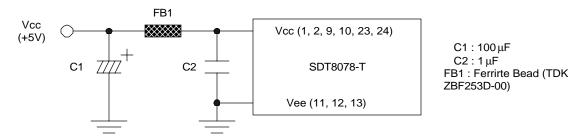


Figure 7 Recommended Power Supply Filtering

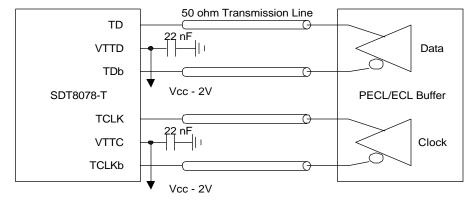


Figure 8 Data and Clock Interface with PECL/ECL Device (DC coupled Interface)

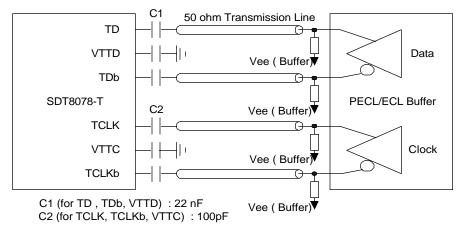


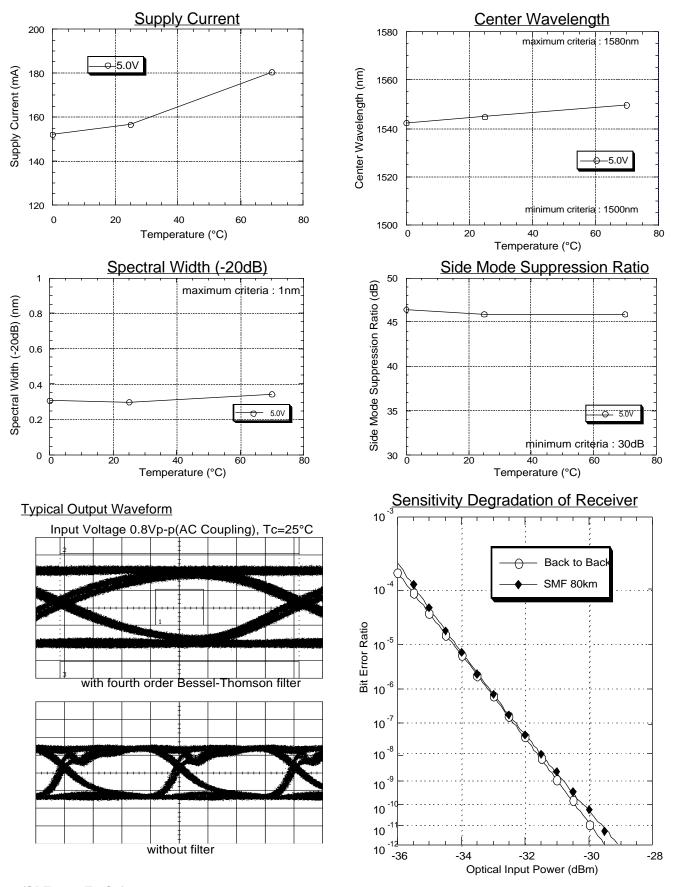
Figure 9 Data and Clock Interface with PECL/ECL Device (AC coupled Interface)

9. Fiber Pigtail Specification

Parameter	Min.	Тур.	Max.	Unit	Note
Core Diameter		9.5		μm	
Cladding Diameter		125		μm	
Outer Diameter		0.9		mm	
Optical Cord Tensile Beak Strength			9.8	N	
Bend Radius	30			mm	

O not give undue force or impact to the optical fiber pigtail. A broken optical fiber may injure skin or human body, or a strong laser beam may cause eye injury. Operate the equipment carefully. Use assisting tools or prospective aids as required.

10. Characteristic Information



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11. Reliability Test (Under qualification)

Bellcore TA	A-NWT-000983 Is	sue 2, December 19	93							
Heading	Heading Test Reference Condition				Sampling			SEI Result		
				LTPD	SS	С	SS	F/C		
Mechanical	Mechanical	MIL-STD-883	Condition B							
Integrity	Shock	Method 2002	5 times/axis							
			500G, 1.0 ms	20%	11	0				
			1,500G, 0.5ms	20%	11	0	11	0		
	Vibration	MIL-STD-883	Condition A	20%	11	0	11	0		
		Method 2007	20 G							
			20-2,000 Hz							
			4 min/cycle; 4 cycles/axis							
	Thermal Shock	MIL-STD-883	ΔT=100°C	20%	11	0	11	0		
		Method 1011								
	Solderability	MIL-STD-883	(steam aging not required)	20%	11	0	11	0		
		Method 2003								
	Fiber Pull		1 Kg; 3 times;5sec.	20%	11	0				
			2 Kg; 3 times; 5sec.	20%	11	0				
Endurance	Accel. Aging	(R)-453	+85C; rated power							
	(High Temp.)	Section 5.18	>5,000hrs.		25		25	0		
			>10,000hrs.		10					
	High Temp.		max. storage T (T=85°C)	20%	11	0				
	Storage		>2,000							
	Low Temp.		min. storage T (T=-40°C)	20%	11	0	11	0		
	Storage		>2,000							
	Temperature	Section 5.20	- 40°C to +85°C							
	Cycling		400 times pass/fail	20%	11	0				
			500 times for info.		11					
			500 times pass/fail	20%	11	0	11	0		
			1000 times for info.		11		11	0		
	Damp Heat	MIL-STD-202 M103	40°C , 95%, 56days	20%	11	0	11	0		
	(if using epoxy)	or IEC 68-2-3	or 85°C /85%RH 2,000hrs.	20%	11	0				
	Cyclic Moisture	Section 5.23		20%	11	0	11	0		
	Resistance									
Special	Internal	MIL-STD-883	< 5,000 ppm	20%	11	0	11	0		
Tests	Moisture	Method 1018	water vapor							
	Flammability	TR357:Sec. 4.4.2.5						ОК		
	ESD Threshold	Section 5.22			6		6	0		

12. Laser Safety

This product uses a semiconductor laser system and is a laser class 1 product acc. FDA, complies with 21CFR1040. 10 and 1040.11. Also this product is a laser class 1 product acc. IEC 825-1.

Class 1 Laser Product

∆Caution

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If this product is used under conditions not recommended in the specification or this product is used with unauthorized revision, classfication for laser product safety standard is invalid. Classify the product again at your responsibility and take appropriate actions.

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13. Ordering Information

Ordering Number	Connector type
SDT8078-TC-QN	SC
SDT8078-TD-QN	FC/PC

14. Other Precaution

Under such a strong vibration environment as in automobile, the performance and reliability are not guaranteed.

The governmental approval is required to export this product to other countries. To dispose of these components, the appropriate procedure should be taken to prevent illegal exportation.

This module must be handled, used and disposed of according to your company's safe working practice.

A Warning



Be sure to carry out correct soldering for connection to peripheral circuits in order to prevent contact failure or short-circuit. Otherwise, a strong laser beam may cause eye injury, overheating or fire.

Do not put this product or components of this product into your mouth. This product contaions material harmful to health.

▲ Caution



Be sure to turn the power off when you touch this product connected to the printed circuit boards. Otherwise, electric shock may occur

Dispose this product or equipment including this product properly as an industrial waste according to the regulations.

15. For More Information

U.S.A.

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