June, 2000





Technical Specification for Optical Transceiver Module

SCM6111-GL

155.52Mb/s Short Haul Intermediate Reach Single 5.0 V	622.08Mb/s Long Haul Long Reach Single 3.3 V	other other
1.3 µm Transmitter	1.55 μm Receiver	other
	(2R / 3R)	(2R / 3R)

SUMITOMO ELECTRIC

Sumitomo Electric reserves the right to make changes in this specification without prior notice.

#Safety Precaution	<u>Symbols</u>	This specification uses various picture symbols to prevent possible injury to operator or other
persons or damage to properties	for appropria	te use of the product. The symbols and definitions are as shown below. Be sure to be familia
with these symbols before reading	g this specifi	cation.

Δ	Warning	Wrong operation without following this instruction may lead to human death or serious injury.
Δ	Caution	Wrong operation without following this instruction may lead to human injury or property damage.

Example of picture symbols

indicates prohibition of actions. Action details are explained thereafter.

indicates compulsory actions or instructions. Action details are explained thereafter.

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1. General

Features of SCM6111-GL are listed below.

* SDH STM-1 L-1.1 / SONET OC-3 LR-1 Compliant

* Power Supply Voltage Single +3.3V

* Compact Package Size 49.0 X 13.59 X 9.4 mm

* Electrical Interface LVPECL for DATA, LVTTL for Signal Detect and Laser Disable

* Fiber Coupled Power

* Input Power Range

* Monitor Functions

-5 ~ 0dBm (Typ. -2dBm) into SMF

-8 ~ -34dBm (Typ. Sensitivity -35dBm)

Laser Bias Monitor, Rear Facet Monitor

* Laser Disable Function

* Signal Detect (SD) Function

* Connector Interface LC Duplex Receptacle

2. Block Diagram

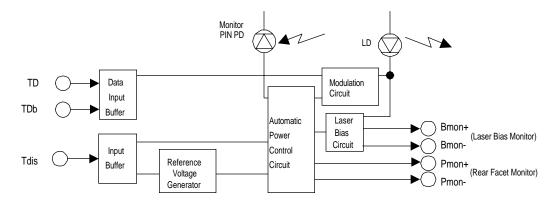


Figure 1-1. Block Diagram (Transmitter)

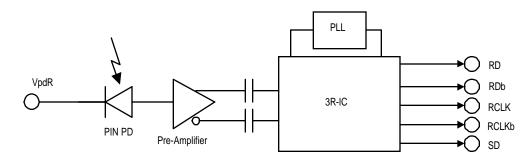


Figure 1-2 Block Diagram (Receiver)

∆Caution

 γ Do not disassemble this product. Otherwise, failure, electrical shock, overheating or fire may occur.

Handle the lead pins carefully. Use assisting tools or prospective aids as required. A lead pin may injure skin or human body

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3. Package Dimension

All dimensions are in mm.

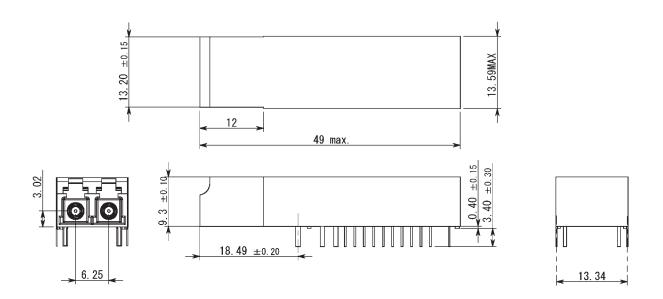


Figure 2-1. Outline Dimensions (SCM6111-GL-ZN/ZW)

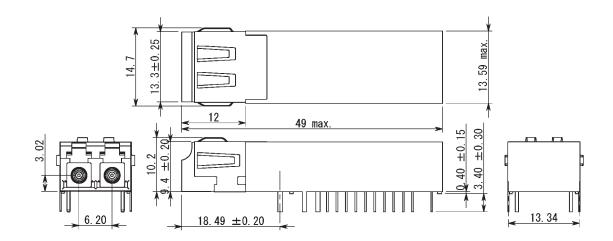


Figure 2-2. Outline Dimensions (SCM6111-GL-CN/CW)

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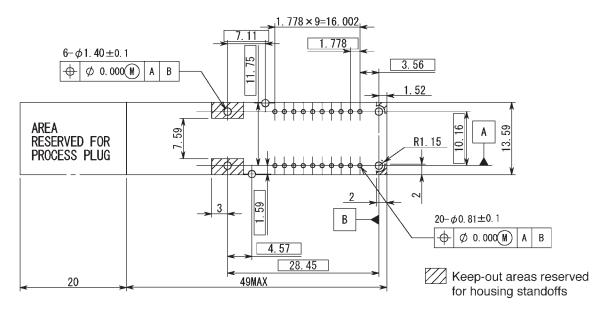


Figure 2-3. Recommended Footprint

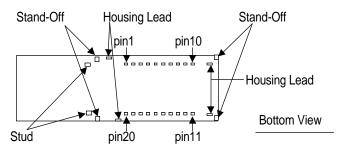
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4. Pin Assignment

No.	Symbol	I/O/P	Level	Description
1	VpdR	Р	+3.3V DC	DC Bias Supply for Receiver PIN-PD.
2	VeeR	Р	GND	Power Supply (-) for Receiver.
3	VeeR	Р	GND	Power Supply (-) for Receiver.
4	RCLKb	0	LVPECL	Recovered Clock Output. Inverted.
5	RCLK	0	LVPECL	Recovered Clock Output. Non-inverted.
6	VeeR	Р	GND	Power Supply (-) for Receiver.
7	VccR	Р	+3.3V DC	Power Supply (+) for Receiver.
8	SD	0	LVTTL	Signal Detect. High level indicates presence of optical input signal (Active High).
9	RDb	0	LVPECL	Inverted Receiver Output Data. No internal terminations are provided.
10	RD	0	LVPECL	Non-Inverted Receiver Output Data. No internal terminations are provided.
11	VccT	Р	+3.3V DC	Power Supply (+) for Transmitter.
12	VeeT	Р	GND	Power Supply (-) for Transmitter.
13	Tdis	I	LVTTL/LVCMOS	Transmitter Disable (Active High). Defaults to logic 0 (enable TX) when left open.
14	TD	I	LVPECL	Non-Inverted Transmitter Input Data. Self biased. Not internally terminated.
15	TDb	I	LVPECL	Inverted Transmitter Input Data. Self biased. Not internally terminated.
16	VeeT	Р	GND	Power Supply (-) for Transmitter.
17	Bmon-	0	Analog Voltage	LDBias Current Monitor. Voltage difference between pins 17 and 18 is proportional
18	Bmon+	0	Analog vollage	to the laser bias current.
19	Pmon-	0	Analog Voltage	Rear Facet Monitor. Transmitter output pow er can be monitored, in terms of rear
20	Pmon+	0	Aliaby Vollage	facet monitor PD current, by measuring voltage difference betw een pins 19 and 20.

Notes

- 1. I/O/P stands for signal input, signal output, and DC power/bias supply, respectively.
- 2. Refer to figure 3 for details of Bmon and Pmon outputs.



5. Absolute Maximum Ratings

Parameter	Symbol	min.	Max	Unit	Note
Storage Case Temperature	Ts	-40	85	°C	1
Operating Case Teperature	Tc	-40	85	°C	
Supply Voltage	Vcc-Vee	0.0	4.0	V	2
Input Voltage	Vi	Vee	Vcc+0.5	V	3
Lead Soldering Conditions	Temperature		260	°C	4
	Time		10	sec.	

Notes:

- 1. No condensation allowed.
- 2. Vcc>Vee, Vcc=+3.3V, Vee=GND
- 3. TD, TDb, Tdis
- 4. Measured on lead pin at 2mm (0.079in.) off the package bottom

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Use the product with the rated voltage described in the specification. If the voltage exceeds the maximum rating, overheating or fire may occur.

▲ Caution

Do not store the product in the area where temperature exceeds the maximum rating, where there is too much moisture or dampness, where there is acid gas or corrosive gas, or other extreme conditions. Otherwise, failure, overheating or fire may occur.

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6. Electrical Interface

(Unless otherwise specified, Vcc-Vee = 3.14 to 3.47 V and all operating temperature shall apply.)

6-1. Transmitter side

Paramete	r	Symbol	min.	Тур.	Max.	Unit	Note
Supply Voltage		Vcc-Vee	3.14	3.30	3.47	V	
Supply Current		ldtx		70	140	mA	1, 2, 3
TD, TDb Input Voltage	High	Vih	Vcc-1.17		Vcc-0.73	V	4, 5
	Low	Vil	Vcc-1.95		Vcc-1.45		
TD, TDb Input Current	High	lih	-10		150	μΑ	4, 5
	Low	lil	-10		10		
Signal Input Rise / Fall Ti	me				1.5	nsec.	6
Tdis Input Voltage	High	Vdi	Vee+2.0		Vcc	V	7
	Low	Vei	Vee		Vee+0.8	V	
Tdis Input Current	High	Idi	-10	140	200	μΑ	
LD Bias Monitor Voltage		Vbm	0.01	0.05	0.50	V	5, 8

Notes:

- 1. Input bias current is not included.
- 2.50% duty cycle data.
- 3. 155.52Mbps, PRBS2^23-1, NRZ.
- 4. Vcc-Vee=3.3V.
- 5. Tc=25°C.
- 6. 20-80%.
- 7. Refer to Section 8, "Relation between Disable Input Voltage and Optical Output Power", for detail.
- 8. The Laser Bias Monitor Current and Rear Facet Monitor Current are calculated as ratios between the corresponding voltages and current sensing resistors, 10Ω and 200Ω , as shown in the figure 3.

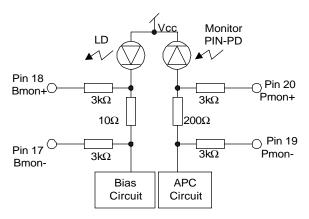


Figure 3 Bmon and Pmon Interface

6-2. Receiver side

Parameter		Symbol	Min.	Тур.	Max.	Unit	Note
Power Supply Voltages		V_{CC}	3.135	3.3	3.465	V	
Power Supply Current		I _{CC}	-	-	200	mA	1
RD, RDb, RCLK, RCLKb	Low	V_{OL}	V _{CC} -1.88	-	V _{CC} -1.62	V	2, 3
Output Voltage	High	V _{OH}	V _{CC} -1.20	-	V _{CC} -0.83	V	
SD Output Voltage	Low		Vee		0.800	V	2
	High		2.400		Vcc	V	
Output Transition Time	•	t _l	-	-	0.5	ns	4
Clock Sampling Point		T_{CSP}	2.6	3.1	3.6	ns	5
Clock Duty Cycle			45		55	%	
Output Clock Random Jitte	er	J_C		1.3	3.6	deg rms	
Output Clock Jitter Peakin	g	J_P			0.1	dB	
SD Assert Time	•	Та	2.3		100	μs	6
SD Deassert Time	•	Td	2.3		100	μs	

Notes:

- 1. Output current is not included. 155.52Mbps, PRBS2^23-1, NRZ.
- 2. Vcc=+3.3V, Tc=25°C.
- 3. RI=50 Ω to Vcc-2V.
- 4. 20-80%. Input capacitance and stray capacitance of measuring devices should be less than 2pF.
- 5. Time difference between RD transition and rising edge of RCLK. Refer to Figure 4.
- 6. 155.52Mbps, PRBS2^23-1, NRZ.

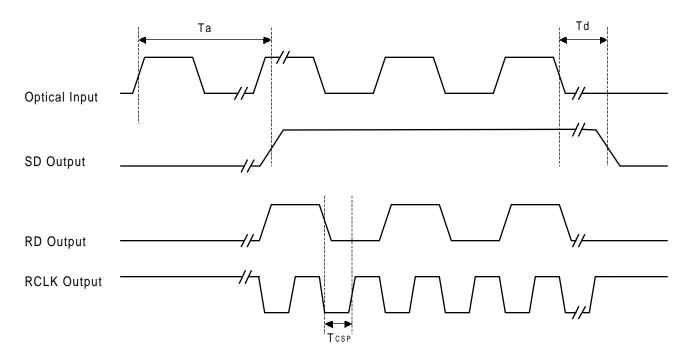


Figure 4 Timimg Chart

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7. Optical Interface

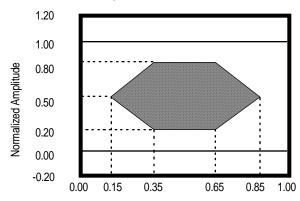
(Unless otherwise specified, Vcc-Vee = 3.14 to 3.47 V and all operating temperature shall apply.)

7-1. Transmitter side

Parameter	Symbol	min.	Тур.	Max.	Unit	Note
Average Output Power	Po	-5.0	-2.0	0.0	dBm	1
Extinction Ratio	Er	10			dB	1
Center Wavelength	λς	1280		1335	nm	
Spectral Width (-20dB Width)	Δλ			1.0	nm	
Side Mode Suppression Ratio	Sr	30			dB	
Eye Mask for Optical Output	Refer to Figure 4					

Notes

^{1.} Measured at 155.52Mbps PRBS2^23-1



Relation between Input Signal and Optical Output Signal

Input Signal		Optical Output Signal
TD TDb		
High	Low	ON (High)
Low	High	OFF (Low)
High	High	Undefined
Low	Low	Undefined

Figure 5 Optical Pulse Mask with Fourth Order Bessel-Thomson Filter Specified in ITU-T G.957

M Warning

Do not look at the laser beam projection area (e.g. end of optical connector) with naked eyes or through optical equipment while the power is supplied to this product. Otherwise, your eyes may be injured.

7-2. Receiver side

Parameter	Symbol	min.	Тур.	Max.	Unit	Note
Optical Input Wavelength	-	1260		1580	nm	
Minimum Sensitivity	Pmin		-35.0	-34.0	dBm	1, 2
Overload	Pmax	0			dBm	1, 2
SD Assert Level	Pa			-36	dBm	2
SD Deassert Level	Pd	-42	-39		dBm	
SD Hysteresis	Phys	1.5		6	dB	

Notes:

8. Relation between Disable Input Voltage and Optical Output Power

Tdis Input Voltage	Optical Output Power
"L"(Vee ~ Vee+0.8V)	Enabled
"H"(Vee+2.0V ~ Vcc)	Disabled (<-45dBm)
Open	Enabled

^{1.} BER=10^-10

^{2.} Measured at the bit rate of 155.52Mbps, PRBS 2^23-1, NRZ

9. Recommended Interface Circuit

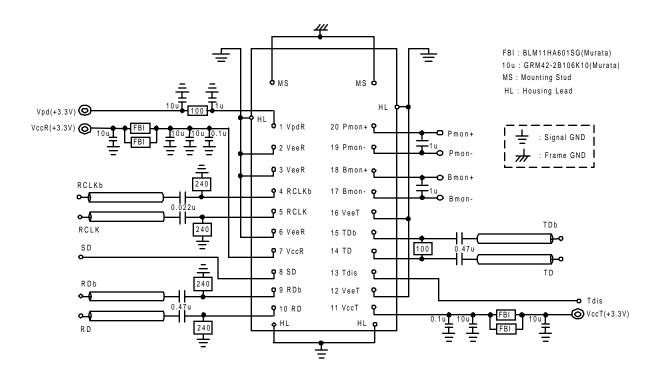


Figure 5. Recommended Interface Circuit

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10. Reliability Test Program

GR-468-CORE Issue 1, December 1998 Laser Module

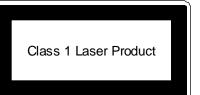
TEST	REFERENCE	CONDITIONS	SAM	IPLIN	G
			LTPD	SS	С
Mechanical	MIL-STD-883	5 times/axis			
Shock	Method 2002	1,500G, 0.5ms	20	11	0
Vibration	MIL-STD-883	Cond. A 20G, 20-2,000G	20	11	0
	Method 2007	Hz, 4min/cy, 4cy/axis			
Thermal Shock	MIL-STD-883	Delta T=100degC	20	11	0
	Method 1011	OdegC to 100degC			
Solderability	MIL-STD-883	(steam aging not	20	11	0
	Method 2003	required)			
Accel. Aging	(R)-4-53 Section	85degC; rated power			-
(High Temp.)	5.18	1,000 hrs. for pass/fail	-	25	
		2,000, 5,000 hrs. for info.		10	
Low Temp.	-	min, storage T	20	11	0
Storage		1,000 hrs. for pass/fail			
		2000 hrs. for info.			
Temperature	Section 5.20	-40degC to +85degC			
Cycling		500 for pass/fail	20	11	0
		1,000 for info.	-	11	-
Damp Heat	MIL-STD-202	85degC/85%RH 1,000hrs.	20	11	0
	Method 103 or				
	IEC-68-2-3				
Cyc. Moist,. Res.	Sec. 5.23	-	20	11	0
Internal	MIL-STD-883	Max. 5,000ppm water	20	11	0
Moisture	Method 1018	vapour			
ESD Threshold	Section 5.22		-	6	-
	Mechanical Shock Vibration Thermal Shock Solderability Accel. Aging (High Temp.) Low Temp. Storage Temperature Cycling Damp Heat Cyc. Moist,. Res. Internal Moisture	Mechanical MIL-STD-883 Shock Method 2002 Vibration MIL-STD-883 Method 2007 Thermal Shock MIL-STD-883 Method 1011 Solderability MIL-STD-883 Method 2003 Accel. Aging (R)-4-53 Section (High Temp.) - 5.18 Low Temp. Storage	Mechanical Shock MIL-STD-883 Method 2002 5 times/axis 1,500G, 0.5ms Vibration MIL-STD-883 Method 2007 Cond. A 20G, 20-2,000G Hz, 4min/cy, 4cy/axis Thermal Shock MIL-STD-883 Method 1011 Delta T=100degC OdegC to 100degC Solderability MIL-STD-883 Method 2003 (steam aging not required) Accel. Aging (High Temp.) (R)-4-53 Section 5.18 85degC; rated power 1,000 hrs. for pass/fail 2,000, 5,000 hrs. for info. Low Temp. - min, storage T 1,000 hrs. for pass/fail 2000 hrs. for info. Temperature Cycling Section 5.20 -40degC to +85degC 500 for pass/fail 1,000 for info. Damp Heat MIL-STD-202 Method 103 or IEC-68-2-3 85degC/85%RH 1,000hrs. Cyc. Moist,. Res. Sec. 5.23 - Internal MIL-STD-883 Method 1018 Max. 5,000ppm water vapour	Mechanical Shock MIL-STD-883 Method 2002 5 times/axis 1,500G, 0.5ms 20 Vibration MIL-STD-883 Cond. A 20G, 20-2,000G Method 2007 Hz, 4min/cy, 4cy/axis 20 Thermal Shock MIL-STD-883 Delta T=100degC Method 1011 OdegC to 100degC 20 Solderability MIL-STD-883 (steam aging not required) 20 Accel. Aging (High Temp.) (R)-4-53 Section 5.18 1,000 hrs. for pass/fail 2,000, 5,000 hrs. for info. - Low Temp. - min, storage T 1,000 hrs. for info. 20 Storage 1,000 hrs. for info. 20 Temperature Section 5.20 40degC to +85degC 500 for pass/fail 2000 hrs. for info. 20 Temperature Cycling Section 5.20 40degC to +85degC 500 for pass/fail 1,000 for info. 20 Damp Heat MIL-STD-202 Method 103 or IEC-68-2-3 85degC/85%RH 1,000hrs. 20 Cyc. Moist,. Res. Sec. 5.23	Mechanical Shock MIL-STD-883 Method 2002 5 times/axis 20 11 Vibration MIL-STD-883 Method 2007 1,500G, 0.5ms 20 11 Vibration MIL-STD-883 Method 2007 Hz, 4min/cy, 4cy/axis 20 11 Thermal Shock MIL-STD-883 Delta T=100degC 20 11 Solderability MIL-STD-883 (steam aging not required) 20 11 Accel. Aging (High Temp.) (R)-4-53 Section 5.18 (steam aging not required) - 25 Low Temp. 5.18 (steam aging not required) - 25 Low Temp. - min, storage T (n)00 hrs. for pass/fail 2000 hrs. for info. 20 11 Storage 1,000 hrs. for pass/fail 2000 hrs. for info. 20 11 Temperature Section 5.20 (steam aging not required) - 20 11 Damp Heat MIL-STD-202 (min, storage T 1,000 hrs. for pass/fail 2000 hrs. for info. - 11 Damp Heat MIL-STD-202 (method 103 or IEC-68-2-3) 85degC/85%RH 1,000hrs. 20 11 Damp Heat MIL-STD-883 (min, steam aging not required) - <

SS: Sample Size

C: Maximum number of failure allowed to pass the test.

11. Laser Safety

This product uses a semiconductor laser system and is a laser class 1 product acc. FDA, complies with 21CFR 1040. 10 and 1040.11. Also this product is a laser class 1 product acc. IEC 825-1.



∆ Caution

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If this product is used under conditions not recommended in the specification or this product is used with unauthorized revision, classfication for laser product safety standard is invalid. Classify the product again at your responsibility and take appropriate actions.

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12. Other Precaution

Under such a strong vibration environment as in automobile, the performance and reliability are not guaranteed.

The governmental approval is required to export this product to other countries. To dispose of these components, the appropriate procedure should be taken to prevent illegal exportation.

This module must be handled, used and disposed of according to your company's safe working practice.

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Be sure to carry out correct soldering for connection to peripheral circuits in order to prevent contact failure or short-circuit. Otherwise, a strong laser beam may cause eye injury, overheating or fire.

Do not put this product or components of this product into your mouth. This product contaions material harmful to health.



Be sure to turn the power off when you touch this product connected to the printed circuit boards. Otherwise, electric shock may occur.

Dispose this product or equipment including this product properly as an industrial waste according to the regulations.

13. Ordering Information

Ordering Number	Connector Type	EMI Shield Finger Option	Operating Case Temperature
SCM6111-GL-ZN	LC Duplex Receptacle,Metallized	Without Finger	-5°C ~ 70°C
SCM6111-GL-CN	LC Duplex Receptacle, Metallized	With Type-C Finger	
SCM6111-GL-ZW	LC Duplex Receptacle,Metallized	Without Finger	-40°C ~ 85°C
SCM6111-GL-CW	LC Duplex Receptacle,Metallized	With Type-C Finger	

14. For More Information

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