

ELECTROSTATIC SENSITIVE DEVICE
OBSERVE HANDLING PRECAUTIONS

MITSUBISHI RF MOSFET MODULE

RA20H8994M

896-902/ 935-941MHz 20W 12.5V, 3 Stage Amp. For MOBILE RADIO

DESCRIPTION

The RA20H8994M is a 20-watt RF MOSFET Amplifier Module for 12.5-volt mobile radios that operate in the 896- to 941-MHz range.

The battery can be connected directly to the drain of the enhancement-mode MOSFET transistors. Without the gate voltage ($V_{GG}=0V$), only a small leakage current flows into the drain and the RF input signal attenuates up to 60 dB. The output power and drain current increase as the gate voltage increases. With a gate voltage around 4V (minimum), output power and drain current increases substantially. The nominal output power becomes available at 4.5V (typical) and 5V (maximum). At $V_{GG}=5V$, the typical gate current is 1 mA. This module is designed for non-linear FM modulation, but may also be used for linear modulation by setting the drain quiescent current with the gate voltage and controlling the output power with the input power.

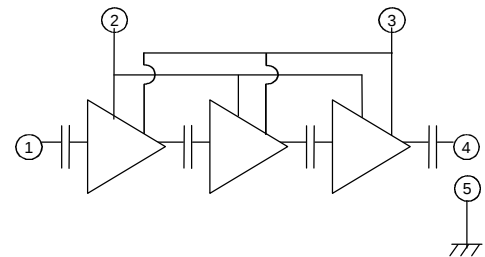
FEATURES

- Enhancement-Mode MOSFET Transistors
($I_{DD}=0$ @ $V_{DD}=12.5V$, $V_{GG}=0V$)
- $P_{out}>20W$, $\eta_T>25\%$ @ $V_{DD}=12.5V$, $V_{GG}=5V$, $P_{in}=50mW$
- Broadband Frequency Range: 896-902/ 935-941MHz
- Low-Power Control Current $I_{GG}=1mA$ (typ) at $V_{GG}=5V$
- Module Size: 66 x 21 x 9.88 mm
- Linear operation is possible by setting the quiescent drain current with the gate voltage and controlling the output power with the input power

ORDERING INFORMATION:

ORDER NUMBER	SUPPLY FORM
RA20H8994M-01	Antistatic tray, 10 modules/tray

BLOCK DIAGRAM



- ① RF Input (P_{in})
- ② Gate Voltage (V_{GG}), Power Control
- ③ Drain Voltage (V_{DD}), Battery
- ④ RF Output (P_{out})
- ⑤ RF Ground (Case)

PACKAGE CODE: H2S



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MITSUBISHI RF POWER MODULE

RA20H8994M

MAXIMUM RATINGS ($T_{\text{case}}=+25^{\circ}\text{C}$, unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	RATING	UNIT
V_{DD}	Drain Voltage	$V_{\text{GG}} < 5\text{V}$	17	V
V_{GG}	Gate Voltage	$V_{\text{DD}} < 12.5\text{V}$, $P_{\text{in}} = 0\text{mW}$	6	V
P_{in}	Input Power	$f = 896\text{-}902 / 935\text{-}941\text{MHz}$, $Z_{\text{G}} = Z_{\text{L}} = 50\Omega$	100	mW
P_{out}	Output Power		40	W
$T_{\text{case(OP)}}$	Operation Case Temperature Range		-30 to +110	$^{\circ}\text{C}$
T_{stg}	Storage Temperature Range		-40 to +110	$^{\circ}\text{C}$

The above parameters are independently guaranteed.

ELECTRICAL CHARACTERISTICS ($T_{\text{case}}=+25^{\circ}\text{C}$, $Z_{\text{G}}=Z_{\text{L}}=50\Omega$, unless otherwise specified)

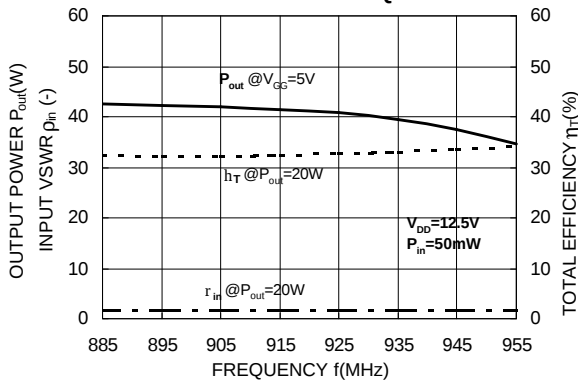
SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
f	Frequency Range		896-902/ 935-941			MHz
P_{out}	Output Power	$V_{\text{DD}} = 12.5\text{V}$, $V_{\text{GG}} = 5\text{V}$, $P_{\text{in}} = 50\text{mW}$	20			W
η_{T}	Total Efficiency	$P_{\text{out}} = 20\text{W}$ (V_{GG} control) $V_{\text{DD}} = 12.5\text{V}$ $P_{\text{in}} = 50\text{mW}$	25			%
$2f_{\text{o}}$	2 nd Harmonic				-30	dBc
ρ_{in}	Input VSWR				3:1	—
I_{GG}	Gate Current			1		mA
—	Stability	$V_{\text{DD}} = 10.0\text{-}15.5\text{V}$, $P_{\text{in}} = 25\text{-}70\text{mW}$, $P_{\text{out}} = 1$ to 25W (V_{GG} control), Load VSWR=3:1	No parasitic oscillation			—
—	Load VSWR Tolerance	$V_{\text{DD}} = 15.2\text{V}$, $P_{\text{in}} = 50\text{mW}$, $P_{\text{out}} = 20\text{W}$ (V_{GG} control), Load VSWR=8:1	No degradation or destroy			—

All parameters, conditions, ratings, and limits are subject to change without notice.

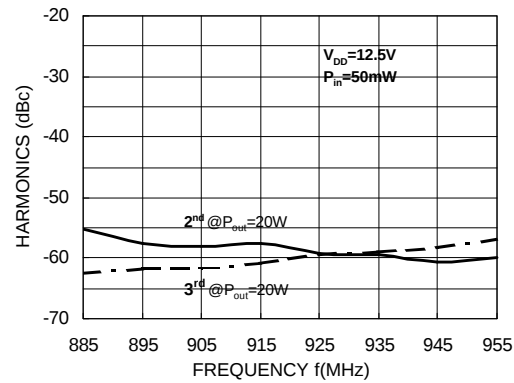


TYPICAL PERFORMANCE ($T_{\text{case}}=+25^{\circ}\text{C}$, $Z_G=Z_L=50\Omega$, unless otherwise specified)

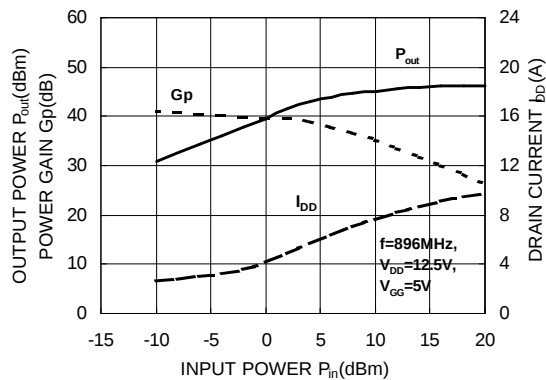
OUTPUT POWER, TOTAL EFFICIENCY,
and INPUT VSWR versus FREQUENCY



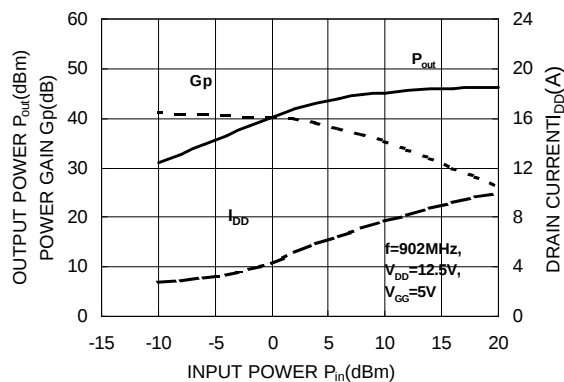
2nd, 3rd HARMONICS versus FREQUENCY



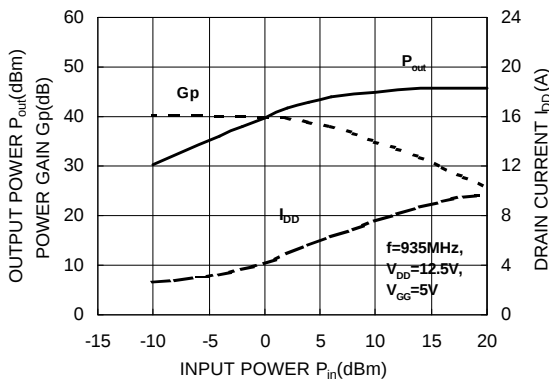
OUTPUT POWER, POWER GAIN and
DRAIN CURRENT versus INPUT POWER



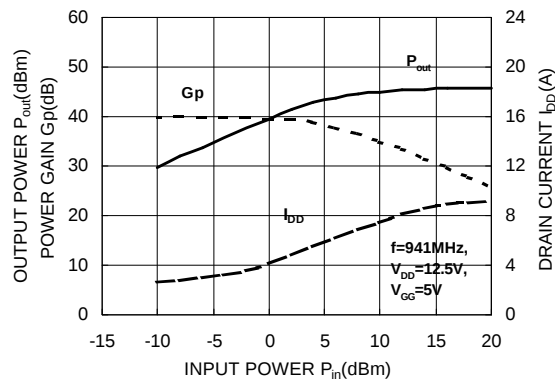
OUTPUT POWER, POWER GAIN and
DRAIN CURRENT versus INPUT POWER



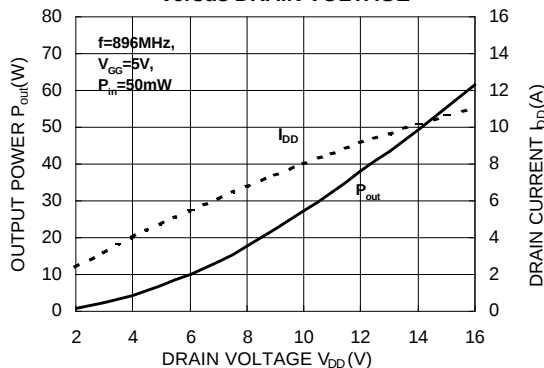
OUTPUT POWER, POWER GAIN and
DRAIN CURRENT versus INPUT POWER



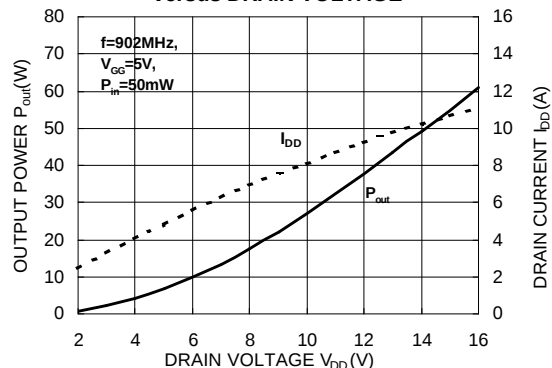
OUTPUT POWER, POWER GAIN and
DRAIN CURRENT versus INPUT POWER



OUTPUT POWER and DRAIN CURRENT
versus DRAIN VOLTAGE



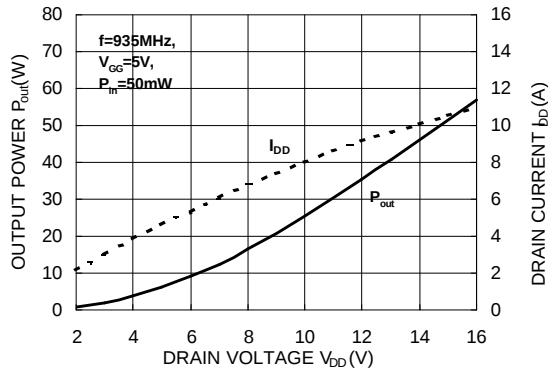
OUTPUT POWER and DRAIN CURRENT
versus DRAIN VOLTAGE



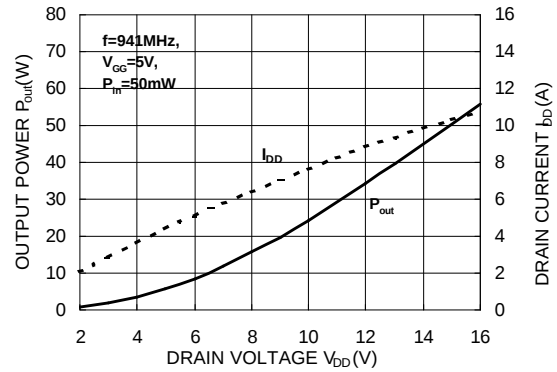


TYPICAL PERFORMANCE ($T_{\text{case}}=+25^{\circ}\text{C}$, $Z_G=Z_L=50\Omega$, unless otherwise specified)

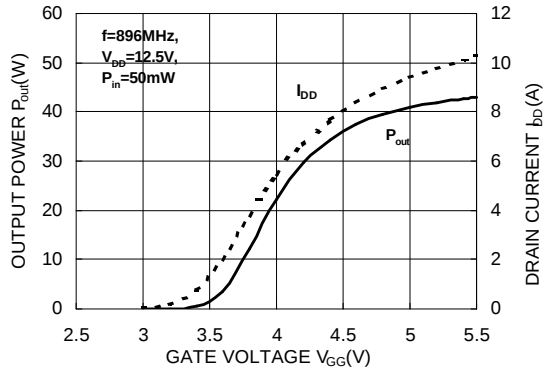
OUTPUT POWER and DRAIN CURRENT
versus DRAIN VOLTAGE



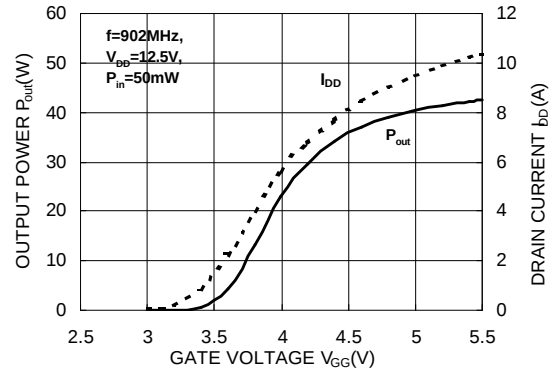
OUTPUT POWER and DRAIN CURRENT
versus DRAIN VOLTAGE



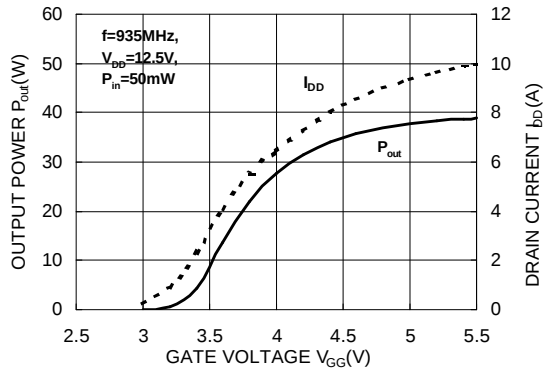
OUTPUT POWER and DRAIN CURRENT
versus GATE VOLTAGE



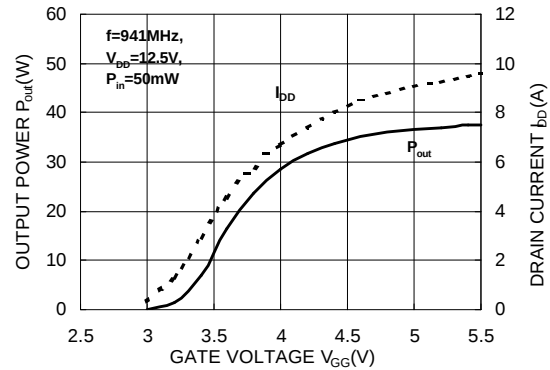
OUTPUT POWER and DRAIN CURRENT
versus GATE VOLTAGE



OUTPUT POWER and DRAIN CURRENT
versus GATE VOLTAGE



OUTPUT POWER and DRAIN CURRENT
versus GATE VOLTAGE





Technical drawing of a mechanical part, showing a top view and a side view. The top view includes dimensions for overall length (66.0 ± 0.5), width (21.0 ± 0.5), and various internal features. The side view shows the profile of the part, including a fillet (R2 ± 0.5) and a hole (Ø0.45 ± 0.15). The drawing is labeled with dimensions and tolerances.

Top View Dimensions:

- Overall Length: 66.0 ± 0.5
- Overall Width: 21.0 ± 0.5
- Distance from Left Edge to First Feature: 3.0 ± 0.3
- Distance from Left Edge to Second Feature: 7.25 ± 0.8
- Distance from Left Edge to Third Feature: 51.5 ± 0.5
- Distance from Left Edge to Fourth Feature: 60.0 ± 0.5
- Distance from Left Edge to Fifth Feature: 55.5 ± 1
- Distance from Left Edge to Sixth Feature: 43.5 ± 1
- Distance from Left Edge to Seventh Feature: 16.5 ± 1
- Distance from Left Edge to Eighth Feature: 12.0 ± 1
- Distance from Left Edge to Ninth Feature: 9.5 ± 0.5
- Distance from Left Edge to Tenth Feature: 2.0 ± 0.5
- Distance from Left Edge to Eleventh Feature: 14.0 ± 1
- Distance from Left Edge to Twelfth Feature: 4.0 ± 0.3
- Distance from Left Edge to Thirteenth Feature: 17.0 ± 0.5

Side View Dimensions:

- Overall Length: (50.4)
- Overall Width: 7.5 ± 0.5 (9.88)
- Distance from Left Edge to First Feature: 3.1 + 0.6/-0.4
- Distance from Left Edge to Second Feature: 0.09 ± 0.02
- Distance from Left Edge to Third Feature: 2.3 ± 0.3
- Distance from Left Edge to Fourth Feature: 7.5 ± 0.5

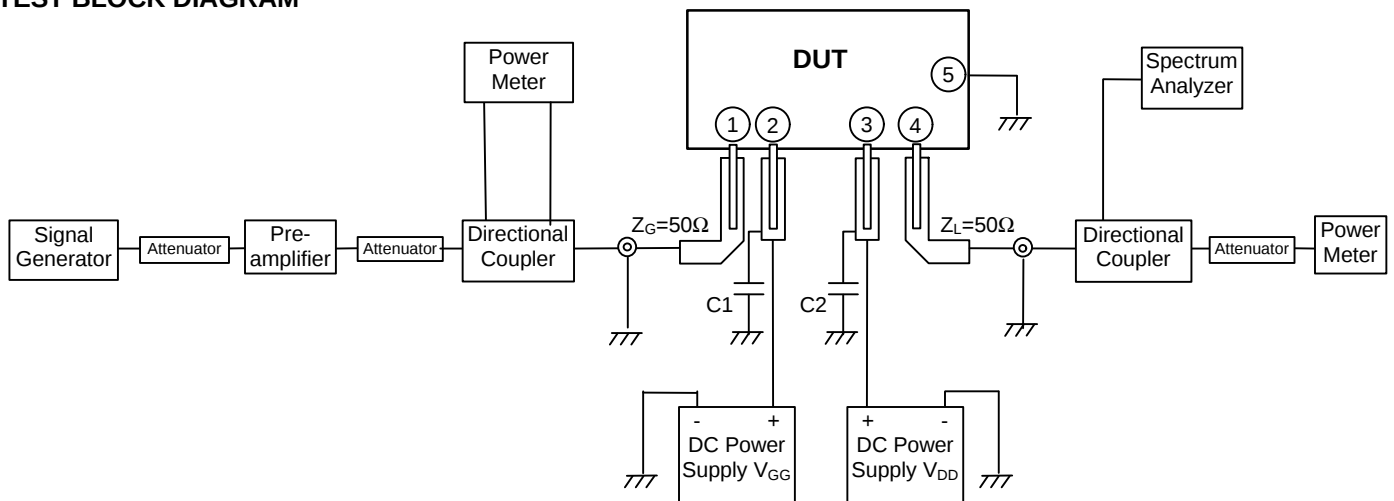
Callouts:

- ①, ②, ③, ④, ⑤: Feature markers.
- 2-R2 ± 0.5: Fillet dimension.
- Ø0.45 ± 0.15: Hole dimension.

- ① RF Input (P_{in})
- ② Gate Voltage (V_{GG})
- ③ Drain Voltage (V_{DD})
- ④ RF Output (P_{out})
- ⑤ RF Ground (Case)



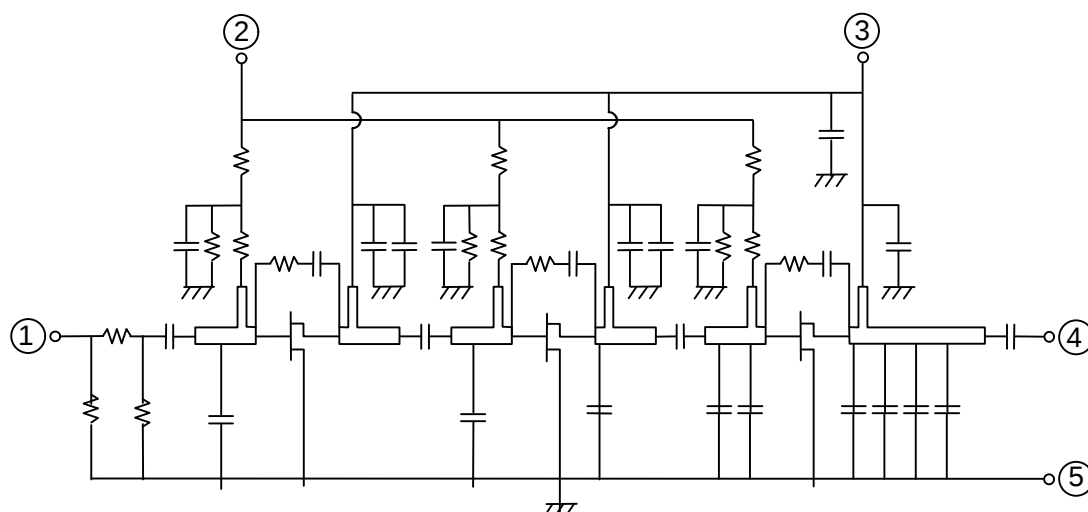
TEST BLOCK DIAGRAM



C1, C2: 4700pF, 22uF in parallel

- ① RF Input (P_{in})
- ② Gate Voltage (V_{GG})
- ③ Drain Voltage (V_{DD})
- ④ RF Output (P_{out})
- ⑤ RF Ground (Case)

EQUIVALENT CIRCUIT





PRECAUTIONS, RECOMMENDATIONS, and APPLICATION INFORMATION:

Construction:

This module consists of an alumina substrate soldered onto a copper flange. For mechanical protection, a plastic cap is attached with silicone. The MOSFET transistor chips are die bonded onto metal, wire bonded to the substrate, and coated with resin. Lines on the substrate (eventually inductors), chip capacitors, and resistors form the bias and matching circuits. Wire leads soldered onto the alumina substrate provide the DC and RF connection.

Following conditions must be avoided:

- Bending forces on the alumina substrate (for example, by driving screws or from fast thermal changes)
- Mechanical stress on the wire leads (for example, by first soldering then driving screws or by thermal expansion)
- Defluxing solvents reacting with the resin coating on the MOSFET chips (for example, Trichlorethylene)
- Frequent on/off switching that causes thermal expansion of the resin
- ESD, surge, overvoltage in combination with load VSWR, and oscillation

ESD:

This MOSFET module is sensitive to ESD voltages down to 1000V. Appropriate ESD precautions are required.

Mounting:

Heat sink flatness must be less than 50 μm (a heat sink that is not flat or particles between module and heat sink may cause the ceramic substrate in the module to crack by bending forces, either immediately when driving screws or later when thermal expansion forces are added).

A thermal compound between module and heat sink is recommended for low thermal contact resistance and to reduce the bending stress on the ceramic substrate caused by the temperature difference to the heat sink.

The module must first be screwed to the heat sink, then the leads can be soldered to the printed circuit board.

M3 screws are recommended with a tightening torque of 0.4 to 0.6 Nm.

Soldering and Defluxing:

This module is designed for manual soldering.

The leads must be soldered after the module is screwed onto the heat sink.

The soldering temperature must be lower than 260°C for a maximum of 10 seconds, or lower than 350°C for a maximum of three seconds.

Ethyl Alcohol is recommend for removing flux. Trichlorethylene solvents must not be used (they may cause bubbles in the coating of the transistor chips which can lift off the bond wires).

Thermal Design of the Heat Sink:

At $P_{\text{out}}=20\text{W}$, $V_{\text{DD}}=12.5\text{V}$ and $P_{\text{in}}=50\text{mW}$ each stage transistor operating conditions are:

Stage	P_{in} (W)	P_{out} (W)	$R_{\text{th(ch-case)}}$ (°C/W)	$I_{\text{DD}} @ \eta_T=25\%$ (A)	V_{DD} (V)
1 st	0.05	1.0	4.5	0.50	12.5
2 nd	1.0	8.0	3.2	1.90	
3 rd	8.0	20.0	1.6	3.90	

The channel temperatures of each stage transistor $T_{\text{ch}} = T_{\text{case}} + (V_{\text{DD}} \times I_{\text{DD}} - P_{\text{out}} + P_{\text{in}}) \times R_{\text{th(ch-case)}}$ are:

$$T_{\text{ch1}} = T_{\text{case}} + (12.5\text{V} \times 0.50\text{A} - 1.0\text{W} + 0.05\text{W}) \times 4.5^\circ\text{C/W} = T_{\text{case}} + 23.9^\circ\text{C}$$

$$T_{\text{ch2}} = T_{\text{case}} + (12.5\text{V} \times 1.90\text{A} - 8.0\text{W} + 1.0\text{W}) \times 3.2^\circ\text{C/W} = T_{\text{case}} + 53.6^\circ\text{C}$$

$$T_{\text{ch3}} = T_{\text{case}} + (12.5\text{V} \times 3.90\text{A} - 20.0\text{W} + 8.0\text{W}) \times 1.6^\circ\text{C/W} = T_{\text{case}} + 58.8^\circ\text{C}$$

For long-term reliability, it is best to keep the module case temperature (T_{case}) below 90°C. For an ambient temperature $T_{\text{air}}=60^\circ\text{C}$ and $P_{\text{out}}=20\text{W}$, the required thermal resistance $R_{\text{th(case-air)}} = (T_{\text{case}} - T_{\text{air}}) / ((P_{\text{out}} / \eta_T) - P_{\text{out}} + P_{\text{in}})$ of the heat sink, including the contact resistance, is:

$$R_{\text{th(case-air)}} = (90^\circ\text{C} - 60^\circ\text{C}) / (20\text{W}/25\% - 20\text{W} + 0.05\text{W}) = 0.50^\circ\text{C/W}$$

When mounting the module with the thermal resistance of 0.50 °C/W, the channel temperature of each stage transistor is:

$$T_{\text{ch1}} = T_{\text{air}} + 53.9^\circ\text{C}$$

$$T_{\text{ch2}} = T_{\text{air}} + 83.6^\circ\text{C}$$

$$T_{\text{ch3}} = T_{\text{air}} + 88.8^\circ\text{C}$$

The 175°C maximum rating for the channel temperature ensures application under derated conditions.



Output Power Control:

Depending on linearity, the following two methods are recommended to control the output power:

a) Non-linear FM modulation:

By the gate voltage (V_{GG}).

When the gate voltage is close to zero, the RF input signal is attenuated up to 60 dB and only a small leakage current flows from the battery into the drain.

Around $V_{GG}=4V$, the output power and drain current increases substantially.

Around $V_{GG}=4.5V$ (typical) to $V_{GG}=5V$ (maximum), the nominal output power becomes available.

b) Linear AM modulation:

By RF input power P_{in} .

The gate voltage is used to set the drain's quiescent current for the required linearity.

Oscillation:

To test RF characteristics, this module is put on a fixture with two bias decoupling capacitors each on gate and drain, a 4.700 pF chip capacitor, located close to the module, and a 22 μF (or more) electrolytic capacitor.

When an amplifier circuit around this module shows oscillation, the following may be checked:

a) Do the bias decoupling capacitors have a low inductance pass to the case of the module?

b) Is the load impedance $Z_L=50\Omega$?

c) Is the source impedance $Z_G=50\Omega$?

Frequent on/off switching:

In base stations, frequent on/off switching can cause thermal expansion of the resin that coats the transistor chips and can result in reduced or no output power. The bond wires in the resin will break after long-term thermally induced mechanical stress.

Quality:

Mitsubishi Electric is not liable for failures resulting from base station operation time or operating conditions exceeding those of mobile radios.

This module technology results from more than 20 years of experience, field proven in tens of millions of mobile radios. Currently, most returned modules show failures such as ESD, substrate crack, and transistor burnout, which are caused by improper handling or exceeding recommended operating conditions. Few degradation failures are found.

Keep safety first in your circuit designs!

Mitsubishi Electric Corporation puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur. Trouble with semiconductors may lead to personal injury, fire or property damage. Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of non-flammable material, or (iii) prevention against any malfunction or mishap.

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