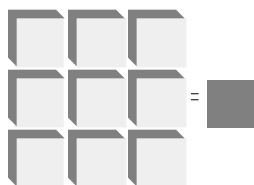


# LSI/CSI



## RED SERIES



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### COMPLEMENTARY MOS (CMOS) DIVIDER

#### RED SERIES

RED 5/6	Divide by 5 or 6
RED 50/60	Divide by 50 or 60
RED 100/120	Divide by 100 or 120
RED 300/360	Divide by 300 or 360
RED 500/600	Divide by 500 or 600
RED 3000/3600	Divide by 3000 or 3600

#### FEATURES:

- Clock input pulse shaper accepts 50 Hz/60 Hz sine wave directly
- Fully static counter operation
- +4.5V to +15 V operation ( $V_{DD}$ - $V_{SS}$ )
- Low power dissipation
- High noise immunity
- Reset
- Input Enable
- 50/Hz/60 Hz division select input
- Output low power TTL compatible at +4.5V operation
- Square Wave Output (except for  $\div 5$ )
- 8 Pin DIP (See Figure 1)

#### APPLICATION:

Time base generator from either 50 Hz or 60 Hz line frequency to produce:

10 pulses per second	(RED 5/6)
1 pulse per second	(RED 50/60)
1 pulse per 2 seconds	(RED 100/120)
1 pulse per .1 minute	(RED 300/360)
1 pulse per 10 seconds	(RED 500/600)
1 pulse per minute	(RED 3000/3600)

#### DESCRIPTION OF OPERATION:

The counter advances by one on each negative transition of the input clock pulse as long as the Enable signal is "High" and the Reset signal is "Low". When the Enable signal is "Low" the input clock pulses will be inhibited and the counter will be held at the state it was in prior to bringing the Enable "Low". A "High" Reset signal clears the counter to zero count.

Depending on the device used, a "Low" on the Division Select input will cause a Divide by 6, 60, 120, 360, 600 or 3600. A "High" on the Division Select will cause a Divide by 5, 50, 100, 300, 500 or 3000.

#### PIN ASSIGNMENT - TOP VIEW STANDARD 8 PIN PLASTIC DIP

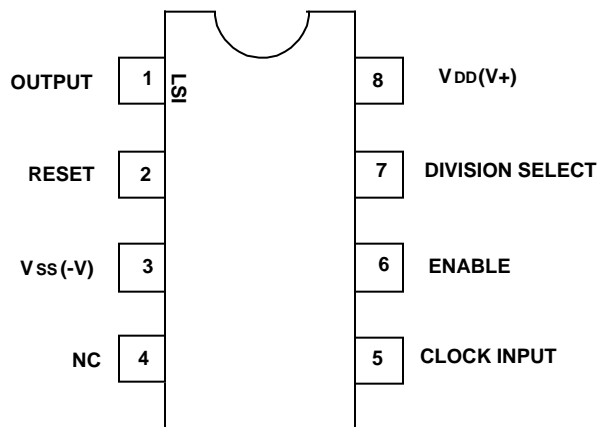


FIGURE 1

#### MARKING AS FOLLOWS:

PART	MARKING
RED 5/6	RED 6
RED 50/60	RED 60
RED 100/120	RED 120
RED 300/360	RED 360
RED 500/600	RED 600
RED 3000/3600	RED 3600

#### MAXIMUM RATINGS:

PARAMETER	SYMBOL	VALUE	UNIT
Storage Temperature	TSTG	-65 to +150	°C
Operating Temperature	TA	-40 to +85	°C
DC Supply Voltage	( $V_{DD}$ - $V_{SS}$ )	+18	Volts
Voltage at any input	VIN	$V_{SS} -.5$ to $V_{DD} +.5$	Volts

The information included herein is believed to be accurate and reliable. However, LSI Computer Systems, Inc. assumes no responsibilities for inaccuracies, nor for any infringements of patent rights of others which may result from its use.

