

Data Sheet January 2002

## 8A, 400V - 600V Ultrafast Dual Diodes

The RURP840CC and RURP860CC are ultrafast dual diodes with soft recovery characteristics ( $t_{rr}$  < 60ns). They have low forward voltage drop and are silicon nitride passivated ion-implanted epitaxial planar construction.

These devices are intended for use as freewheeling/clamping diodes and rectifiers in a variety of switching power supplies and other power switching applications. Their low stored charge and ultrafast soft recovery minimize ringing and electrical noise in many power switching circuits reducing power loss in the switching transistors.

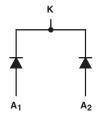
Formerly developmental type TA09616.

# **Ordering Information**

PART NUMBER	PACKAGE	BRAND
RURP840CC	TO-220AB	RURP840C
RURP860CC	TO-220AB	RURP860C

NOTE: When ordering, use the entire part number.

# Symbol



### **Features**

•	Ultrafast with Soft Recovery	<60ns
•	Operating Temperature1	75°C
•	Reverse Voltage	600V

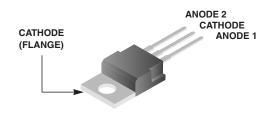
- · Avalanche Energy Rated
- Planar Construction

# **Applications**

- · Switching Power Supplies
- · Power Switching Circuits
- · General Purpose

## **Packaging**

**JEDEC TO-220AB** 



<b>Absolute Maximum Ratings</b> (Per Leg) T <sub>C</sub> = 25°C, Unless Otherwise Specified			
	RURP840CC	RURP860CC	UNITS
Peak Repetitive Reverse Voltage	400	600	V
Working Peak Reverse Voltage	400	600	V
DC Blocking Voltage	400	600	V
Average Rectified Forward Current	8	8	Α
Repetitive Peak Surge Current	16	16	Α
Nonrepetitive Peak Surge Current	100	100	Α
Maximum Power Dissipation	75	75	W
Avalanche Energy (See Figures 10 and 11)	20	20	mJ
Operating and Storage Temperature	-65 to 175	-65 to 175	οС

### RURP840CC, RURP860CC

**Electrical Specifications** (Per Leg)  $T_C = 25^{\circ}C$ , Unless Otherwise Specified

			RURP840CC		RURP860CC			
SYMBOL	TEST CONDITION	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
V <sub>F</sub>	I <sub>F</sub> = 8A	-	-	1.3	-	-	1.5	V
	I <sub>F</sub> = 8A, T <sub>C</sub> = 150 <sup>o</sup> C	-	-	1.0	-	-	1.2	V
I <sub>R</sub>	V <sub>R</sub> = 400V	-	-	100	-	-	-	μΑ
	V <sub>R</sub> = 600V	-	-	-	-	-	100	μА
	V <sub>R</sub> = 400V, T <sub>C</sub> = 150°C	-	-	500	-	-	-	μΑ
	V <sub>R</sub> = 600V, T <sub>C</sub> = 150°C	-	-	-	-	-	500	μА
t <sub>rr</sub>	$I_F = 1A$ , $dI_F/dt = 200A/\mu s$	-	-	60	-	-	60	ns
	$I_F = 8A$ , $dI_F/dt = 200A/\mu s$	-	-	70	-	-	70	ns
ta	$I_F = 8A$ , $dI_F/dt = 200A/\mu s$	-	32	-	-	32	-	ns
t <sub>b</sub>	$I_F = 8A$ , $dI_F/dt = 200A/\mu s$	-	21	-	-	21	-	ns
Q <sub>RR</sub>	$I_F = 8A$ , $dI_F/dt = 200A/\mu s$	-	195	-	-	195	-	nC
СЈ	V <sub>R</sub> = 10V, I <sub>F</sub> = 0A	-	25	-	-	25	-	pF
$R_{ heta JC}$		-	-	2	-	-	2	°C/W

#### **DEFINITIONS**

 $V_F$  = Instantaneous forward voltage (pw = 300 $\mu$ s, D = 2%).

I<sub>R</sub> = Instantaneous reverse current.

 $t_{rr}$  = Reverse recovery time (See Figure 9), summation of  $t_a$  +  $t_b$ .

 $t_a$  = Time to reach peak reverse current (See Figure 9).

 $t_b$  = Time from peak  $I_{RM}$  to projected zero crossing of  $I_{RM}$  based on a straight line from peak  $I_{RM}$  through 25% of  $I_{RM}$  (See Figure 9).

Q<sub>RR</sub> = Reverse recovery charge.

C<sub>.1</sub> = Junction Capacitance.

 $R_{\theta JC}$  = Thermal resistance junction to case.

pw = pulse width.

D = duty cycle.

# **Typical Performance Curves**

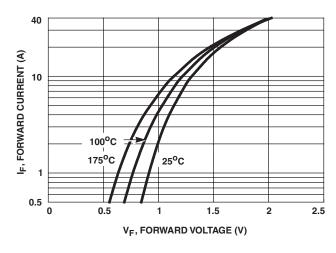


FIGURE 1. FORWARD CURRENT vs FORWARD VOLTAGE

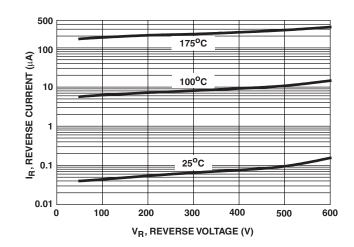


FIGURE 2. REVERSE CURRENT vs REVERSE VOLTAGE

# Typical Performance Curves (Continued)

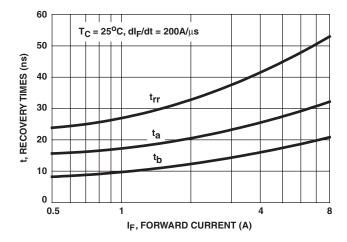


FIGURE 3. t<sub>rr</sub>, t<sub>a</sub> AND t<sub>b</sub> CURVES vs FORWARD CURRENT

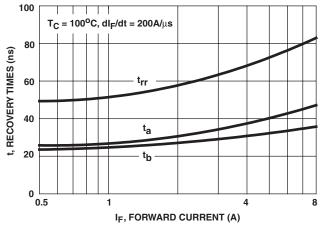


FIGURE 4. t<sub>rp</sub>, t<sub>a</sub> AND t<sub>b</sub> CURVES vs FORWARD CURRENT

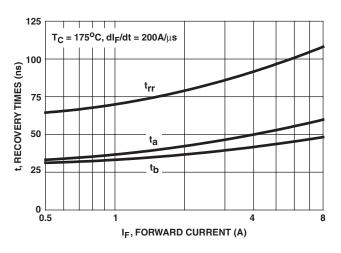


FIGURE 5.  $t_{rr}$ ,  $t_a$  AND  $t_b$  CURVES vs FORWARD CURRENT

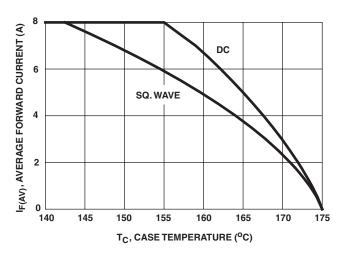


FIGURE 6. CURRENT DERATING CURVE

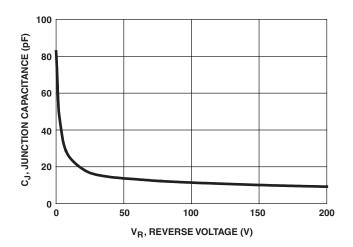


FIGURE 7. JUNCTION CAPACITANCE vs REVERSE VOLTAGE

©2002 Fairchild Semiconductor Corporation RURP840CC, RURP860CC Rev. B

### Test Circuits and Waveforms

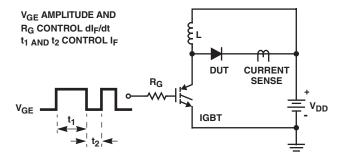


FIGURE 8. t<sub>rr</sub> TEST CIRCUIT

I = 1A L = 40mH  $R < 0.1\Omega$   $E_{AVL} = 1/2LI^2 \left[ V_{R(AVL)} / (V_{R(AVL)} - V_{DD}) \right]$   $Q_1 = IGBT \left( BV_{CES} > DUT \, V_{R(AVL)} \right)$   $V_{DD}$   $V_{DD}$   $V_{DD}$ 

FIGURE 10. AVALANCHE ENERGY TEST CIRCUIT

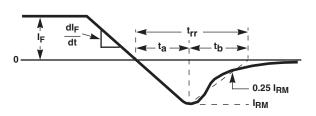


FIGURE 9. t<sub>rr</sub> WAVEFORMS AND DEFINITIONS

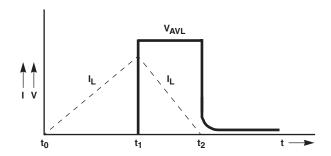


FIGURE 11. AVALANCHE CURRENT AND VOLTAGE WAVEFORMS

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### **Definition of Terms**

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