



# R14K-ST11 Four Port RJ Format Optical Transceiver for Gigabit Ethernet and Fibre Channel

3.3 Volts, 850nm VCSEL, Multi-Mode at up to 1.5 Gbaud, up to 550 meters

## FEATURES

- Four Port RJ Format optical transceiver - duplex LC interface
- Optimized for 62.5 or 50/125μ multimode optical fiber
- Compatible with wave solder and aqueous wash processes
- Enables reuse of existing RJ-45 UTP equipment cabinets
- Overall metal shield with enhanced grounding tabs
- Full compliance to IEEE and ANSI requirements
- Differential LVPECL inputs and outputs
- Single +3.3 V power supply per port

## APPLICATIONS

The R14K-ST11 multimode glass optical fiber transceivers provide low profile, cost effective solutions for high data rate, multimode (up to 1.5 Gigabaud, up to 550 Meters) optical fiber data links with a multimode duplex LC connector interface.

These transceivers are fully compliant with the IEEE Gigabit Ethernet and ANSI Fibre Channel standards but can be used for any other data communications purpose within their operating parameters.

## DESCRIPTION

The R14K-ST11 fiber optic transceivers consist of four transmitter and receiver functions integrated into a four port RJ Format harmonica module. The optical transmitters are high output 850nm VCSELs. The transmitter input lines are driven with differential LVPECL signals applied to the Transmit (TX+ and TX-) pins. These signals are internally converted to suitable modulation currents by CMOS integrated circuits. The optical transmitters can be disabled by applying LVTTTL signals to the Transmit Disable (TDIS) pins.

The optical receivers consist of PIN and Preamplifier assemblies and CMOS limiting post-amplifier integrated circuits. Outputs from the receivers consist of differential LVPECL data signals on the Receive (RX+ and RX-) pins and a single ended LVTTTL signal detect functions on the Signal Detect (SD) pins.

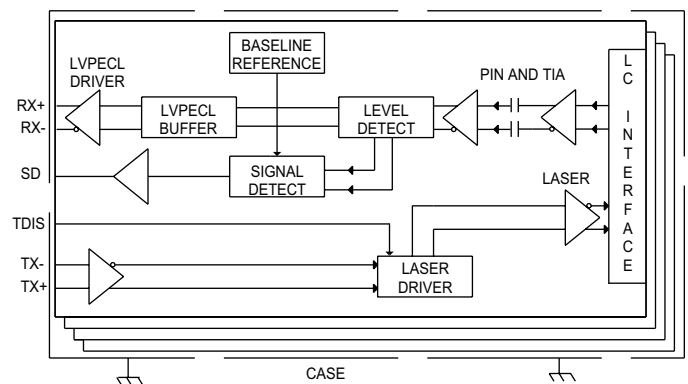
## ORDERING INFORMATION

Application	Description	Temp. Range	Part Number
Gigabit Ethernet Fibre Channel	1000Base-SX ANSI X3.297	0 to +70° C	R14K-ST11
Gigabit Ethernet Fibre Channel	1000Base-SX ANSI X3.297	-40 to +85° C	R14K-ST11H

## TRANSMISSION DISTANCES

Application	Media	Distance
Gigabit Ethernet IEEE - 802.3z	62.5/125 - 160MHz*Km	220M
	62.5/125 - 200MHz*Km	275M
	50/125 - 400MHz*Km	500M
	50/125 - 500MHz*Km	550M
Fibre Channel ANSI X3.297	62.5/125 - 160MHz*Km	300M
	50/125 - 500MHz*Km	500M

## BLOCK DIAGRAM



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## ABSOLUTE MAXIMUM RATINGS

Absolute maximum limits mean that no catastrophic damage will occur if the product is subjected to these ratings for short periods, provided each limiting parameter is in isolation and all other parameters have values within the performance specification. It should not be assumed that limiting values of more than one parameter can be applied to the product at the same time.

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Storage Temperature	$T_S$	-55		+100	° C
Lead Soldering Temperature	$T_{SOLD}$			+260	° C
Lead Soldering Time	$t_{SOLD}$			10	Seconds
Supply Voltage	$V_{CC}$	-0.5		+6.0	V
Data Input Voltage	$V_I$	-0.5		$V_{CC}$	V
Differential Input Voltage (p-p)	$V_D$			2.0	V
Output Current	$I_O$			50	mA

## RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Operating Temperature Limit Part Number xxx-xxxx Part Number xxx-xxxxH	$T_A$	0 -40		+70 +85	° C
Supply Voltage	$V_{CC}$	+3.135		+3.465	V
TX Differential Input Voltage (p-p)	$V_D$	0.350	0.800	1.250	V
Transmit Disable Voltage	$V_{TD}$	$V_{CC} - 1.3$		$V_{CC}$	V
Transmit Enable Voltage	$V_{TEN}$	$V_{EE}$		$V_{EE} + 0.8$	V
RX Data Output Load	$R_L$		50		Ohms

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## TRANSMITTERS

T<sub>A</sub> = Operating Temperature Range, VCCTX = 3.15V to 3.45V

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Baud Rate		0.25		1.5	GBaud
Optical Output Power <sup>1</sup>	P <sub>O</sub>	-9.5		-4.0	dBm
Optical Output Wavelength	λ <sub>OUT</sub>	830	850	860	nm
Spectral Width (RMS)	Δλ <sub>RMS</sub>			0.85	nm
Extinction Ratio	ER	9			dB
Optical Rise / Fall Time (20% - 80%)	t <sub>R</sub> , t <sub>F</sub>			0.26	nS
Relative Intensity Noise	RIN			-117	dB / Hz
Total Jitter	TJ			227	pS
Coupled Power Ratio	CPR	9			dB

1. BER=10<sup>-12</sup> @ 1.25 Gbaud, PRBS 2<sup>7</sup>-1, NRZ, Compliant with IEEE 802.3z and ANSI X3.297

## RECEIVERS

T<sub>A</sub> = Operating Temperature Range, VCCR<sub>X</sub> = 3.15V to 3.45V

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Baud Rate		0.25		1.5	GBaud
Optical Wavelength	λ <sub>IN</sub>	770		860	nm
Optical Sensitivity <sup>1</sup>	P <sub>I</sub>	-17		0	dBm
Optical Return Loss	ORL	12	30		dB
Signal Detect Asserted	P <sub>a</sub>			-17	dBm
Signal Detect Deasserted	P <sub>d</sub>	-29			dBm
Signal Detect Hysteresis	P <sub>a</sub> - P <sub>d</sub>	1.5			dB
Signal Detect Assert Time	t <sub>Assert</sub>		<10	100	μS
Signal Detect Deassert Time	t <sub>Deassert</sub>		<10	350	μS

1. BER=10<sup>-12</sup> @ 1.25 Gbaud, PRBS 2<sup>7</sup>-1, NRZ, Compliant with IEEE 802.3z and ANSI X3.297

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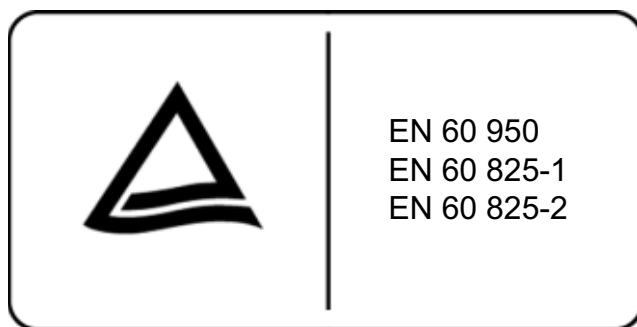
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## REGULATORY COMPLIANCE

Requirement	Feature	Condition	Notes
MIL-STD-883-3015.7	ESD	Class II	2200V
IEC-801-2	ESD	Human Body Model	25KV
IEC-801-3	EMI	Immunity	10V/M
FCC	EMI	Class B	>20dB
EN 55022 (CISPR 22A)	EMI	Class B	10V/M
IEC-825 Issue 1993-11	Eye Safety	Class 1	TUV Certificate Number R 2171007
FDA CDRH 21-CFR 1040	Eye Safety	Class 1	CDRH Accession Number 9930009



File Number: E209124



File Number: R 2071007

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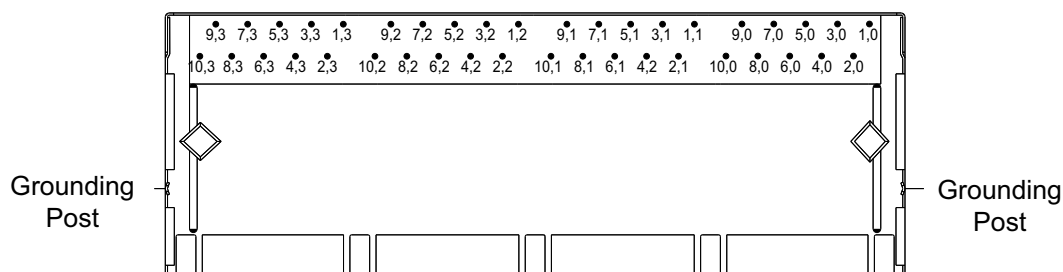
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## PIN NUMBER ASSIGNMENTS

Bottom View of Component

For Ports 0 to N

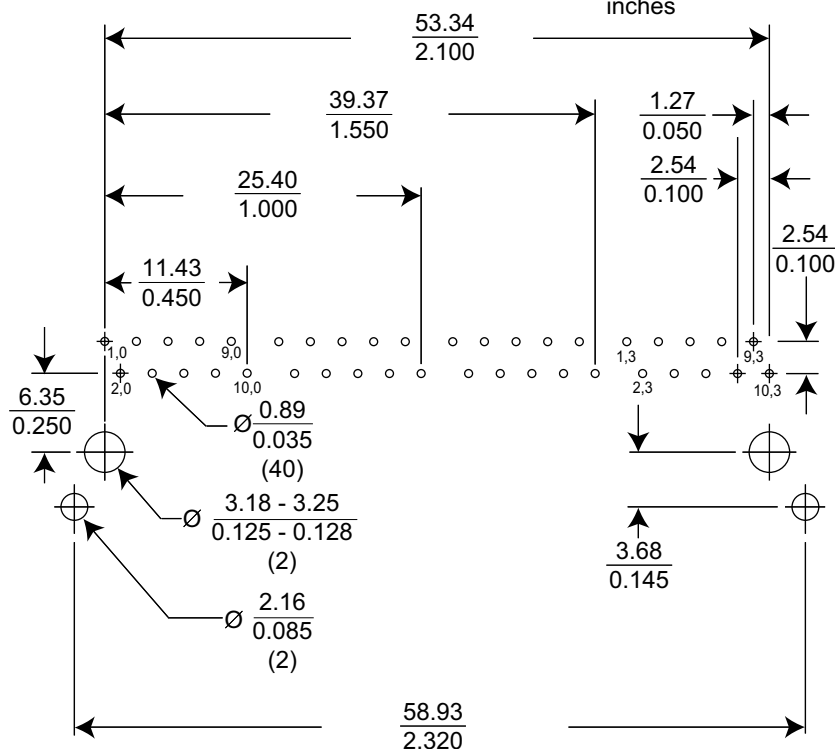
Pin Assignment (Pin #, Port #)



## RECOMMENDED PCB HOLE LAYOUT

Top Side View

Dimensions Are Shown As:  $\frac{\text{mm}}{\text{inches}}$



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## PIN FUNCTIONS

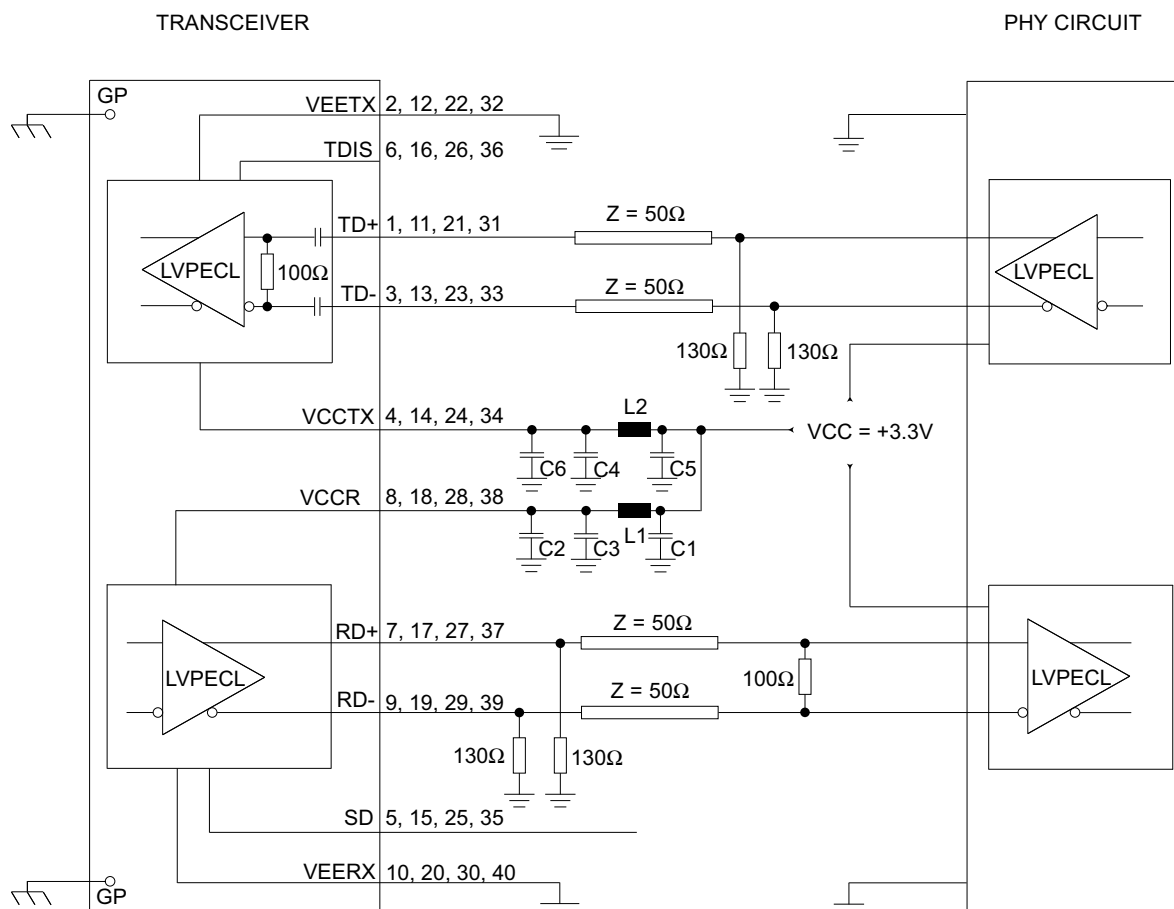
Multiport Transceivers for Port Numbers 0 to N

Pin Number (Pin #, Port #)	Symbol (Port #)	Description	Logic Family
GP	GP	Grounding Post <i>Connect to Chassis Ground</i>	N/A
(1, 0)...(1, N)	TXD + (0:N)	Transmitter DATA In	LVPECL
(2, 0)...(2, N)	VEETX (0:N)	Transmitter Signal Ground	N/A
(3, 0)...(3, N)	TXD - (0:N)	Transmitter DATA In	LVPECL
(4, 0)...(4, N)	VCCTX (0:N)	Transmitter Power Supply	N/A
(5, 0)...(5, N)	SD (0:N)	Signal Detect <i>Satisfactory Optical Output: Logic "1" Output</i> <i>Fault Condition: Logic "0" Output</i>	LVTTTL
(6, 0)...(6, N)	TDIS (0:N)	Transmitter Disable	LVTTTL
(7, 0)...(7, N)	RXD + (0:N)	Receiver DATA Out	LVPECL
(8, 0)...(8, N)	VCCR <sub>X</sub> (0:N)	Receiver Power Supply	N/A
(9, 0)...(9, N)	RXD - (0:N)	Receiver DATA Out	LVPECL
(10, 0)...(10, N)	VEER <sub>X</sub> (0:N)	Receiver Signal Ground	N/A

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## TRANSCEIVER APPLICATION SCHEMATIC For Interface To +3.3V LVPECL Circuits



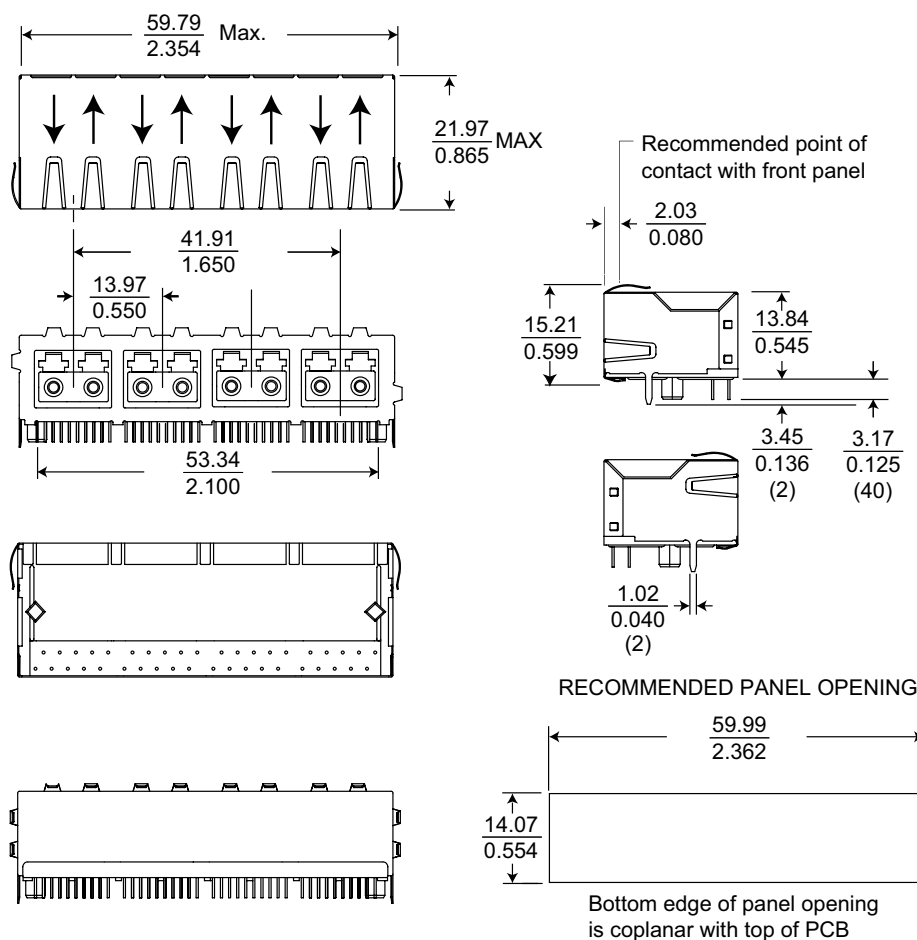
L1, L2 = MuRata BLM21A601S or Equivalent  
C1, C2, C6 = 10.0nF  
C3, C4, C5 = 4.7μF to 10.0μF

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## TRANSCEIVER OUTLINE DRAWING

Dimensions Shown As:  $\frac{\text{mm}}{\text{inches}}$



### IMPORTANT NOTICE

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