

The schematic diagram illustrates a 6-channel differential signal conditioning circuit. Each channel is composed of an instrumentation amplifier (INA128), a precision centertap diode (Th), and a precision rectifier (diodes and op-amp). The input stage includes a resistor network (Rfo) and a feedback network (Rfo=1.5kΩ). The output stage includes a resistor network (Rfo) and a feedback network (Rfo=1.5kΩ). The circuit is powered by a 5V supply and ground.

Symbol	Parameter	Condition	Ratings	Unit
V _D	Supply Voltage	Applied between : V _{UP1} -V _{UPC} V _{VP1} -V _{VPc} , V _{WP1} -V _{WPC} , V _{N1} -V _{Nc}	20	V
V _{CIN}	Input Voltage	Applied between : U _P -V _{UPC} , V _P -V _{VPc} , W _P -V _{WPC} U _N • V _N • W _N -V _{Nc}	20	V
V _{FO}	Fault Output Supply Voltage	Applied between : U _{FO} -V _{UPC} , V _{FO} -V _{VPc} , W _{FO} -V _{WPC} Fo-V _{Nc}	20	V
I _{FO}	Fault Output Current	Sink current at U _{FO} , V _{FO} , W _{FO} and Fo terminal	20	mA

PM200CVA060**FLAT-BASE TYPE
INSULATED PACKAGE****TOTAL SYSTEM**

Symbol	Parameter	Condition	Ratings	Unit
V _{CC(Prot)}	Supply Voltage Protected by SC	V _D = 13.5 ~ 16.5V, Inverter Part, T _j = 125°C Start	400	V
V _{CC(surge)}	Supply Voltage (Surge)	Applied between : P-N, Surge value or without switching	500	V
T _C	Module Case Operating Temperature		-20 ~ +100	°C
T _{stg}	Storage Temperature		-40 ~ +125	°C
V _{iso}	Isolation Voltage	60Hz, Sinusoidal, Charged part to Base, AC 1 min.	2500	V _{rms}

ELECTRICAL CHARACTERISTICS (T_j = 25°C, unless otherwise noted)**INVERTER PART**

Symbol	Parameter	Test Condition		Limits			Unit
				Min.	Typ.	Max.	
V _{CE(sat)}	Collector-Emitter Saturation Voltage	V _D = 15V, I _C = 200A	T _J = 25°C	—	2.35	2.80	V
		V _{CIN} = 0V	T _J = 125°C	—	2.55	3.05	
V _{EC}	FWDi Forward Voltage	-I _C = 200A, V _D = 15V, V _{CIN} = 5V		—	2.20	3.30	V
t _{on}	Switching Time	V _D = 15V, I _{CIN} = 0V↔5V V _{CC} = 300V, I _C = 200A T _J = 125°C Inductive Load (upper and lower arm)		0.4	0.8	2.1	μs
t _{rr}				—	0.2	0.3	
t _{c(on)}				—	0.3	1.1	
t _{off}				—	1.8	2.9	
t _{c(off)}				—	0.6	1.2	
I _{CES}	Collector-Emitter Cutoff Current	V _{CE} = V _{CES} , V _D = 15V	T _J = 25°C	—	—	1	mA
			T _J = 125°C	—	—	10	

CONTROL PART

Symbol	Parameter	Test Condition		Limits			Unit
				Min.	Typ.	Max.	
Id	Circuit Current	VD = 15V, VCIN = 5V	VN1-VNC	—	40	55	mA
			VXP1-VXPC	—	13	18	
Vth(ON)	Input ON Threshold Voltage	Applied between : UP-VUPC, VP-VVPC, WP-VWPC UN • VN • WN-VNC		1.2	1.5	1.8	V
Vth(OFF)	Input OFF Threshold Voltage			1.7	2.0	2.3	
SC	Short Circuit Trip Level	−20≤ TJ ≤ 125°C, VD = 15V		310	—	—	A
toff(SC)	Short Circuit Current Delay Time	VD = 15V		—	10	—	μs
OT	Over Temperature Protection	Base-plate	Trip level	111	118	125	°C
OTr		Temperature detection, VD = 15V	Reset level	—	100	—	
UV	Supply Circuit Under-Voltage Protection	Tj ≤ 125°C	Trip level	11.5	12.0	12.5	V
UVr			Reset level	—	12.5	—	
I _{FO} (H)	Fault Output Current (Note-1)	VD = 15V, VFO = 15V		—	—	0.01	mA
I _{FO} (L)				—	10	15	
tFO	Minimum Fault Output Pulse Width (Note-1)	VD = 15V		1.0	1.8	—	ms

(Note-1) Fault output is given only when the internal SC, OT & UV protection.
 Fault output of OT protection operate by lower arm
 Fault output of OT, UV protection given pulse while over level.

PM200CVA060

**FLAT-BASE TYPE
INSULATED PACKAGE**

THERMAL RESISTANCES

Symbol	Parameter	Test Condition	Limits			Unit
			Min.	Typ.	Max.	
R _{th(j-c)Q}	Junction to case Thermal	Inverter IGBT part (per 1/6 module)	—	—	0.231	°C/W
R _{th(j-c)F}	Resistances	Inverter FWDi part (per 1/6 module)	—	—	0.35	
R _{th(c-f)}	Contact Thermal Resistance	Case to fin, Thermal grease applied (per 1 module)	—	—	0.025	

MECHANICAL RATINGS AND CHARACTERISTICS

Symbol	Parameter	Test Condition	Limits			Unit
			Min.	Typ.	Max.	
—	Mounting torque	Mounting part screw : M5	2.5	3.0	3.5	N • m
—	Mounting torque	Main terminal screw : M5	2.5	3.0	3.5	N • m
—	Weight		—	730	—	g

RECOMMENDED CONDITIONS FOR USE

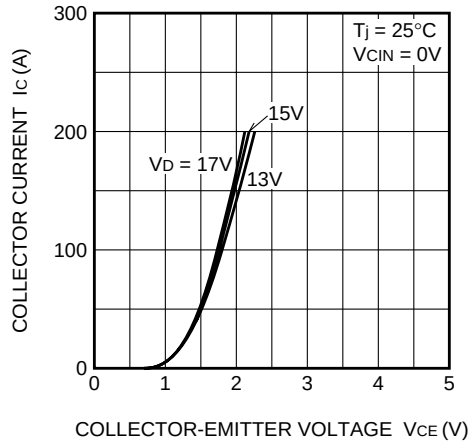
Symbol	Parameter	Test Condition	Recommended value	Unit
V _{CC}	Supply Voltage	Applied across P-N terminals	≤ 400	V
V _D	Control Supply Voltage	Applied between : V _{UP1} -V _{UPC} , V _{VP1} -V _{VPC} V _{WP1} -V _{WPC} , V _{N1} -V _{NVC}	15 ± 1.5	V
V _{CIN(ON)}	Input ON Voltage	Applied between : U _P -V _{UPC} , V _P -V _{VPC} , W _P -V _{WPC} U _N • V _N • W _N -V _{NVC}	≤ 0.8	V
V _{CIN(OFF)}	Input OFF Voltage		≥ 4.0	
t _{dead}	Arm Shoot-through Blocking Time	For IPM's each input signals	≥ 2.5	μs
f _{PWM}	PWM Input Frequency	Using Application Circuit input signal of IPM, 3φ Sinusoidal PWM VVVF inverter	≤ 20	kHz

PM200CVA060

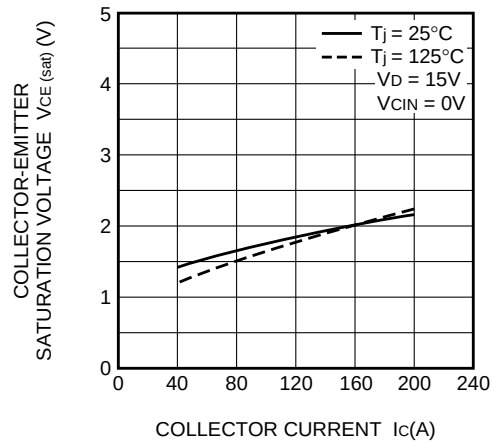
FLAT-BASE TYPE
INSULATED PACKAGE

PERFORMANCE CURVES

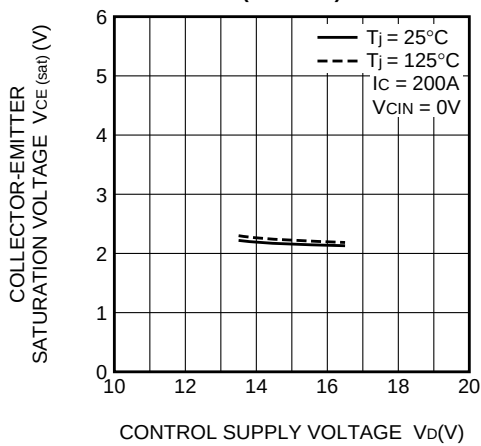
**OUTPUT CHARACTERISTICS
(TYPICAL)**



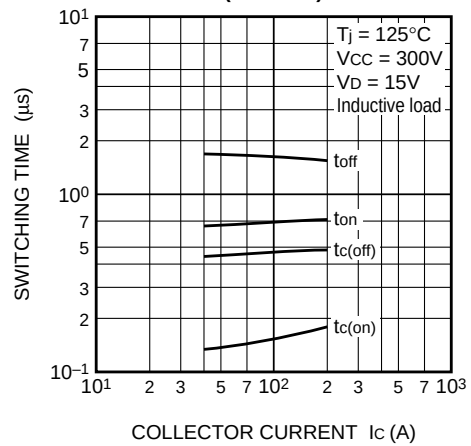
**COLLECTOR-EMITTER SATURATION
VOLTAGE CHARACTERISTICS
(TYPICAL)**



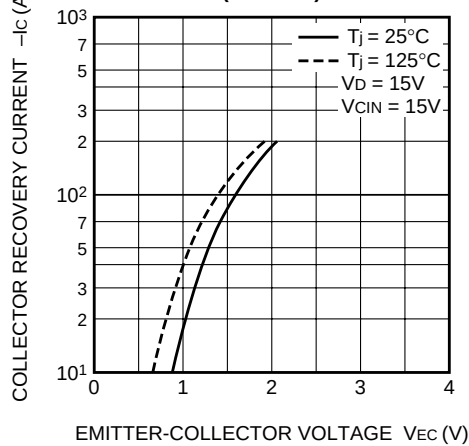
**COLLECTOR-EMITTER SATURATION
VOLTAGE CHARACTERISTICS
(TYPICAL)**



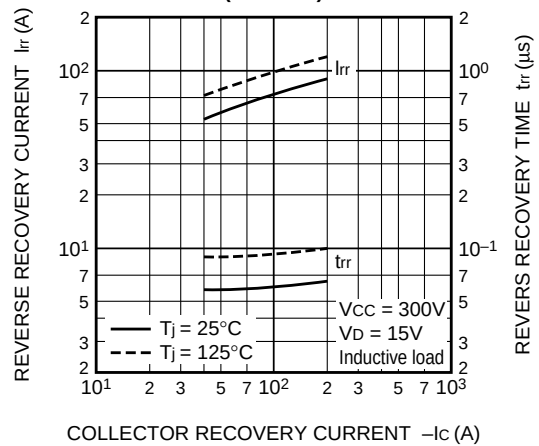
**SWITCHING CHARACTERISTICS
(TYPICAL)**



**DIODE FORWARD CHARACTERISTICS
(TYPICAL)**



**DIODE REVERSE RECOVERY CHARACTERISTICS
(TYPICAL)**



PM200CVA060

FLAT-BASE TYPE
INSULATED PACKAGE

