

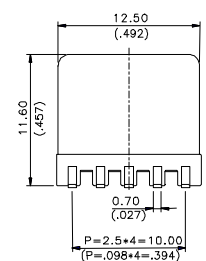
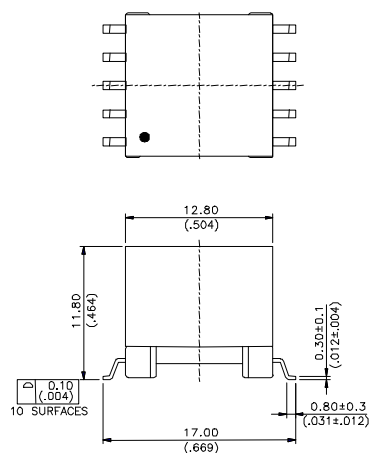
Feature

- ◆ Recommended by Analog Devices for use with their AD20msp930 ADSL chipset.
- ◆ Excellent THD performance in a small footprint.

Specifications

Parameter	Condition		Min.	Typ	Max.	Unit
Turn ratio	Pin(1-4):Pin(10-7)			1:1.2		±2%
Inductance	Pin(1-4) tie (2-3)	@10 KHz , 100mV	1.75			mH
	Pin(10-7) tie (8-9)		2.42			mH
Leakage Inductance	Pin(1-4) with 3-2,8-9,10-7 shorted	@100KHz , 100mV			7.5	μH
DC Resistance	Pin(1-4)	@10 KHz , 100mV			1.3	Ω
	Pin(10-7)				1.5	Ω
Longitudinal Balance		@10KHz~1.1MHz	50			dB
THD		@30KHz		-80		dB
Insertion loss		@300KHz			0.5	dB
Frequency Response		@30KHz to 1.1MHz			±1.0	dB
Hi-Pot	Chip to line	1 minute	1500			Vrms
Load Resistance	Chip side			100		ohms

Dimension



TOLERANCES $\pm 0.25\text{mm}$
($\pm .010\text{inch}$)

Schematic

