

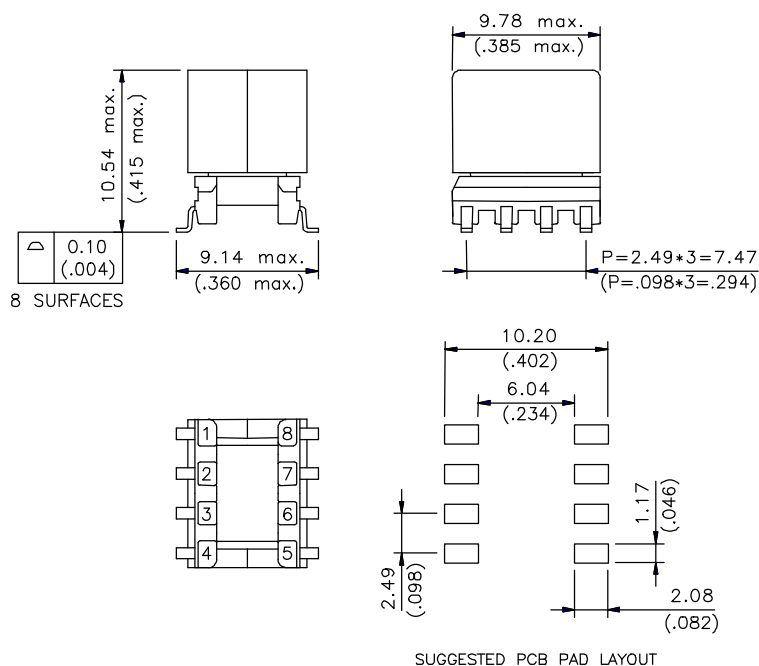
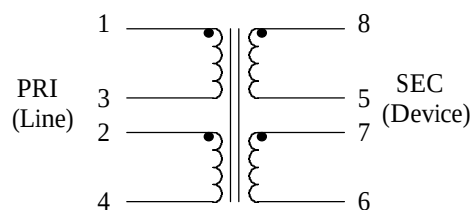
Feature :

- ◆ Designed to comply with the following requirements as defined by IEC950, EN60950, UL1950/CSA950 and AS/NZS3260:
 - Supplementary insulation for a primary circuit at a working voltage of 250Vrms.
- ◆ Operating temperature range : 0°C to +70°C

Specifications @ 25°C

Parameters	Condition	Min.	Typ	Max.	Unit
Turns ratio	Pin (1-4) : (8-6) tie(2+3,5+7)		3 : 1±1.5%		
Inductance	@10KHz,100mV Pin (1-4) tie(2+3)		1.2±10%		mH
Leakage Inductance	@100KHz,100mV Pin (8-6) tie(2+3, 5+7, 1+4)			4	μH
Interwinding Capacitance	@10KHz,1V Pin(1-8) tie(2+3, 5+7)			30	pF
DCR	Pin(1-3), (2-4)		0.64±10%		Ω
	Pin(8-5), (7-6)		0.58±10%		Ω
Isolation Voltage	Pin(1-8) tie(2+3, 5+7)	1875			Vrms
Longitudinal Balance	@20KHz-1.1MHz, per ITU Method(L->M)	-46			dB
THD	*Note			-80	dB

Note : @40KHz, 5.3Vrms across PRI, 11 ohm load, 100 ohm input, tie(2+3,5+7)

Dimension

Schematic


Unless otherwise specified , all tolerances are mm(inch) ± 0.25(0.01)