

T3/E3/DS3/STS-1 Interface Transformers

Feature

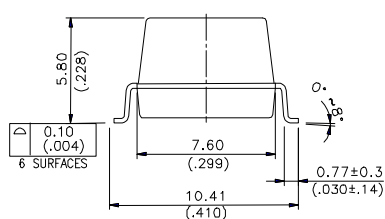
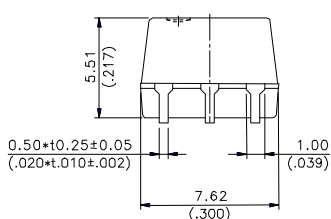
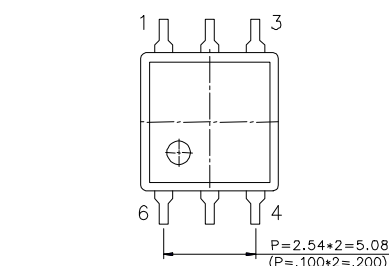
- ◆ Designed for T3/E3/DS3/STS-1interface.
- ◆ Designed to meet IR 235⁰C Peak Requirement.
- ◆ 1500Vrms isolation.
- ◆ Operating temperature of –40 ⁰C to + 85⁰C.

Specifications

Parameter	Condition		Min.	Typ.	Max.	Unit
Turn Ratio	Pin(4-6) : (3-1)			1 :2CT±2%		
Inductance	Pin(4-6)	10KHz , 100mV @25 ⁰ C	40			μH
	Pin(4-6)	10KHz , 100mV @-40 ⁰ C	19			μH
Leakage inductance	Pin(4-6)	100KHz , 100mV			0.11	μH
C _{ww}	Pin(4-6) to (3-1)	100KHz , 100mV			12.0	PF
Voltage Isolation	Pin(4-6) to (3-1)	1 minute	1500			Vrms
bandwidth		Impedance 75 ohms	0.25		500	MHz

Dimension

Schematic



TOLERANCES $\frac{\pm 0.25\text{mm}}{(\pm .010\text{inch})}$

