

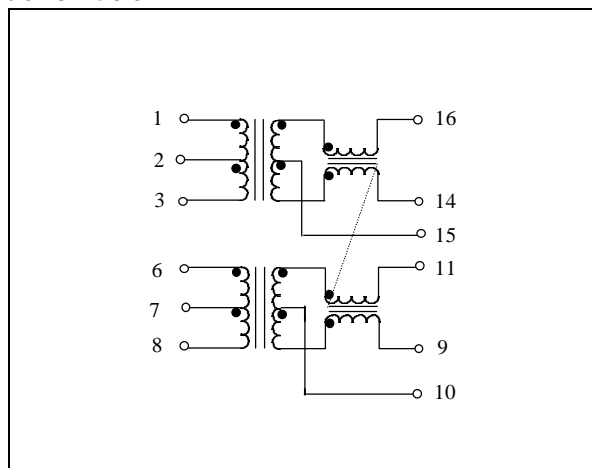
### Feature

- ◆ Designed to meet IEEE802.3 requirement.
- ◆ Low profile PTH package

### Specifications

Parameter	Condition	Min.	Typ.	Max	Unit
Inductance	Pin(1-3) & (6-8)	100			μH
Turn Ratio	Pin(1-3):(16-14)		1 : 1		
	Pin(6-8) : (11-9)		1 : 1		
Interwinding capacitance	@100KHz,200mV			9	pF
Leakage inductance	@100KHz,200mV			0.3	uH
DCR	Pin(1-3) & (6-8)			0.3	Ω
Return loss	@1-10MHz	-15			dB
Insertion loss	@1-10MHz			-0.5	dB
CMRR	@1-10MHz	-50			dB
	@10-50MHz	-40			dB
Voltage Isolation	1 sec	2000			Vrms

### Schematic



### Description

The PA162456 is a magnetic module that is specifically designed for the implementation of analog interface in Ethernet AUI or MAC application.

This module consist of three individual transformer which provide wide bandwidth , fast rise time and impedance matching.

The PA162456 provide high voltage isolation to protect against static charge on the twisted pair line.

A typical application is shown as below.

The components and guideline given in this application should serve as a starting point for design .Please refer to related data sheet of IC transceiver chipset for more detail information.

### Dimension:±0.25

