

Product Features

- PI74ALVCHT16835 is designed for low-voltage operation
- $V_{CC} = 2.3V$ to $3.6V$
- 5V I/O Tolerant Inputs and Outputs
- Typical V_{OLP} (Output Ground Bounce)
 $< 0.8V$ at $V_{CC} = 3.3V$, $T_A = 25^\circ C$
- Typical V_{OHV} (Output V_{OH} Undershoot)
 $< 2.0V$ at $V_{CC} = 3.3V$, $T_A = 25^\circ C$
- Bus Hold retains last active bus state during 3-State, eliminating the need for external pullup resistors
- Industrial operation at $-40^\circ C$ to $+85^\circ C$
- Packages available:
 - 56-pin 240 mil wide plastic TSSOP (A)
 - 56-pin 300 mil wide plastic SSOP (V)
 - 56-pin 173 mil wide plastic TVSOP (K)

Product Description

Pericom Semiconductor's PI74ALVCH series of logic circuits are produced using the Company's advanced 0.5 micron CMOS technology, achieving industry leading speed.

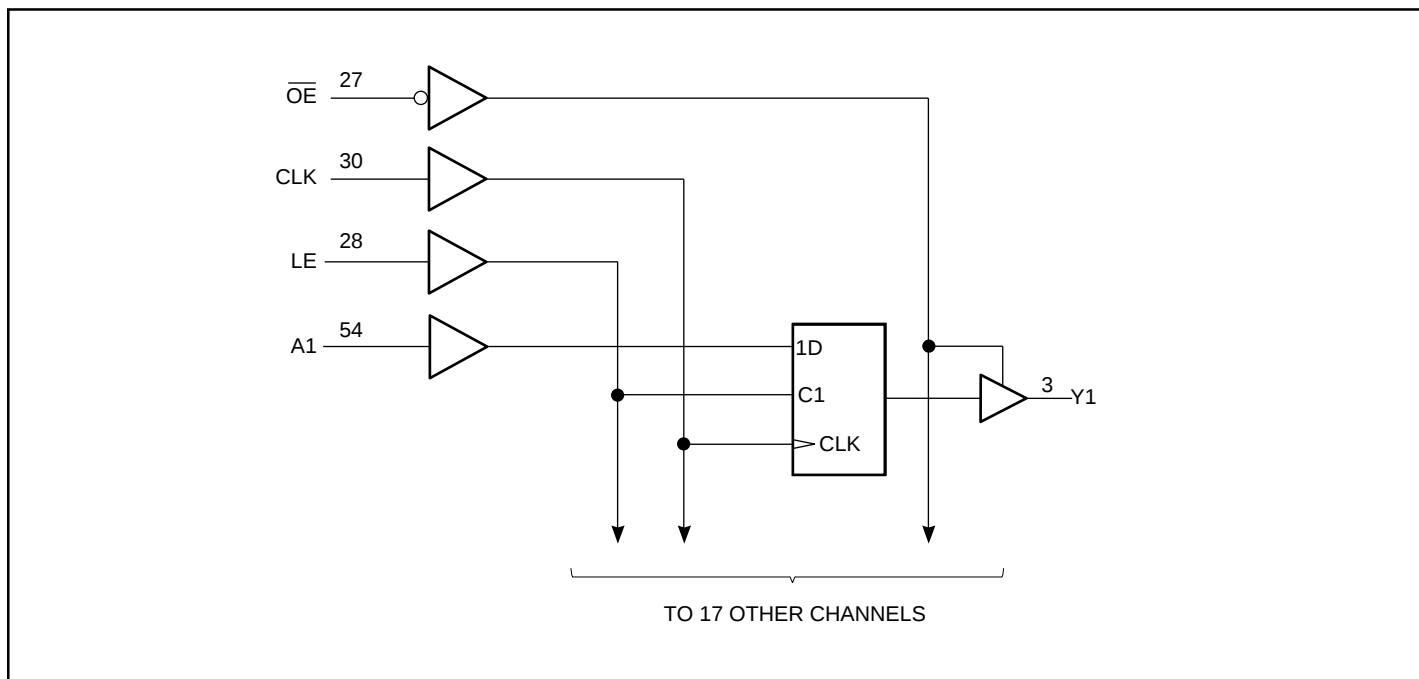
The 18-bit PI74ALVCHT16835 universal bus driver is designed for 2.3V to 3.6V V_{CC} operation.

Data flow from A to Y is controlled by Output Enable ($\overline{OE}$). The device operates in the transparent mode when LE is HIGH. The A data is latched if CLK is held at a high or low logic level. If LE is LOW, the A-bus is stored in the latch/flip-flop on the low-to-high transition of CLK. When $\overline{OE}$ is HIGH, the outputs are in the high-impedance state.

To ensure the high-impedance state during power up or power down, OE should be tied to V_{CC} through a pullup resistor; the minimum value of the resistor is determined by the current-sinking capability of the driver.

Active bus-hold circuitry is provided to hold unused or floating data inputs at a valid logic level.

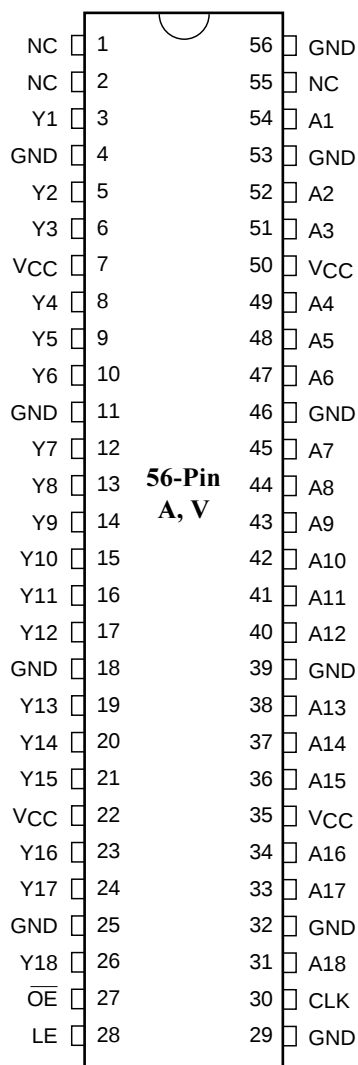
Logic Block Diagram



Product Pin Description

Pin Name	Description
$\overline{OE}$	Output Enable Input (Active LOW)
LE	Latch Enable
CLK	Clock Input
A	Data Input
Y	Data Output
GND	Ground
V _{CC}	Power

Product Pin Configuration



Truth Table^{(1)†}

Inputs				Outputs Y
$\overline{OE}$	LE	CLK	A	
H	X	X	X	Z
L	H	X	L	L
L	H	X	H	H
L	L	↑	L	L
L	L	↑	H	H
L	L	H	X	Y ₀ ⁽²⁾
L	L	L	X	Y ₀ ⁽³⁾

Note:

1. H = High Signal Level
L = Low Signal Level
Z = High Impedance
↑ = Transition LOW-to-HIGH
X = Irrelevant
2. Output level before the indicated steady-state input conditions were established, provided that CLK is high before LE goes low.
3. Output level before the indicated steady-state input conditions were established.

Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	–65°C to +150°C
Ambient Temperature with Power Applied	–40°C to +85°C
Input Voltage Range, V_{IN}	–0.5V to $V_{CC} + 0.5V$
Output Voltage Range, V_{OUT}	–0.5V to $V_{CC} + 0.5V$
DC Input Voltage	–0.5V to +5.0V
DC Output Current	100 mA
Power Dissipation	1.0W

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Recommended Operating Conditions⁽¹⁾

Parameters	Description	Test Conditions	Min.	Typ.	Max.	Units
V_{CC}	Supply Voltage		2.3		3.6	V
V_{IH}	Input HIGH Voltage	$V_{CC} = 2.3V$ to $2.7V$	1.7			
		$V_{CC} = 2.7V$ to $3.6V$	2.0			
V_{IL}	Input LOW Voltage	$V_{CC} = 2.3V$ to $2.7V$			0.7	
		$V_{CC} = 2.7V$ to $3.6V$			0.8	
V_{IN}	Input Voltage		0		5.5	
V_{OUT}	Output Voltage		0		5.5	
I_{OH}	High-level Output Current	$V_{CC} = 2.3V$			–12	mA
		$V_{CC} = 2.7V$			–12	
		$V_{CC} = 3.0V$			–24	
I_{OL}	Low-level Output Current	$V_{CC} = 2.3V$			12	
		$V_{CC} = 2.7V$			12	
		$V_{CC} = 3.0V$			24	
T_A	Operating Free-Air Temperature		–40		85	°C

Note:

- Unused control inputs must be held HIGH or LOW to prevent them from floating.

DC Electrical Characteristics (Over the Operating Range, $T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, $V_{CC} = 3.3\text{V} \pm 10\%$)

Parameters	Test Conditions		V _{CC} ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
V _{OH}	I _{OH} = -100 μA		Min. to Max.	V _{CC} -0.2			V
	I _{OH} = -6 mA	V _{IH} = 1.7V	2.3V	2.0			
	I _{OH} = -12 mA	V _{IH} = 1.7V	2.3V	1.7			
		V _{IH} = 2.0V	2.7V	2.2			
		V _{IH} = 2.0V	3.0V	2.4			
	I _{OH} = -24 mA	V _{IH} = 2.0V	3.0V	2.0			
V _{OL}	I _{OL} = 100 μA		Min. to Max.			0.2	V
	I _{OL} = 6 mA	V _{IL} = 0.7V	2.3V			0.4	
	I _{OL} = 12 mA	V _{IL} = 0.7V	2.3V			0.7	
		V _{IL} = 0.8V	2.7V			0.4	
	I _{OL} = 24 mA	V _{IL} = 0.8V	3.0V			0.55	
I _I	V _I = V _{CC} or GND		3.6V			±5	μA
I _{OFF}	V _{IN} or V _{OUT} = 5.5V					10	
I _I (Hold) ⁽³⁾	V _I = 0.8V		3.0V	75			
	V _I = 2.0V			-75			
	V _I = 0 to 3.6V		3.6V			±500	
I _{OZ} ⁽⁴⁾	V _O = V _{CC} or GND		3.6V			±10	
I _{CC}	V _I = V _{CC} or GND	I _O = 0	3.6V			40	
ΔI _{CC}	One input at V _{CC} - 0.6V, Other inputs at V _{CC} or GND		3V to 3.6V			750	
C _I Control Inputs	V _I = V _{CC} or GND		3.3V		3.5		pF
Data Input	V _O = V _{CC} or GND		3.3V		6		
C _O Outputs	V _O = V _{CC} or GND		3.3V		7		

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at $V_{CC} = 3.3\text{V}$, $+25^{\circ}\text{C}$ ambient and maximum loading.
3. Bus Hold maximum dynamic current required to switch the input from one state to another.
4. For I/O ports, the I_{OZ} includes the input leakage current.

Timing Requirements over Operating Range

Parameters	Description	V _{CC} = 2.5 V ± 0.2V		V _{CC} = 2.7V		V _{CC} = 3.3V ± 0.3V		Units
		Min.	Max.	Min.	Max.	Min.	Max.	
f _{CLOCK}	Clock frequency	0	150	0	150	0	150	MHz
t _W Pulse Duration	LE high	3.3		3.3		3.3		ns
	CLK high or low	3.3		3.3		3.3		
t _{SU} Setup time	Data before CLK ↑	2.2		2.1		1.7		
	Data before LE ↓, CLK High	1.9		1.6		1.5		
	Data before LE ↓, CLK Low	1.3		1.1		1		
t _H Hold time	Data after CLK ↑	0.6		0.6		0.7		
	Data after LE ↓, CLK High or Low	1.4		1.7		1.4		
Δt/Δv ⁽¹⁾	Input Transition Rise or Fall	0	10	0	10	0	10	ns/V

Note:

- Unused control inputs must be held HIGH or LOW to prevent them from floating.

Switching Characteristics Over Operating Range⁽¹⁾

Parameters	From (Input)	To (Output)	V _{CC} = 2.5V ± 0.2V		V _{CC} = 2.7V		V _{CC} = 3.3 V ± 0.3V		Units
			Min. ⁽²⁾	Max.	Min. ⁽²⁾	Max.	Min. ⁽²⁾	Max.	
f _{MAX}			150		150		150		MHz
t _{PD}	A	Y	1	4.4		4.2	1	3.6	ns
t _{PD}	LE	Y	1.3	5.9		4.9	1.3	4.2	
t _{PD}	CLK	Y	1.4	5.6		5.2	1.4	4.5	
t _{EN}	OE	Y	1.4	5.6		5.6	1.1	4.6	
t _{DIS}	OE	Y	1	5.2		4.3	1.3	3.9	
T _{SK} ⁽³⁾							—	0.5	

Notes:

- See test circuit and waveforms.
- Minimum limits are guaranteed but not tested on Propagation Delays.
- Skew between any two outputs of the same package switching in the same direction. This parameter is not production tested.

Switching Characteristics, from 0°C to 65°C, C_L = 50pF

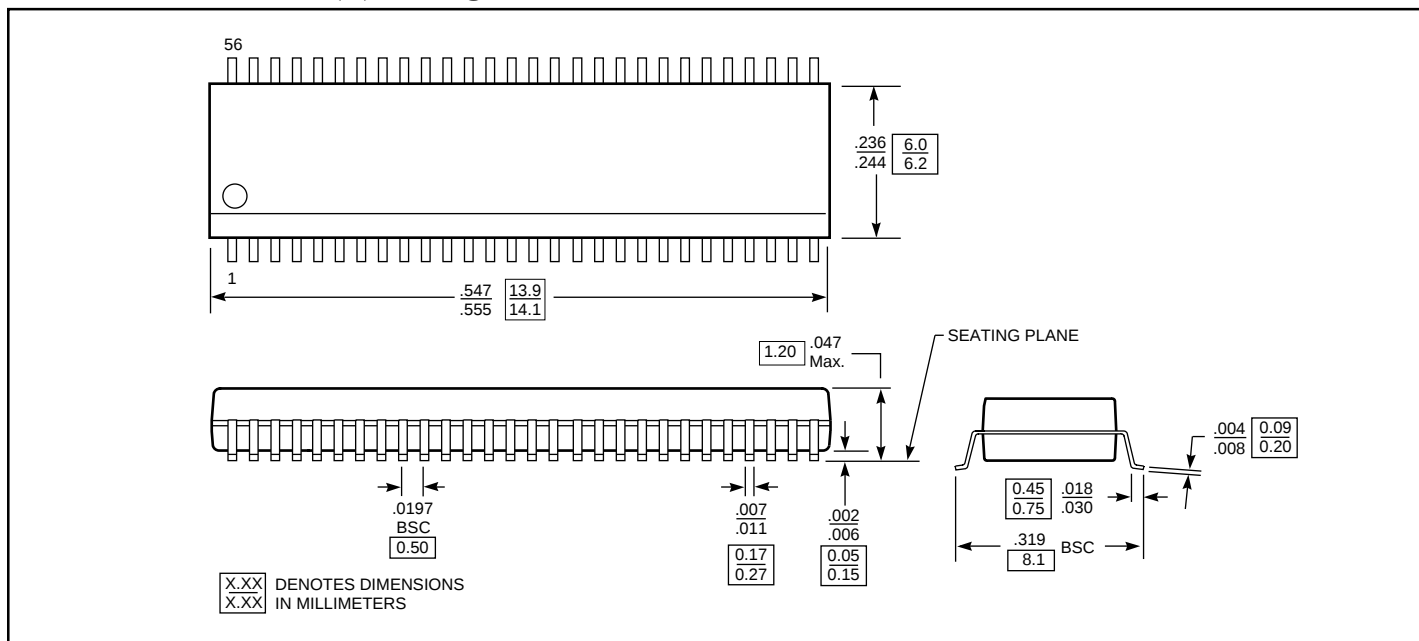
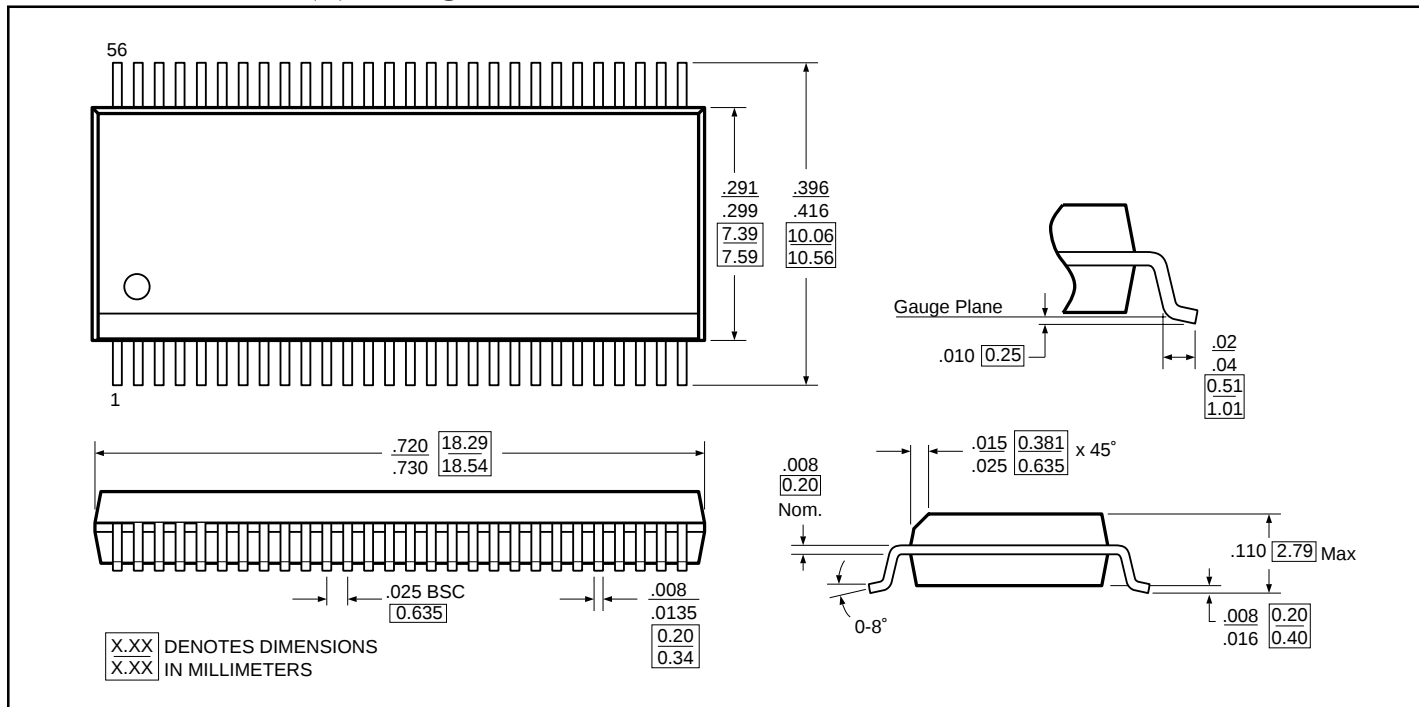
Parameter	From (Input)	To (Output)	V _{CC} = 3.3V ± 0.15V		Units
			Min.	Max.	
t _{PD}	CLK	Y	1.7	4.5	ns

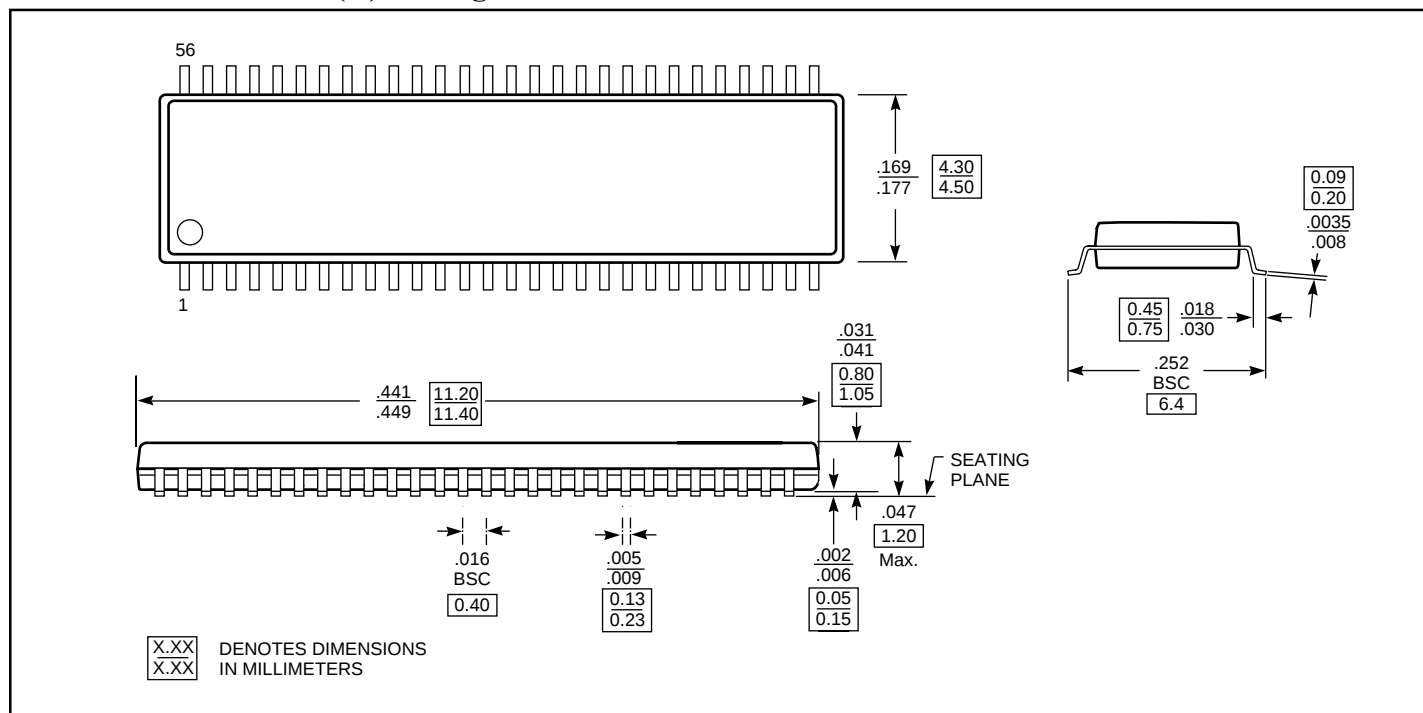
Operating Characteristics, T_A = 25°C

Parameter		Test Conditions	V _{CC} = 2.5V ± 0.2V	V _{CC} = 3.3V ± 0.3V	Units
			Typical		
C _{PD} Power Dissipation Capacitance	Outputs Enabled	C _L = 50pF, f= 10 MHz	26	31	pF
	Outputs Disabled		12	14	

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56-Pin 240-Mil TSSOP (A) Package

56-Pin 300-Mil SSOP (V) Package


56-Pin 173-Mil TVSOP (K) Package

Ordering Information

Part	Pin-Package	Operating Range
PI74ALVCHT16835A	56-Pin - TSSOP	-40°C to 85°C
PI74ALVCHT16835K	56-Pin - TVSOP	
PI74ALVCHT16835V	56-Pin - SSOP	