PI74ALVC162834

18-Bit Universal Bus Driver with 3-State Outputs

Product Features

- PI74ALVC162834 is designed for low voltage operation, V_{CC} = 2.3V to 3.6V
- Outputs have equivalent 26-Ohm series resistors
- Supports PC100 Registered DIMM
- Typical V_{OLP} (Output Ground Bounce) <0.8V at V_{CC} = 3.3V, T_A = 25°C
- Typical V_{OHV} (Output V_{OH} Undershoot) <2.0V at V_{CC} = 3.3V, T_A = 25°C
- Industrial operation at –40°C to +85°C
- Packages available:
  – 56-pin 240 mil wide plastic TSSOP (A)
  – 56-pin 173 mil wide plastic TVSOP (K)
  – 56-pin 300 mil wide plastic SSOP (V)

Product Description

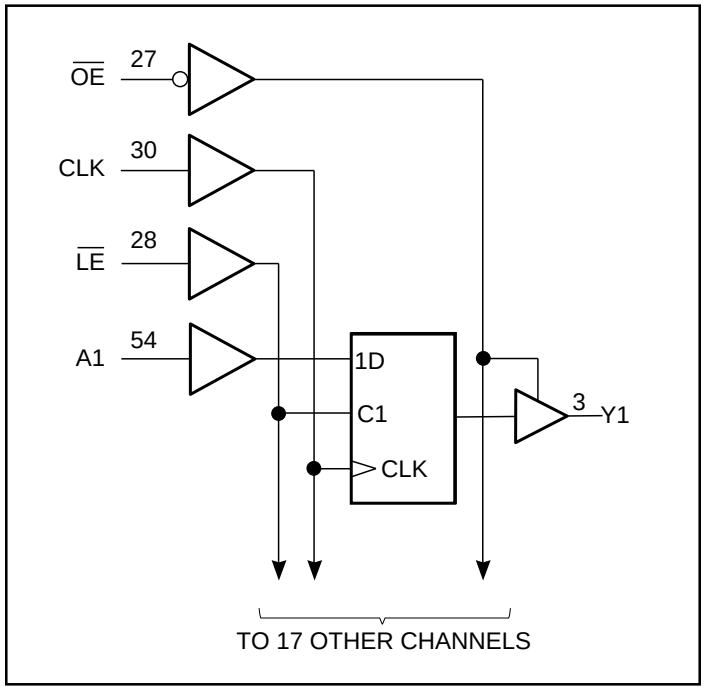
Pericom Semiconductor's PI74ALVC series of logic circuits are produced using the Company's advanced 0.5 micron CMOS technology, achieving industry leading speed.

The 18-bit PI74ALVC162834 universal bus driver is designed for 2.3V to 3.6V Vcc operation.

Data flow from A to Y is controlled by Output Enable ($\overline{OE}$). The device operates in the transparent mode when $\overline{LE}$ is LOW. The A data is latched if CLK is held at a high or low logic level. If $\overline{LE}$ is HIGH, the A-bus is stored in the latch/flip-flop on the low-to-high transition of CLK. When $\overline{OE}$ is HIGH, the outputs are in the high-impedance state.

To ensure the high-impedance state during power up or power down, $\overline{OE}$ should be tied to Vcc through a pullup resistor; the minimum value of the resistor is determined by the current-sinking capability of the driver.

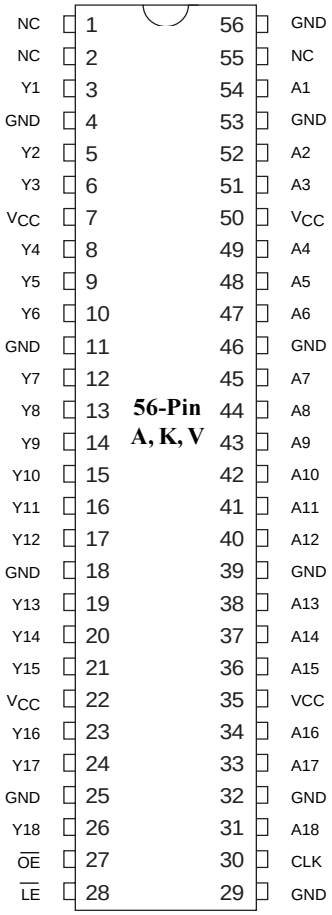
Logic Block Diagram



Product Pin Description

Pin Name	Description
$\overline{OE}$	Output Enable Input (Active LOW)
$\overline{LE}$	Latch Enable (Active LOW)
CLK	Clock Input
A	Data Input
Y	Data Output
GND	Ground
V_{CC}	Power

Product Pin Configuration



Truth Table[1]

Inputs				Outputs
$\overline{OE}$	$\overline{LE}$	CLK	A	Y
H	X	X	X	Z
L	L	X	L	L
L	L	X	H	H
L	H	↑	L	L
L	H	↑	H	H
L	H	H	X	Yo[2]
L	H	L	X	Yo[3]

Notes:

1 H = High Signal Level
L = Low Signal Level
Z = High Impedance
↑ = Transition LOW-to-HIGH
X = Irrelevant

2. Output level before the indicated steady-state input conditions were established, provided that CLK is high before $\overline{LE}$ goes HIGH.
3. Output level before the indicated steady-state input conditions were established.

Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	–65°C to +150°C
Ambient Temperature with Power Applied	–40°C to +85°C
Input Voltage Range, V_{IN}	–0.5V to V_{CC}+0.5V
Output Voltage Range, V_{OUT}	–0.5V to V_{CC}+0.5V
DC Input Voltage	–0.5V to +5.0V
DC Output Current	100mA
Power Dissipation	1.0W

Note:
Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Recommended Operating Conditions(1)

Parameters	Description	Test Conditions	Min.	Typ.	Max.	Units
V_{CC}	Supply Voltage		2.3		3.6	V
V_{IH}	Input HIGH Voltage	V_{CC} = 2.3V to 2.7V	1.7			
		V_{CC} = 2.7V to 3.6V	2.0			
V_{IL}	Input LOW Voltage	V_{CC} = 2.3V to 2.7V			0.7	
		V_{CC} = 2.7V to 3.6V			0.8	
V_{IN}	Input Voltage		0		V_{CC}	
V_{OUT}	Output Voltage		0		V_{CC}	
I_{OH}	High-level Output Current	V_{CC} = 2.3V			–6	mA
		V_{CC} = 2.7V			–8	
		V_{CC} = 3.0V			–12	
I_{OL}	Low-level Output Current	V_{CC} = 2.3V			6	
		V_{CC} = 2.7V			8	
		V_{CC} = 3.0V			12	
T_A	Operating Free-Air Temperature		–40		85	°C

Note:
1. Unused control inputs must be held HIGH or LOW to prevent them from floating.

DC Electrical Characteristics (Over the Operating Range, $T_A = -40°C$ to $+85°C$, $V_{CC} = 3.3V \pm 10\%$)

Parameter	Conditions		V_{CC}(1)	Min(1)	Typ(2)	Max(1)	Units
V_{OH}	$I_{OH} = -100\mu A$		Min. to Max.	V_{CC} - 0.2			V
	$I_{OH} = -4mA$	$V_{IH} = 1.7V$	2.3V	1.9			
	$I_{OH} = -6mA$	$V_{IH} = 1.7V$	2.3V	1.7			
		$V_{IH} = 2.0V$	3.0V	2.4			
	$I_{OH} = -8mA$	$V_{IH} = 2.0V$	2.7V	2.0			
	$I_{OH} = -12mA$	$V_{IH} = 2.0V$	3.0V	2.0			
V_{OL}	$I_{OH} = 100\mu A$		Min. to Max.			0.2	
	$I_{OH} = 4mA$	$V_{IL} = 0.7V$	2.3V			0.4	
	$I_{OH} = 6mA$	$V_{IL} = 0.7V$	2.3V			0.55	
		$V_{IL} = 0.8V$	3.0V			0.55	
	$I_{OH} = 8mA$	$V_{IL} = 0.8V$	2.7V			0.6	
	$I_{OH} = 12mA$	$V_{IL} = 0.8V$	3.0V			0.8	
Ii	$V_I = V_{CC}$ or GND		3.6V			±5	µA
I_{OZ}	$V_O = V_{CC}$ or GND		3.6V			±5	
I_{CC}	$V_I = V_{CC}$ or GND		3.6V			40	
ΔI_{CC}	One input at V_{CC} - 0.6V, Other inputs a V_{CC} or GND		3V to 3.6V			750	
C_I Controls Inputs	$V_I = V_{CC}$ or GND		3.3V		3.5		pF
Data Input	$V_O = V_{CC}$ or GND		3.3V		6		
C_O Outputs	$V_O = V_{CC}$ or GND		3.3V		7		

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at Vcc = 3.3V, +25°C ambient and maximum loading.
3. For I/O ports, the I_{OZ} includes the input leakage current.

Timing Requirements over Operating Range

Parameters	Description	V_{CC} = 2.5 V ±0.2V		V_{CC} = 2.7V		V_{CC} = 3.3V ±0.3V		Units
		Min.	Max.	Min.	Max.	Min.	Max.	
f_{CLOCK}	Clock frequency	0	150	0	150	0	150	MHz
t_W Pulse Duration	$\overline{LE}$ LOW	3.3		3.3		3.3		ns
	CLK HIGH or LOW	3.3		3.3		3.3		
t_{SU} Setup time	Data before CLK↑	2.1		2.1		1.7		
	Data before $\overline{LE}$↑, CLK HIGH	2.3		2.3		1.9		
	Data before $\overline{LE}$↑, CLK LOW	1.9		1.9		1.5		
t_H Hold time	Data after CLK↑	0.6		0.6		0.7		
	Data after $\overline{LE}$↑, CLK HIGH or LOW	0.8		8		0.9		
$\Delta t/\Delta v$[1]	Input Transition Rise or Fall	0	10	0	10	0	10	ns/V

Note:
1. Unused control inputs must be held HIGH or LOW to prevent them from floating.

Switching Characteristics Over Operating Range[1]

Parameters	From (Input)	To (Output)	V_{CC} = 2.5V ± 0.2V		V_{CC} = 2.7V		V_{CC} = 3.3V ±0.3V		Units
			Min.	Max.	Min.	Max.	Min.	Max.	
f_{max}			150		150		150		MHz
t_{pd}	A	Y	1	5.2		5	1	4.2	ns
t_{pd}	$\overline{LE}$	Y	1.3	6.0		6.8	1.3	5.8	
t_{pd}	CLK	Y	1.4	6.8		6.1	1.4	5.4	
t_{en}	$\overline{OE}$	Y	1.4	6.3		6.5	1.5	5.9	
t_{dis}	$\overline{OE}$	Y	1	4.4		5.2	1.8	5.0	

Notes:
1. Unused control inputs must be held HIGH or LOW to prevent them from floating.

Switching Characteristics, from 0°C to 65°C, C_L = 50pF

Parameter	From (Input)	To (Output)	V_{CC} = 3.3V ± 0.15V		Units
			Min.	Max.	
t_{pd}	A	Y	1.4	3.9	ns
	$\overline{LE}$		1.8	5.5	
	CLK		1.8	5.2	

Operating Characteristics, T_A = 25°C

Parameters		Test Conditions	V_{CC} = 2.5V ± 0.2V	V_{CC} = 3.3V ± 0.3V	Units
			Typical	Typical	
C_{pd} Power Dissipation Capacitance	Outputs Enabled	C_L = 0pF, F = 10 MHz	38	41	pF
	Outputs Disabled		13	15	