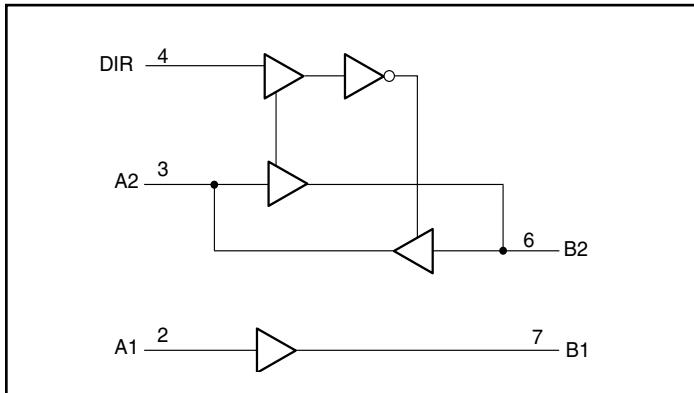


2-Bit Level Shifting Buffer/Transceiver with Configurable Dual Supply Voltage

Product Features

- Operation Voltage: $1.65V \leq V_{CCA} \leq 3.6V$
 $2.3V \leq V_{CCB} \leq 5.5V$
 $V_{CCA} \leq V_{CCB}$
- High Speed: $t_{pd} = 5ns$ typical into $30pF$ @ $3V$ V_{CC}
- Power down high-impedance inputs and outputs
- High output drive: $\pm 12mA$ at $3V$ V_{CCA}/V_{CCB}
 $\pm 24mA$ at $5V$ V_{CCB}
- Industrial operation at $-40^{\circ}C$ to $+85^{\circ}C$
- Packaging:
 - 8-pin MSOP (U)
 - 12-pin TDFN (ZE)

Logic Block Diagram



Function Table

Input		Output
DIR	DATA	
L	B2	A2
H	A2	B2
X	A1	B1

Pin Description

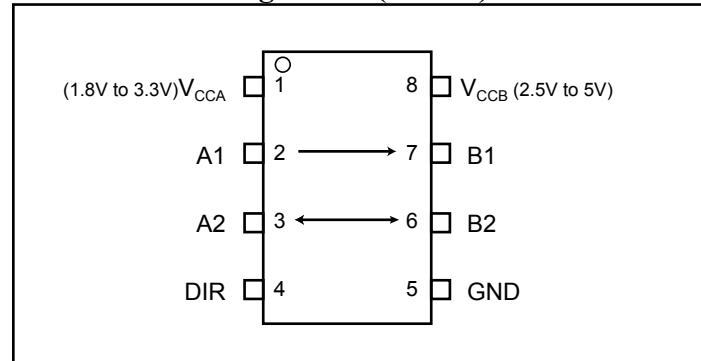
Pin Name	Description
DIR	Direction Control
A1	Data Input
B1	Data Output
A2, B2	Data Input/Output
V _{CCA} , V _{CCB}	Power Supply

Product Description

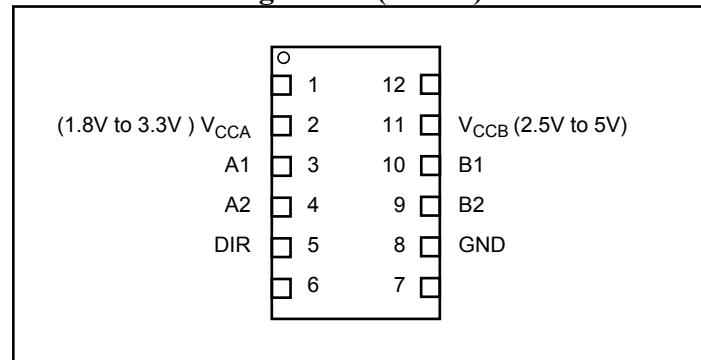
Pericom Semiconductor's STX series of logic circuits are produced using the Company's advanced CMOS technology, achieving industry leading performance.

Pericom Semiconductor's PI74STX2G4245, a 2-bit noninverting buffer/transceiver, contains two separate supply rails: A port (V_{CCA}), set to operate from 1.8V to 3.3V; B port (V_{CCB}), set to operate from 2.5V to 5V. The A1 to B1 translation is configured as an unidirectional buffer; the translation between A2 and B2 is bidirectional and its direction is controlled by the DIR pin.

Product Pin Configuration(MSOP)



Product Pin Configuration(TDFN)



**Maximum Ratings**(Absolute maximum ratings over operating free-air temperature range from V_{CCB} at 2.5V & V_{CCA} at 1.2V, unless otherwise noted)

Supply voltage range:	V _{CCA}	-0.5V to +6V	DC Input Diode Current, I _{IK} (V _O <0)	-50mA
	V _{CCB}	-0.5V to +6V	DC Output Diode Current, I _{OK} (V _O <0)	-50mA
Input Voltage Range, V _I :			DC Output Current, (I _{OUT})	±50mA
	Input A ₁ ⁽¹⁾	-0.5V to +6V	DC V _{CC} or GND Current (I _{CC} /I _{GND}).....	±100mA
	I/O A ₂ ^(1,2)	-0.5V to V _{CCA} +0.5V	Storage temperature range, T _{STG}	-65°C to 150°C
	I/O B ₂ ^(1,2)	-0.5V to V _{CCB} +0.5V	Junction Lead Temperature (I _{OS})	260°C
Output Voltage Range, V _O			Power Dissipation: MSOP	200mW
	Output B ₁ ^(1,2) ..	-0.5V to V _{CCB} +0.5V		

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

1. The input negative voltage and output voltage ratings may be exceeded if the input and output current ratings are observed.
2. This value is limited to 6V.

Recommended Operating Conditions for A Port (V_{CCA})⁽¹⁾

Parameter	Description	Test Conditions	Min.	Max.	Units
V _{CCA}	Supply Voltage		1.65	3.6	V
V _{IH}	High-level input voltage	V _{CCA} = 1.65V to 1.95V	0.7 x V _{CCA}		
		V _{CCA} = 2.3V to 3.6V	0.7 x V _{CCA}		
V _{IL}	Low-level input voltage	V _{CCA} = 1.65 to 1.95V		0.3 x V _{CCA}	
		V _{CCA} = 2.3V to 3.6V		0.3 x V _{CCA}	
V _{IA}	Input Voltage		0	V _{CCA}	
V _{OA}	Output Votage		0	V _{CCA}	
I _{OH}	High-level Output Current	V _{CCA} = 1.65 V to 1.95V		-4	mA
		V _{CCA} = 2.3V to 2.7V		-8	
		V _{CCA} = 3V to 3.6V		-12	
I _{OL}	Low-level Output Current	V _{CCA} = 1.65V to 1.95V		4	
		V _{CCA} = 2.3V to 2.7V		8	
		V _{CCA} = 3V to 3.6V		12	
Δt/ΔV	Input transition rise or fall rate	V _{CCA} = 1.65V to 1.95V		20	ns/V
		V _{CCA} = 2.3V to 3.6V		10	
T _A	Operation free-air temperature		-40	85	°C

Note:

1. To ensure proper device operation, all unused inputs must be held at V_{CCA} or GND.

Recommended Operating Conditions for B Port (V_{CCB})⁽¹⁾

Paramaters	Description	Test Conditions	Min.	Max.	Units
V_{CCB}	Supply Voltage	$V_{CCB} = 2.3V \text{ to } 5.5V$	2.3	5.5	V
V_{IH}	High-level input voltage		$0.7 \times V_{CCB}$		
V_{IL}	Low-level input voltage			$0.3 \times V_{CCB}$	
V_{IB}	Input Voltage		0	V_{CCB}	
V_{OB}	Output Votage		0	V_{CCB}	
I_{OH}	High-level Output Current	$V_{CCB} = 2.3V \text{ to } 2.7V$		-8	mA
		$V_{CCB} = 3V \text{ to } 3.6V$		-12	
		$V_{CCB} = 4.5V \text{ to } 5.5V$		-24	
I_{OL}	Low-level Output Current	$V_{CCB} = 2.3V \text{ to } 2.7V$		8	
		$V_{CCB} = 3V \text{ to } 3.6V$		12	
		$V_{CCB} = 4.5V \text{ to } 5.5V$		24	
$\Delta t/\Delta V$	Input transition rise or fall rate	$V_{CCB} = 2.3V \text{ to } 5.5V$		10	m/V
T_A	Operation free-air temperature		-40	85	°C

Note:

1. To ensure proper device operation, all unused inputs must be held at V_{CCB} or GND.

Electrical Characteristics (Over recommended operating free-air temperature range)

Parameters	Test Conditions	V _{CCA} ⁽¹⁾	V _{CCB} ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
V _{OHA} (B to A)	I _{OH} = -100uA	1.65V to 3.6V	2.3V ≤ V _{CCB} ≤ 5.5V and V _{CCB} ≥ V _{CCA}	V _{CCB} -0.1			V
	I _{OH} = -4mA	1.65V		1.3			
	I _{OH} = -8mA	2.3V		1.8			
	I _{OH} = -12mA	3.0V		2.4			
V _{O LA} (B to A)	I _{OH} = 100uA	1.65V to 3.6V				0.1	
	I _{OH} = 4mA	1.65V				0.24	
	I _{OH} = 8mA	2.3V				0.3	
	I _{OH} = 12mA	3.0V				0.4	
V _{OHB} (A to B)	I _{OH} = -100uA	1.65V ≤ V _{CCA} ≤ 3.6V and V _{CCB} ≥ V _{CCA}	2.3V to 5.5V	V _{CCB} -0.1			
	I _{OH} = -8mA		2.3V	1.8			
	I _{OH} = -12mA		3.0V	2.4			
	I _{OH} = -24mA		4.5V	3.8			
V _{O LB} (A to B)	I _{OH} = 100uA		2.3V to 5.5V			0.1	
	I _{OH} = 8mA		2.3V			0.3	
	I _{OH} = 12mA		3.0V			0.4	
	I _{OH} = 24mA		4.5V			0.55	
I _I ⁽³⁾	V _I = V _{CCA} /V _{CCB} or GND	1.65V to 3.6V and V _{CCB} ≥ V _{CCA}	2.3V to 5.5V and V _{CCB} ≥ V _{CCA}			±5	μA
I _{OFF}	V _I or V _O = V _{CCB} or GND	0V	0 to 5.5V			±10	
	V _I or V _O = V _{CCA} or GND	0 to 3.6V	0V			±10	
I _{CCA}	V _I = V _{CCA} or GND, I _O = 0	1.65V to 3.6V and V _{CCB} ≥ V _{CCA}	2.3 to 5.5V and V _{CCB} ≥ V _{CCA}			10	
	V _I = V _{CCB} or GND, I _O = 0					10	
C _I	A1, DIR	V _I = V _{CCA} or GND	1.8V	3.3V		4.5	pF
C _O	B1	V _O = V _{CCB} or GND	1.8V	3.3V		4.5	
C _{IO}	A2, B2	V _I or V _O = V _{CCA} /V _{CCB} or GND	1.8V	3.3V		6.0	

Notes:

1. When $1.65 \leq V_{CCA} \leq 1.95V$, V_{CCB} can not be greater than 3.6V
2. All Typical values are at $T_A = 25^\circ C$
3. For I/O ports, the parameter I_I includes the output leakage current.



ADVANCED INFORMATION

PI74STX2G4245
2-Bit Level Shifting Buffer/Transceiver with
Configurable Dual Supply Voltage

Switching Characteristics for $V_{CCA} = 1.8V \pm 0.15V$

(Over recommended operating free-air temperature range, see figure 1)

Parameter	From (Input)	To (Output)	$V_{CCB} = 2.5V \pm 0.2V$		$V_{CCB} = 3.3V \pm 0.3V$		Units
			Min.	Max.	Min.	Max.	
t _{PHL}	A	B	1.0	8	1.0	8	ns
t _{PLH}			1.0	8	1.0	8	
t _{PHL}		A	1.0	7	1.0	7	
t _{PLH}			1.0	7	1.0	7	
t _{SK(O)}				1.5		1	

Switching Characteristics for $V_{CCA} = 2.5V \pm 0.2V$

(Over recommended operating free-air temperature range, see figure 1)

Parameter	From (Input)	To (Output)	$V_{CCB} = 2.5V \pm 0.2V$		$V_{CCB} = 3.3V \pm 0.3V$		$V_{CCB} = 5V \pm 0.5V$		Units
			Min.	Max.	Min.	Max.	Min.	Max.	
t _{PHL}	A	B	1.0	7	1.0	6.5	1.0	6	ns
t _{PLH}			1.0	7	1.0	6.5	1.0	6	
t _{PHL}		A	1.0	6	1.0	6	1.0	5	
t _{PLH}			1.0	6	1.0	6	1.0	5	
t _{SK(O)}				1.5		1		1	

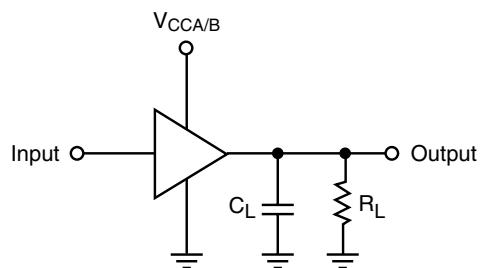
Switching Characteristics for $V_{CCA} = 3.3V \pm 0.3V$

(Over recommended operating free-air temperature range, see figure 1)

Parameter	From (Input)	To (Output)	$V_{CCB} = 3.3V \pm 0.3V$		$V_{CCB} = 5V \pm 0.5V$		Units
			Min.	Max.	Min.	Max.	
t _{PHL}	A	B	1.0	5	1.0	5	ns
t _{PLH}			1.0	5	1.0	5	
t _{PHL}		A	1.0	5	1.0	5	
t _{PLH}			1.0	5	1.0	5	
t _{SK(O)}				1		1	

AC Loading and Waveforms

$V_{CCA/B}$	C_L	R_L
$1.8 \pm 0.15V$	15pF	1KΩ
$2.5V \pm 0.2V$	15pF	500Ω
3.0V to 3.6V	30pF	500Ω
5V ± 0.5V	50pF	500Ω



C_L includes load and stray capacitance
Input PRR = 1.0 MHz; t_W = 500ns

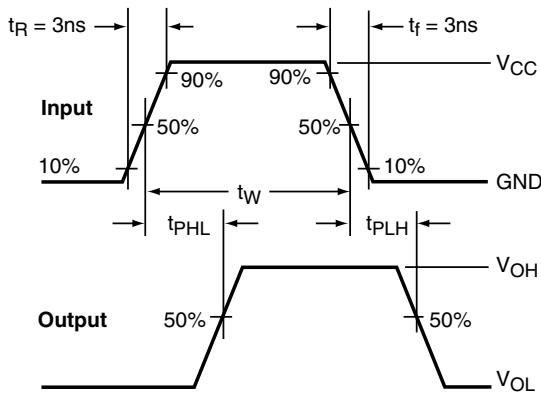
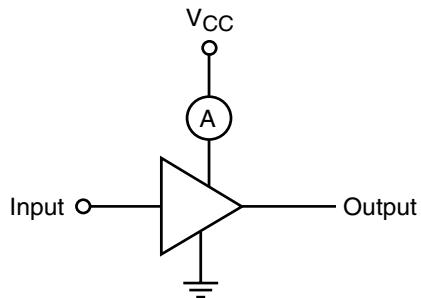


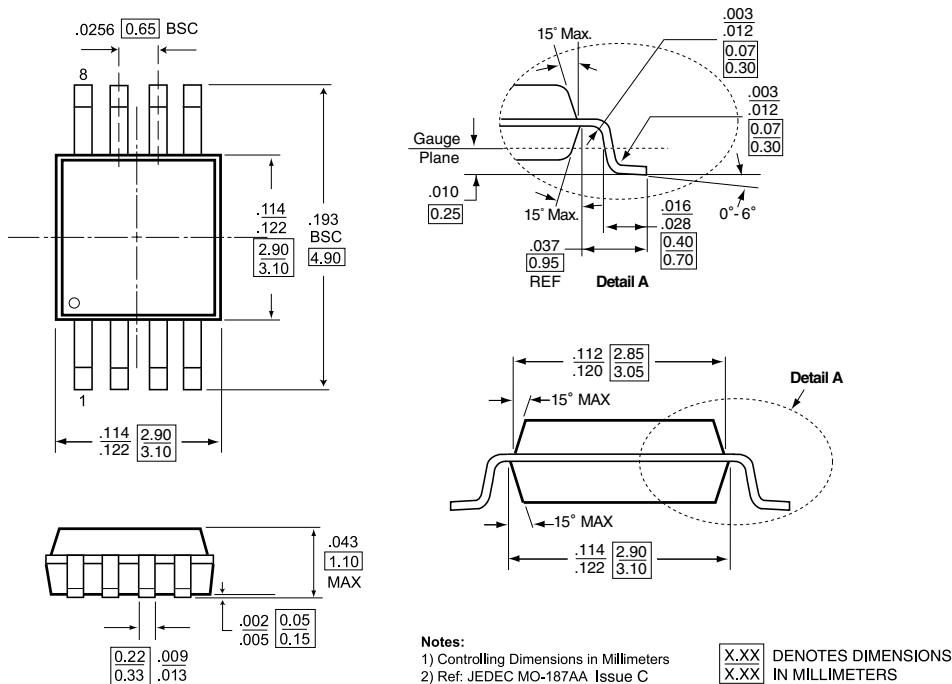
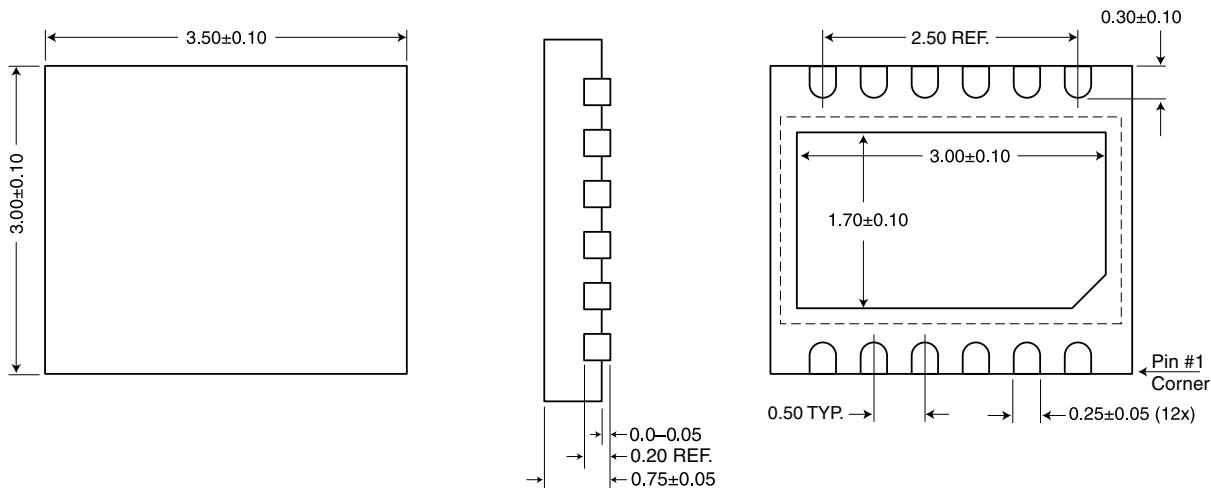
Figure 3. AC Waveforms

Figure 1. AC Test Circuit



Input = AC Waveform; $t_r = t_f = 1.8ns$;
PRR = 10 MHz; Duty Cycle = 50%

Figure 2. ICCD Test Circuit

Packaging Mechanical: 8-Pin MSOP (U)

Packaging Mechanical: 12-pin TDFN (ZE)




ADVANCED INFORMATION

PI74STX2G4245

2-Bit Level Shifting Buffer/Transceiver with
Configurable Dual Supply Voltage

Ordering Information

Ordering Code	Package Code	Package Type	Package Top Marking
PI74STX2G4245UX	U	8-pin MSOP	STX2G,4245
PI74STX2G4245ZEEX	ZE	Pb-free & Green, 12-pinTDFN	DE

Notes:

1. Thermal Characteristics can be found on the web: <http://www.pericom.com/packaging/>
2. X = Tape and Reel
3. Number of Transistors = TBD