

## Fast CMOS 3.3V 16-Bit Bidirectional Transceiver

### Product Features

- Compatible with LCX™ and LVT™ families of products
- Supports 5V Tolerant Mixed Signal Mode Operation
  - Input can be 3V or 5V
  - Output can be 3V or connected to 5V bus
- Advanced Low Power CMOS Operation
- Excellent output drive capability:  
Balanced drives (24 mA sink and source)
- Pin compatible with industry standard double-density pinouts
- Low ground bounce outputs
- Hysteresis on all inputs
- Industrial operating temperature range:  $-40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$
- Multiple center pins and distributed Vcc/GND pins minimize switching noise
- Packages available:
  - 48-pin 240 mil wide thin plastic TSSOP (A)
  - 48-pin 300 mil wide plastic SSOP (V)

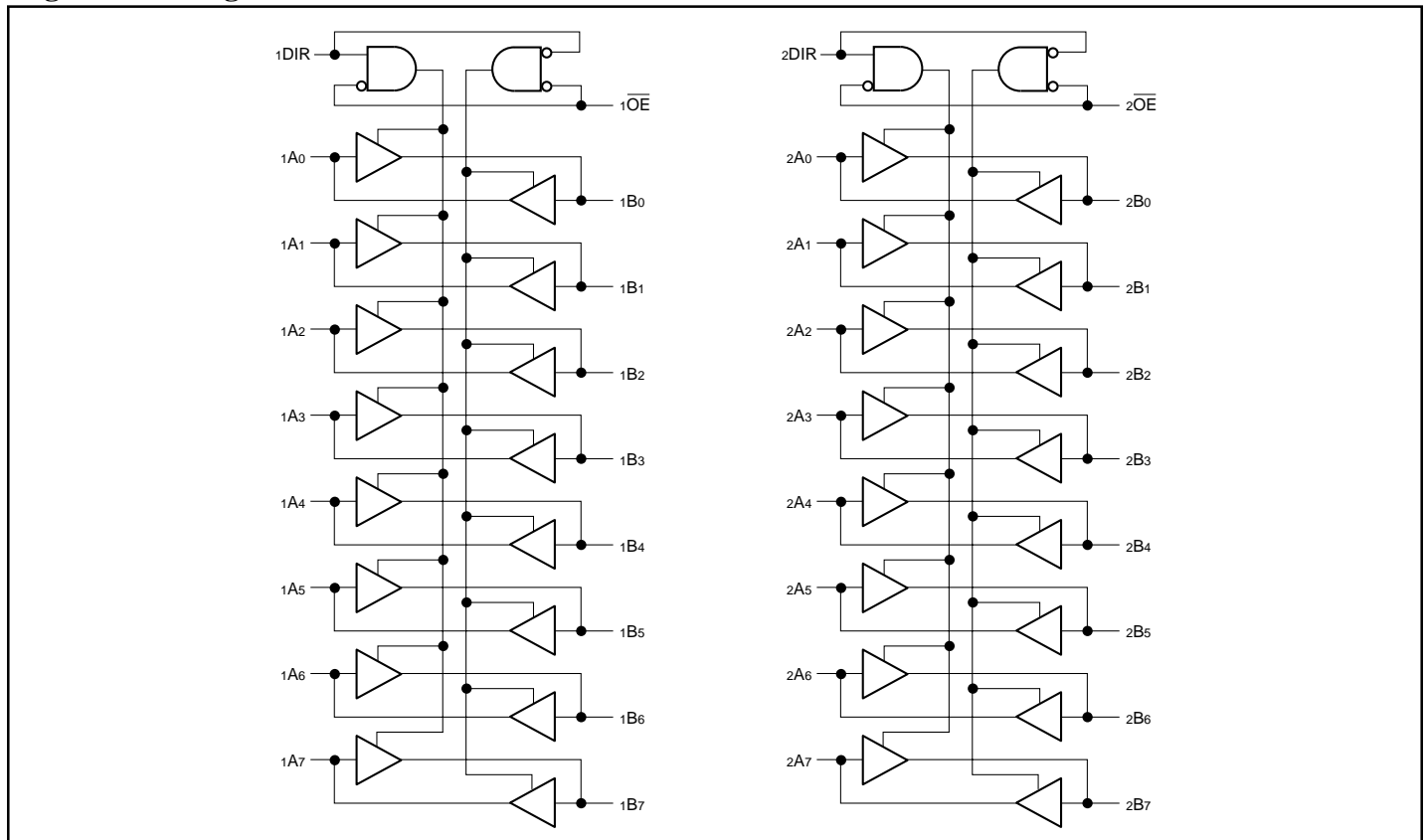
### Product Description

Pericom Semiconductor's PI74LPT series of logic circuits are produced in the Company's advanced 0.6 micron CMOS technology, achieving industry leading speed grades.

The PI74LPT16245 is a 16-bit bidirectional transceiver designed for asynchronous two-way communication between data buses. The direction control input pin (xDIR) determines the direction of data flow through the bidirectional transceiver. The Direction and Output Enable controls are designed to operate this device as either two independent 8-bit transceivers or one 16-bit transceiver. The output enable ( $\overline{\text{OE}}$ ) input, when HIGH, disables both A and B ports by placing them in HIGH Z condition.

The PI74LPT16245 can be driven from either 3.3V or 5.0V devices allowing this device to be used as a translator in a mixed 3.3/5.0V system.

### Logic Block Diagram



## Product Pin Description

Pin Name	Description
$\overline{xOE}$	3-State Output Enable Inputs (Active LOW)
$\overline{xDIR}$	Direction Control Input
$\overline{xAx}$	Side A Inputs or 3-State Inputs
$\overline{xBx}$	Side B Outputs or 3-State Outputs
GND	Ground
VCC	Power

## Truth Table

Inputs <sup>(1)</sup>		Outputs <sup>(1)</sup>
$\overline{xOE}$	$\overline{xDIR}$	
L	L	Bus B Data to Bus A
L	H	Bus A Data to Bus B
H	X	Z

### Note:

1. H = High Voltage Level, X = Don't Care,  
L = Low Voltage Level, Z = High Impedance

## Product Pin Configuration

1DIR	1	48	$\overline{1OE}$
1B0	2	47	1A0
1B1	3	46	1A1
GND	4	45	GND
1B2	5	44	1A2
1B3	6	43	1A3
VCC	7	42	VCC
1B4	8	41	1A4
1B5	9	40	1A5
GND	10	39	GND
1B6	11	38	1A6
1B7	12	37	1A7
2B0	13	36	2A0
2B1	14	35	2A1
GND	15	34	GND
2B2	16	33	2A2
2B3	17	32	2A3
VCC	18	31	VCC
2B4	19	30	2A4
2B5	20	29	2A5
GND	21	28	GND
2B6	22	27	2A6
2B7	23	26	2A7
2DIR	24	25	$\overline{2OE}$

48-PIN  
V48  
A48

## Capacitance ( $T_A = 25^\circ\text{C}$ , $f = 1\text{ MHz}$ )

Parameters <sup>(1)</sup>	Description	Test Conditions	Typ	Max.	Units
$C_{IN}$	Input Capacitance	$V_{IN} = 0\text{ V}$	4.5	6	pF
$C_{OUT}$	Output Capacitance	$V_{OUT} = 0\text{ V}$	5.5	8	pF

### Note:

1. This parameter is determined by device characterization but is not production tested.

## Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature .....	–55°C to +125°C
Ambient Temperature with Power Applied .....	–40°C to +85°C
Supply Voltage to Ground Potential (Inputs & Vcc Only) .....	–0.5V to +7.0V
Supply Voltage to Ground Potential (Outputs & D/O Only) ..	–0.5V to +7.0V
DC Input Voltage .....	–0.5V to +7.0V
DC Output Current .....	120 mA
Power Dissipation .....	1.0W

### Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

## DC Electrical Characteristics (Over the Operating Range, TA = –40°C to +85°C, VCC = 2.7V to 3.6V)

Parameters	Description	Test Conditions <sup>(1)</sup>		Min.	Typ <sup>(2)</sup>	Max.	Units
VIH	Input HIGH Voltage (Input pins)	Guaranteed Logic HIGH Level		2.2	—	5.5	V
	Input HIGH Voltage (I/O pins)			2.0	—	5.5	V
VIL	Input LOW Voltage (Input and I/O pins)	Guaranteed Logic LOW Level		–0.5	—	0.8	V
IIH	Input HIGH Current (Input pins)	VCC = Max.	VIN = 5.5V	—	—	±1	μA
	Input HIGH Current (I/O pins)	VCC = Max.	VIN = VCC	—	—	±1	μA
IIL	Input LOW Current (Input pins)	VCC = Max.	VIN = GND	—	—	±1	μA
	Input LOW Current (I/O pins)	VCC = Max.	VIN = GND	—	—	±1	μA
IOZH	High Impedance Output Current (3-State Output pins)	VCC = Max.	VOU = 5.5V	—	—	±1	μA
IOZL		VCC = Max.	VOU = GND	—	—	±1	μA
VIK	Clamp Diode Voltage	VCC = Min., IIN = –18 mA		—	–0.7	–1.2	V
IODH	Output HIGH Current	VCC = 3.3V, VIN = VIH or VIL, VO = 1.5V <sup>(3)</sup>		–36	–60	–110	mA
IODL	Output LOW Current	VCC = 3.3V, VIN = VIH or VIL, VO = 1.5V <sup>(3)</sup>		50	90	200	mA
VOH	Output HIGH Voltage	VCC = Min. VIN = VIH or VIL	IOH = –0.1 mA	Vcc-0.2	—	—	V
			IOH = –3 mA	2.4	3.0	—	V
		VCC = 3.0V, VIN = VIH or VIL	IOH = –8 mA	2.4 <sup>(5)</sup>	3.0	—	V
			IOH = –24 mA	2.0	—	—	V
VOL	Output LOW Voltage	VCC = Min. VIN = VIH or VIL	IOL = 0.1 mA	—	—	0.2	V
			IOL = 16 mA	—	0.2	0.4	V
			IOL = 24 mA	—	0.3	0.5	V
IOS	Short Circuit Current <sup>(4)</sup>	VCC = Max. <sup>(3)</sup> , VOUT = GND		–60	–85	–240	mA
IOFF	Power Down Disable	VCC = 0V, VIN or VOUT ≤ 4.5V		—	—	±100	μA
VH	Input Hysteresis			—	150	—	mV

### Notes:

- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at VCC = 3.3V, +25°C ambient and maximum loading.
- Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
- This parameter is guaranteed but not tested.
- VOH = VCC – 0.6V at rated current.

**Power Supply Characteristics**

Parameters	Description	Test Conditions <sup>(1)</sup>		Min.	Typ <sup>(2)</sup>	Max.	Units
I <sub>CC</sub>	Quiescent Power Supply Current	V <sub>CC</sub> = Max.	V <sub>IN</sub> = GND or V <sub>CC</sub>		0.1	10	μA
ΔI <sub>CC</sub>	Quiescent Power Supply Current TTL Inputs HIGH	V <sub>CC</sub> = Max.	V <sub>IN</sub> = V <sub>CC</sub> – 0.6V <sup>(3)</sup>		2.0	30	μA
I <sub>CCD</sub>	Dynamic Power Supply <sup>(4)</sup>	V <sub>CC</sub> = Max., Outputs Open xOE = xDIR = GND One Bit Toggling 50% Duty Cycle	V <sub>IN</sub> = V <sub>CC</sub> V <sub>IN</sub> = GND		50	75	μA/ MHz
I <sub>C</sub>	Total Power Supply Current <sup>(6)</sup>	V <sub>CC</sub> = Max., Outputs Open f <sub>i</sub> = 10 MHz 50% Duty Cycle xOE = xDIR = GND One Bit Toggling	V <sub>IN</sub> = V <sub>CC</sub> – 0.6V V <sub>IN</sub> = GND		0.5	0.8	mA
		V <sub>CC</sub> = Max., Outputs Open f <sub>i</sub> = 2.5 MHz 50% Duty Cycle xOE = xDIR = GND 16 Bits Toggling	V <sub>IN</sub> = V <sub>CC</sub> – 0.6V V <sub>IN</sub> = GND		2.0	3.3 <sup>(5)</sup>	

**Notes:**

- For Max. or Min. condition, use appropriate value specified under Electrical Characteristics for the applicable device.
- Typical values are at V<sub>CC</sub> = 3.3V, +25°C ambient.
- Per TTL driven input; all other inputs at V<sub>CC</sub> or GND.
- This parameter is not directly testable, but is derived for use in Total Power Supply Calculations.
- Values for these conditions are examples of the I<sub>CC</sub> formula. These limits are guaranteed but not tested.
- I<sub>C</sub> = I<sub>QUIESCENT</sub> + I<sub>INPUTS</sub> + I<sub>DYNAMIC</sub>  

$$I_C = I_{CC} + \Delta I_{CC} D_H N_T + I_{CCD} (f_{CP}/2 + f_i N_i)$$

I<sub>CC</sub> = Quiescent Current (I<sub>CC1</sub>, I<sub>CC2</sub> and I<sub>CC3</sub>)  
ΔI<sub>CC</sub> = Power Supply Current for a TTL High Input  
D<sub>H</sub> = Duty Cycle for TTL Inputs High  
N<sub>T</sub> = Number of TTL Inputs at D<sub>H</sub>  
I<sub>CCD</sub> = Dynamic Current Caused by an Input Transition Pair (HLH or LHL)  
f<sub>CP</sub> = Clock Frequency for Register Devices (Zero for Non-Register Devices)  
N<sub>CP</sub> = Number of Clock Inputs at f<sub>CP</sub>  
f<sub>i</sub> = Input Frequency  
N<sub>i</sub> = Number of Inputs at f<sub>i</sub>  
All currents are in milliamps and all frequencies are in megahertz.

**Switching Characteristics over Operating Range<sup>(1)</sup>**

Parameters	Description	Conditions <sup>(2)</sup>	LPT16245		LPT16245A		LPT16245C		Units
			Com.		Com.		Com.		
			Min. <sup>(3)</sup>	Max.	Min. <sup>(3)</sup>	Max.	Min. <sup>(3)</sup>	Max.	
tPLH tPHL	Propagation Delay A to B, B to A	CL = 50 pF RL = 500Ω	1.5	5.2	1.5	4.6	1.5	4.1	ns
tpZH tpZL	Output Enable Time x $\overline{\text{OE}}$ to A or B		1.5	7.2	1.5	6.2	1.5	5.8	ns
tpHZ tPLZ	Output Disable Time <sup>(4)</sup> x $\overline{\text{OE}}$ to A or B		1.5	7.2	1.5	5.0	1.5	4.8	ns
tpZH tpZL	Output Enable Time xDIR to A or B		1.5	7.2	1.5	6.2	1.5	5.8	ns
tpHZ tPLZ	Output Disable Time xDIR to A or B <sup>(4)</sup>		1.5	7.2	1.5	5.0	1.5	4.8	ns
tsk(o)	Output Skew <sup>(5)</sup>			0.5		0.5		0.5	ns

**Notes:**

1. Propagation Delays and Enable/Disable times are with Vcc = 3.3V ±0.3V, normal range. For Vcc = 2.7V, extended range, all Propagation Delays and Enable/Disable times should be degraded by 20%.
2. See test circuit and wave forms.
3. Minimum limits are guaranteed but not tested on Propagation Delays.
4. This parameter is guaranteed but not production tested.
5. Skew between any two outputs, of the same package, switching in the same direction. This parameter is guaranteed by design.