

Product Features

- Functionally compatible with FCT3, LVT, and 74 series 16244 families of products
- 3-State outputs
- 5V Tolerant inputs and outputs
- 2.0V-3.6V V_{CC} supply operation
- Balanced sink and source output drives (24mA)
- Low ground bounce outputs
- Power Down High Impedance inputs and outputs
- Supports live insertion
- ESD Protection exceeds 2000V, Human Body Model
200V, Machine Model
- Packages available:
 - 48-pin 240-mil wide plastic TSSOP (A)
 - 48-pin 300-mil wide plastic SSOP (V)

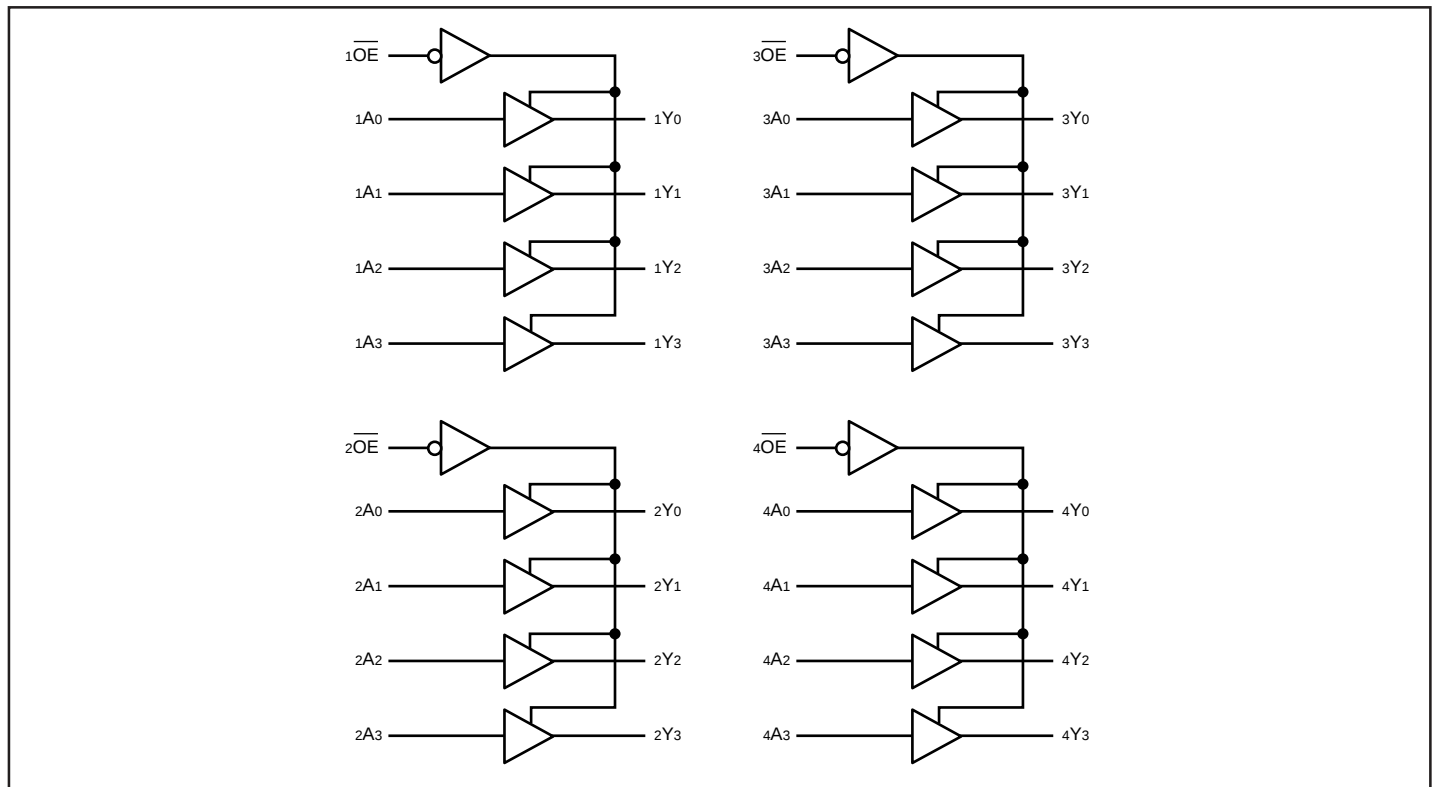
Product Description

Pericom Semiconductor's PI74LCX series of logic circuits are produced using the Company's advanced 0.6 micron CMOS technology achieving high speed while maintaining low power operation.

The PI74LCX16244 is a 16-bit buffer/line driver designed for driving memory loads. With its balanced-drive characteristics, this high-speed, low power device provides lower ground bounce, transmission line matching of signals, fewer line reflections and lower EMI and RFI effects. This makes it ideal for driving on-board buses and transmission lines. This device is designed with three-state controls to operate in a Quad-Nibble, Dual-Byte, or a single 16-bit word mode.

The PI74LCX16244 can be driven from either 3.3V or 5.0V devices allowing this device to be used as a translator in a mixed 3.3V/5.0V system.

Logic Block Diagram



Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	–65°C to +150°C
Ambient Temperature with Power Applied	–40°C to +85°C
Supply Voltage to Ground Potential (Inputs & V _{CC} Only)	–0.5V to +7.0V
Supply Voltage to Ground Potential (Outputs & D/O Only)	–0.5V to +7.0V
DC Input Voltage	–0.5V to +7.0V
DC Output Current	120mA
Power Dissipation	1.0W

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Recommended Operating Conditions

Symbol	Parameter		Min.	Max.	Units
V _{CC}	Supply Voltage	Operating	2.0	3.6	V
		Data Retention	1.5	3.6	
V _I	Input Voltage		0	5.5	
V _O	Output Voltage	HIGH or LOW State	0	V _{CC}	
		3-State	0	5.5	
I _{OH} /I _{OL}	Output Current	V _{CC} = 3.0V-3.6V	—	±24	mA
		V _{CC} = 2.7V	—	±12	
T _A	Free-Air Operating Temperature		–40	+85	°C
ΔtΔV	Input Edge Rate	V = 0.8V-2.0V, V _{CC} = 3.0V	0	10	ns/V

Product Pin Description

Pin Name	Description
$\overline{xOE}$	3-State Output Enable Inputs (Active LOW)
xAX	Inputs
xYx	3-State Outputs
GND	Ground
VCC	Power

Truth Table⁽¹⁾

Inputs ⁽¹⁾		Outputs ⁽¹⁾
$\overline{xOE}$	xAX	xYx
L	L	L
L	H	H
H	X	Z

Note:

1. H = High Voltage Level, X = Don't Care,
L = Low Voltage Level, Z = High Impedance

Product Pin Configuration

$\overline{1OE}$	1		48	$\overline{2OE}$
$1Y0$	2		47	$1A0$
$1Y1$	3		46	$1A1$
GND	4		45	GND
$1Y2$	5		44	$1A2$
$1Y3$	6		43	$1A3$
VCC	7		42	VCC
$2Y0$	8		41	$2A0$
$2Y1$	9		40	$2A1$
GND	10		39	GND
$2Y2$	11	48-Pin	38	$2A2$
$2Y3$	12	A, V	37	$2A3$
$3Y0$	13		36	$3A0$
$3Y1$	14		35	$3A1$
GND	15		34	GND
$3Y2$	16		33	$3A2$
$3Y3$	17		32	$3A3$
VCC	18		31	VCC
$4Y0$	19		30	$4A0$
$4Y1$	20		29	$4A1$
GND	21		28	GND
$4Y2$	22		27	$4A2$
$4Y3$	23		26	$4A3$
$\overline{4OE}$	24		25	$\overline{3OE}$

DC Electrical Characteristics (Over the Operating Range, $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$, $V_{CC} = 2.7\text{V}$ to 3.6V)

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ. ⁽²⁾	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level		2.0	—	—	V
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW Level		—	—	0.8	
V_{OH}	Output HIGH Voltage	$V_{CC} = 2.7\text{-}3.6$	$I_{OH} = -0.1\text{mA}$	$V_{CC} - 0.2$	—	—	
		$V_{CC} = 2.7$	$I_{OH} = -12\text{mA}$	2.2	—	—	
		$V_{CC} = 3.0$	$I_{OH} = -18\text{mA}$	2.4	—	—	
			$I_{OH} = -24\text{mA}$	2.2	—	—	
V_{OL}	Output LOW Voltage	$V_{CC} = 2.7\text{-}3.6$	$I_{OL} = 0.1\text{mA}$	—	—	0.2	
		$V_{CC} = 2.7$	$I_{OL} = 12\text{mA}$	—	—	0.4	
		$V_{CC} = 3.0$	$I_{OL} = 16\text{mA}$	—	—	0.4	
			$I_{OL} = 24\text{mA}$	—	—	0.55	
V_{IK}	Clamp Diode Voltage	$V_{CC} = \text{Min.}, I_{IN} = -18\text{mA}$		—	-0.7	-1.2	
I_I	Input Leakage Current	$0 \leq V_I \leq 5.5\text{V}$	$V_{CC} = 2.7\text{-}3.6$	—	—	± 5	μA
I_{OZ}	Tri-State Output Leakage	$0 \leq V_O \leq 5.5\text{V}$ $V_I = V_{IH} \text{ OR } V_{IL}$	$V_{CC} = 2.7\text{-}3.6$	—	—	± 5	
I_{OFF}	Power Down Disable	$V_{CC} = 0\text{V}, V_{IN} \text{ or } V_{OUT} \leq 5.5\text{V}$		—	—	10	
I_{CC}	Quiescent Power Supply Current	$V_{CC} = \text{Max.}$	$V_{IN} = \text{GND or } V_{CC}$	—	0.1	10	
ΔI_{CC}	Quiescent Power Supply Current TTL Inputs HIGH	$V_{CC} = \text{Max.}$	$V_{IN} = V_{CC} - 0.6\text{V}^{(3)}$	—	—	500	

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at $V_{CC} = 3.3\text{V}$, $+25^\circ\text{C}$ ambient.
3. Per TTL driven input; all other inputs at V_{CC} or GND.

Capacitance

Parameters	Description	Test Conditions	Typ.	Units
C_{IN}	Input Capacitance	$V_{CC} = \text{Open}, V_I = 0\text{V or } V_{CC}$	7	pF
C_{OUT}	Output Capacitance	$V_{CC} = 3.3\text{V}, V_I = 0\text{V or } V_{CC}$	8	
C_{PD}	Power Dissipation Capacitance	$V_{CC} = 3.3\text{V}, V_I = 0\text{V or } V_{CC}, F = 10 \text{ MHz}$	20	

Switching Characteristics over Operating Range

Parameters	Description	Conditions	$V_{CC} = 3.3V \pm 0.3V$		$V_{CC} = 2.7V$		Units
			Min.	Max.	Min.	Max.	
t_{PHL}	Propagation Delay, Dxx to Oxx	$C_L = 50pF$ $R_L = 500\Omega$	1.5	4.5	1.5	5.2	ns
t_{PZH}	Output Enable time		1.5	5.5	1.5	6.3	
t_{PHZ}	Output Disable Time		1.5	5.4	1.5	5.7	
$t_{sk(o)}$	Output Skew ⁽¹⁾		—	1.0	—	—	

Note:

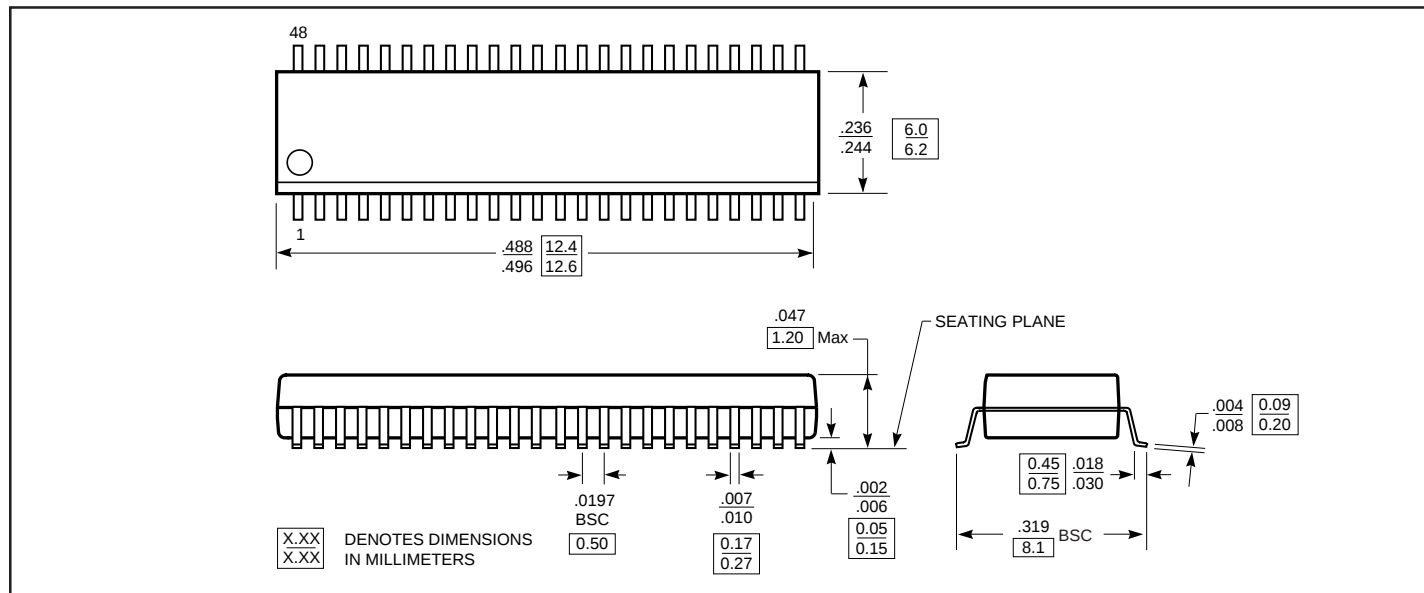
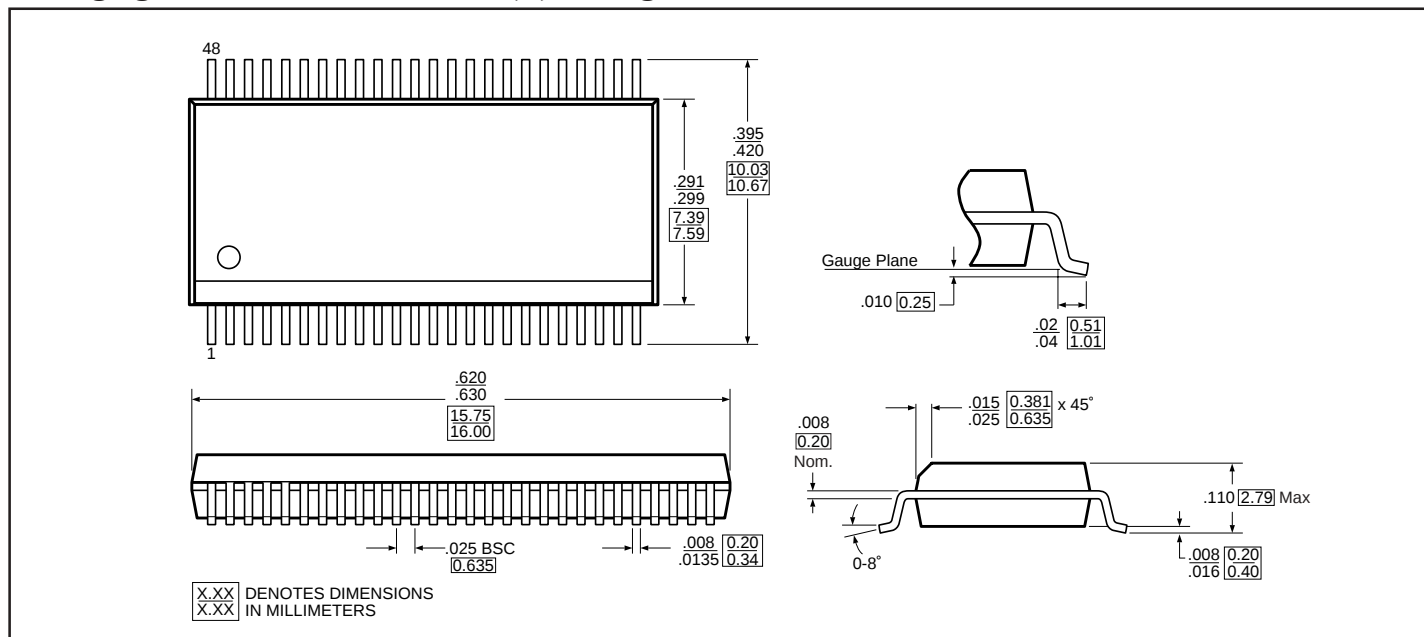
1. Skew between any two outputs, of the same package, switching in the same direction.

Dynamic Switching Characteristics ($T_A = +25^\circ C$)

Parameters	Description	Test Conditions ⁽¹⁾	Typ.	Units
V_{OLP}	Dynamic LOW Peak Voltage	$V_{CC} = 3.3V$, $C_L = 50pF$ $V_{IH} = 3.3V$, $V_{IL} = 0V$	0.8	V
V_{OLV}	Dynamic LOW Valley Voltage	$V_{CC} = 3.3V$, $C_L = 50pF$ $V_{IH} = 3.3V$, $V_{IL} = 0V$		

Note:

1. Measured with n–1 outputs switching from High-to-Low or Low-to-High. The remaining output is measured in the LOW state.

Packaging Mechanical: 48-Pin TSSOP (A) Package

Packaging Mechanical: 48-Pin SSOP (V) Package

Ordering Information

Ordering Code	Description
PI74LCX16244A	48-Pin 240-mil wide Plastic TSSOP (A)
PI74LCX16244V	48-Pin 300-mil wide Plastic SSOP (V)