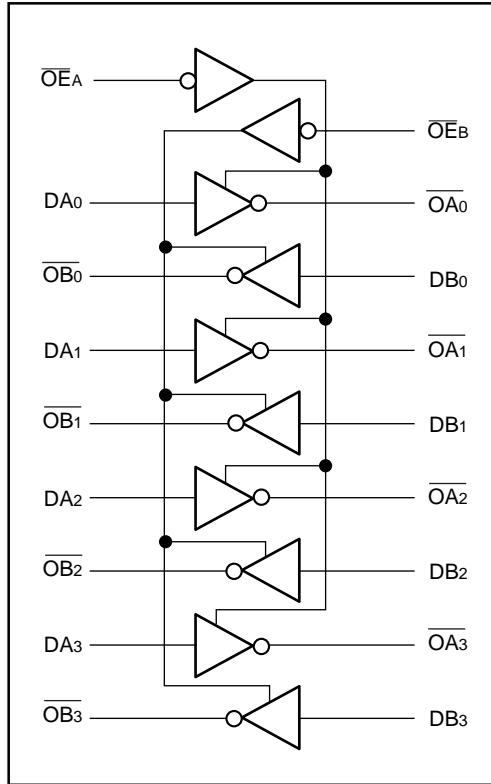


Fast CMOS 3.3V 8-Bit Buffer/Line Driver

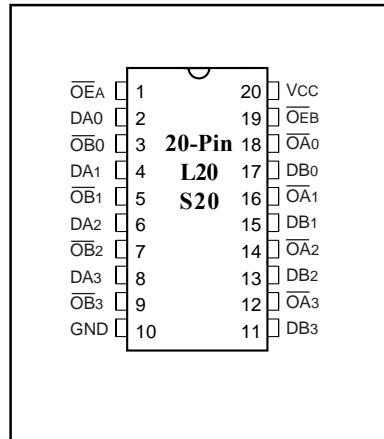
Product Features

- Functionally compatible with FCT3, LVT, and 74 series 240 families of products
- Tri-State outputs
- 5V Tolerant inputs and outputs
- 2.0V-3.6V Vcc supply operation
- Balanced sink and source output drives (24 mA)
- Low ground bounce outputs
- Power Down High Impedance inputs and outputs
- Supports live insertion
- ESD Protection exceeds 2000V, Human Body Model
200V, Machine Model
- Packages available:
– 20-pin 173-mil wide plastic TSSOP (L)
– 20-pin 300-mil wide plastic SOIC (S)

Logic Block Diagram



Product Pin Configuration



Product Pin Description

| Pin Name | Description |
|---------------------------------------|-------------------------------------------|
| \overline{OE}_A , \overline{OE}_B | 3-State Output Enable Inputs (Active LOW) |
| D _{XX} | Inputs |
| \overline{O}_{XX} | Outputs |
| GND | Ground |
| VCC | Power |

Truth Table⁽¹⁾

| Inputs | | | Outputs |
|-------------------|-------------------|-----------------|---------------------|
| \overline{OE}_A | \overline{OE}_B | D _{XX} | \overline{O}_{XX} |
| L | L | L | H |
| L | L | H | L |
| H | H | X | Z |

Note:

1. H = High Voltage Level, X = Don't Care,
L = Low Voltage Level, Z = High Impedance

Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

| | |
|---------------------------------------------------------------|-----------------|
| Storage Temperature | -65°C to +150°C |
| Ambient Temperature with Power Applied | -40°C to +85°C |
| Supply Voltage to Ground Potential (Inputs & Vcc Only) | -0.5V to +7.0V |
| Supply Voltage to Ground Potential (Outputs & D/O Only) | -0.5V to +7.0V |
| DC Input Voltage | -0.5V to +7.0V |
| DC Output Current | 120 mA |
| Power Dissipation | 1.0W |

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Recommended Operating Conditions

| Symbol | Parameter | | Min. | Max. | Units |
|---------|--------------------------------|---------------------------|------|------|-------|
| Vcc | Supply Voltage | Operating | 2.0 | 3.6 | V |
| | | Data Retention | 1.5 | 3.6 | |
| Vi | Input Voltage | | 0 | 5.5 | |
| Vo | Output Voltage | HIGH or LOW State | 0 | Vcc | |
| | | TRI-State | 0 | 5.5 | |
| IoH/IOL | Output Current | Vcc = 3.0V-3.6V | — | ±24 | mA |
| | | Vcc = 2.7V | — | ±12 | |
| Ta | Free-Air Operating Temperature | | -40 | +85 | °C |
| Δt/ΔV | Input Edge Rate | V = 0.8V-2.0V, Vcc = 3.0V | 0 | 10 | ns/V |

DC Electrical Characteristics (Over the Operating Range, $T_A = -40^\circ\text{C}$ to $+85^\circ\text{C}$, $V_{CC} = 2.7\text{V}$ to 3.6V)

| Parameters | Description | Test Conditions ⁽¹⁾ | | Min. | Typ ⁽²⁾ | Max. | Units |
|-----------------|---------------------------------------------------|--------------------------------------------------------------------|------------------------------------------|---------------------|--------------------|---------|---------------|
| V_{IH} | Input HIGH Voltage | Guaranteed Logic HIGH Level | | 2.0 | — | — | V |
| V_{IL} | Input LOW Voltage | Guaranteed Logic LOW Level | | — | — | 0.8 | |
| V_{OH} | Output HIGH Voltage | $V_{CC} = 2.7\text{-}3.6$ | $I_{OH} = -0.1 \text{ mA}$ | $V_{CC}\text{-}0.2$ | — | — | |
| | | $V_{CC} = 2.7$ | $I_{OH} = -12 \text{ mA}$ | 2.2 | — | — | |
| | | $V_{CC} = 3.0$ | $I_{OH} = -18 \text{ mA}$ | 2.4 | — | — | |
| | | | $I_{OH} = -24 \text{ mA}$ | 2.2 | — | — | |
| V_{OL} | Output LOW Voltage | $V_{CC} = 2.7\text{-}3.6$ | $I_{OL} = 0.1 \text{ mA}$ | — | — | 0.2 | μA |
| | | $V_{CC} = 2.7$ | $I_{OL} = 12 \text{ mA}$ | — | — | 0.4 | |
| | | $V_{CC} = 3.0$ | $I_{OL} = 16 \text{ mA}$ | — | — | 0.4 | |
| | | | $I_{OL} = 24 \text{ mA}$ | — | — | 0.55 | |
| V_{IK} | Clamp Diode Voltage | $V_{CC} = \text{Min.}, I_{IN} = -18 \text{ mA}$ | | — | -0.7 | -1.2 | μA |
| I_I | Input Leakage Current | $0 \leq V_I \leq 5.5\text{V}$ | $V_{CC} = 2.7\text{-}3.6$ | — | — | ± 5 | |
| I_{OZ} | Tri-State Output Leakage | $0 \leq V_O \leq 5.5\text{V}$ $V_I = V_{IH} \text{ or } V_{IL}$ | $V_{CC} = 2.7\text{-}3.6$ | — | — | ± 5 | |
| I_{OFF} | Power Down Disable | $V_{CC} = 0\text{V}, V_{IN} \text{ or } V_{OUT} \leq 5.5\text{V}$ | | — | — | 10 | |
| I_{CC} | Quiescent Power Supply Current | $V_{CC} = \text{Max.}$ | $V_{IN} = \text{GND} \text{ or } V_{CC}$ | — | 0.1 | 10 | |
| ΔI_{CC} | Quiescent Power Supply Current TTL Inputs HIGH | $V_{CC} = \text{Max.}$ | $V_{IN} = V_{CC} - 0.6\text{V}^{(3)}$ | — | — | 500 | |

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at $V_{CC} = 3.3\text{V}$, $+25^\circ\text{C}$ ambient.
3. Per TTL driven input; all other inputs at V_{CC} or GND.

Capacitance

| Parameters | Description | Test Conditions | Typ. | Units |
|------------|-------------------------------|--------------------------------------------------------------------------------|------|-------------|
| C_{IN} | Input Capacitance | $V_{CC} = \text{Open}, V_I = 0\text{V} \text{ or } V_{CC}$ | 7 | pF |
| C_{OUT} | Output Capacitance | $V_{CC} = 3.3\text{V}, V_I = 0\text{V} \text{ or } V_{CC}$ | | |
| C_{PD} | Power Dissipation Capacitance | $V_{CC} = 3.3\text{V}, V_I = 0\text{V} \text{ or } V_{CC}, F = 10 \text{ MHz}$ | | |

Switching Characteristics over Operating Range

| Parameters | Description | Conditions | $V_{CC} = 3.3V \pm 0.3V$ | | $V_{CC} = 2.7V$ | | Units |
|--------------|---------------------------------|--------------------------------------------|--------------------------|------|-----------------|------|-------|
| | | | Min. | Max. | Min. | Max. | |
| tPLH tPHL | Propagation Delay Dxx to Oxx | $C_L = 50 \text{ pF}$ $R_L = 500\Omega$ | 1.5 | 6.5 | 1.5 | 7.5 | ns |
| tpZH tpZL | Output Enable time | | 1.5 | 8.0 | 1.5 | 9.0 | |
| tPHZ tPLZ | Output Disable Time | | 1.5 | 7.0 | 1.5 | 8.0 | |
| tsk(o) | Output Skew ⁽¹⁾ | | — | 1.0 | — | — | |

Note:

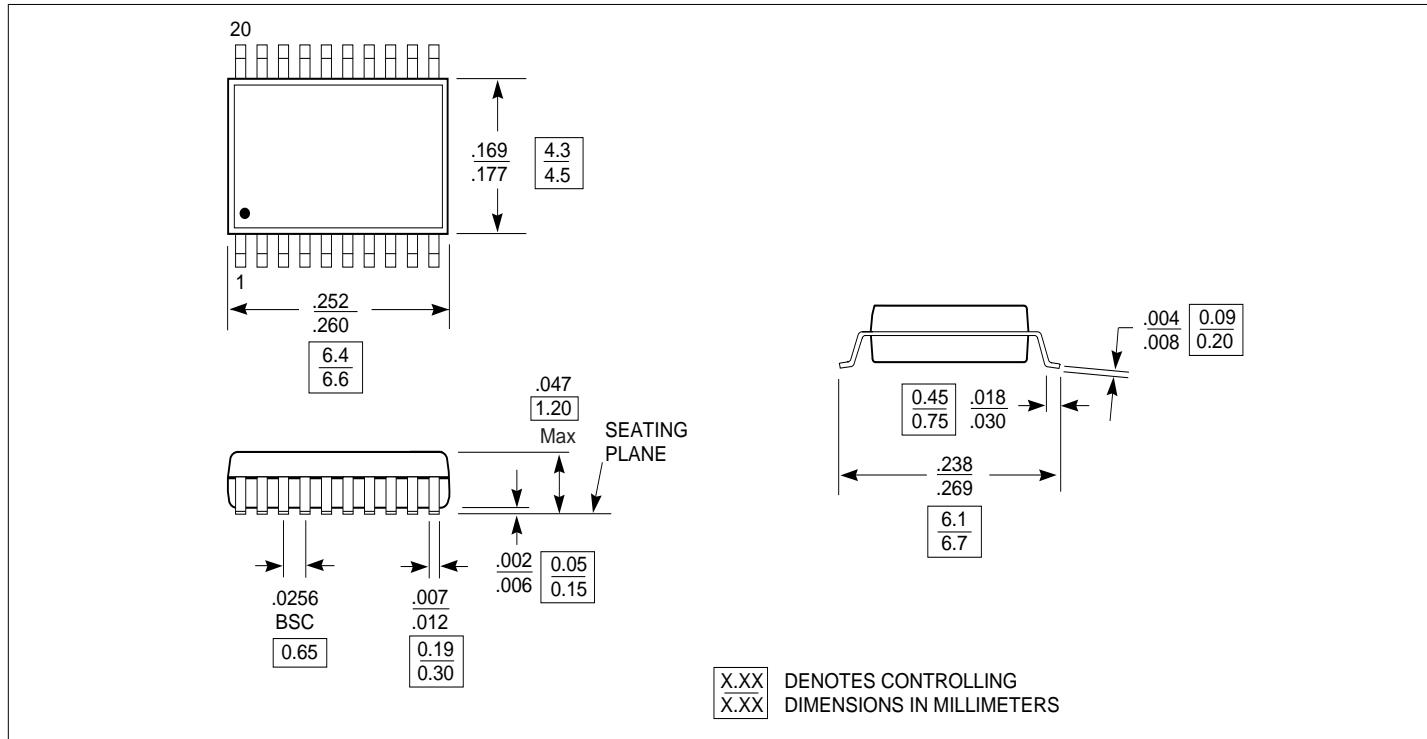
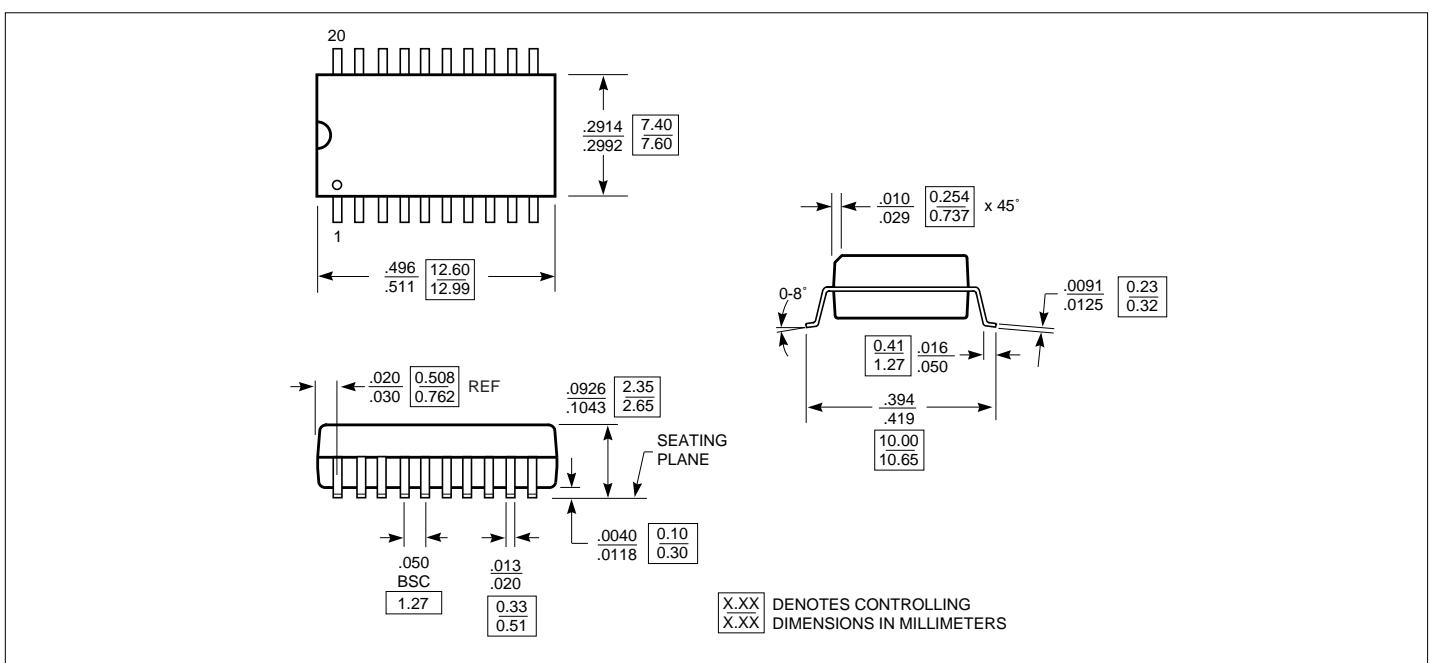
1. Skew between any two outputs of the same package, switching in the same direction.

Dynamic Switching Characteristics ($T_A = +25^\circ C$)

| Parameters | Description | Test Conditions ⁽¹⁾ | Typ. | Units |
|------------|----------------------------|----------------------------------------------------------------------|------|-------|
| VOLP | Dynamic LOW Peak Voltage | $V_{CC} = 3.3V, C_L = 50 \text{ pF}$ $V_{IH} = 3.3V, V_{IL} = 0V$ | 0.8 | V |
| VOLV | Dynamic LOW Valley Voltage | $V_{CC} = 3.3V, C_L = 50 \text{ pF}$ $V_{IH} = 3.3V, V_{IL} = 0V$ | | |

Note:

1. Measured with n-1 outputs switching from High-to-Low or Low-to-High. The remaining output is measured in the LOW state.

20-pin 173-mil wide plastic TSSOP (L) Package

20-pin 300-mil wide plastic SOIC (S) Package

 Thermal Characteristics can be found on the web: <http://www.pericom.com/packages/>

Ordering Information

Note:

1. Thermal characteristics can be found on the web at <http://www.pericom.com/packaging>