

PI3VT3861

10-Bit, 2-Port, Low Voltage Translator Bus Switch

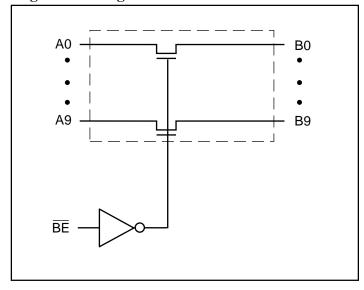
Product Features

- Near zero propagation delay
- Low ON resistance switches connect inputs to outputs
- 2.5V Supply Voltage Operation
- 2.5V or 1.8V switching
- Packages available:
 - -24-pin, 150 mil wide plastic QSOP(Q)
 - -24-pin, 173 mil wide plastic TSSOP(L)

Applications

- 3.3V to 2.5V level shifting (voltage translation)
- 2.5V to 1.8V level shifting (voltage translation)

Logic Block Diagram



Truth Table(1)

Function	BE	A0-9
Disconnect	Н	Hi-Z
Connect	L	B0-9

Notes:

1. H=High Voltage Level L=Low Voltage Level Hi-Z=High Impedance

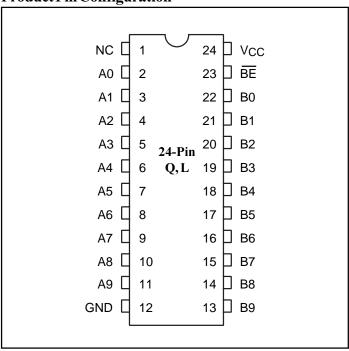
Product Description

Pericom Semiconductor's PI3VT series of logic circuits are produced using the Company's advanced submicron CMOS technology, achieving industry leading performance.

The PI3VT3861, either a 2.5V or 3.3V 10-bit, 2-port bus switch, is designed with a low ON resistance (5 ohms). The switches are turned ON or enabled by the Bus Enable (BEn) input signal.

Given a 3.3V supply and an input voltage of 3.3V, the switch translates to an output voltage of 2.5V. Similarly, when operated with a 2.5V supply and given 2.5V inputs, the device translates the outputs to 1.8V.

Product Pin Configuration



Product Pin Description

Pin Name	Description
$\overline{ m BE}$	Bus Enable Input (Active LOW)
A0-9	Bus A
В0-9	Bus B
GND	Ground
V_{CC}	Power



Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

+150°C
to +85°C
to+4.6V
to+5.5V
120mA
0.5W
1

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over Operating Range, $T_A = -40$ °C to +85°C, $V_{CC} = 3.3$ V ± 10 %)

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
V _{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	1.8		V _{CC} +0.3V	V
$V_{ m IL}$	Input LOW Voltage	Guaranteed Logic LOW Level	-0.5		0.8	v
I_{IH}	Input HIGH Current	$V_{CC} = Max., V_{IN} = V_{CC}$			±1	
$I_{ m IL}$	Input LOW Current	$V_{CC} = Max., V_{IN} = GND$			±1	μA
I _{OZH}	High Impedance Output Current	$0 \le A, B \le V_{CC}$			±1	
		$V_{CC} = Min., V_{IN} = 0.0V, I_{ON} = 8mA$		5	8	
D	Switch On Decistones	$V_{CC} = Min., V_{IN} = 1.7V, I_{ON} = 8mA$		10	22	Ω
R _{ON}	RON Switch On Resistance	$V_{CC} = 2.3V$, $V_{IN} = 0.0V$, $I_{ON} = 8mA$		6	9	52
		$V_{CC} = 2.3 \text{ V}, V_{IN} = 1.3 \text{ V}, I_{ON} = 8 \text{mA}$		25	30	
17	Pagg Valtaga	$V_{IN} = V_{CC} = 3.3V$, $I_{OUT} = -5\mu A$	2.3	2.5	2.9	V
V_{P}	Pass Voltage	$V_{IN} = V_{CC} = 2.5V$, $I_{OUT} = -5\mu A$		1.8		v

Capacitance ($T_A = 25$ °C, f = 1 MHz)

Parameters (5)	Description	Test Conditions	Тур.	Units
C_{IN}	Input Capacitance		3.5	
$C_{ m OFF}$	A/B Capacitance, Switch Off	$V_{IN} = 0V$	5.0	pF
C _{ON}	A/B Capacitance, Switch On		10.0	

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. Typical values are at $V_{CC} = 3.3V$, $T_A = 25$ °C ambient and maximum loading.
- 3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
- 4. Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A,B) pins.

2

5. This parameter is determined by device characterization but is not production tested.

PS8534 10/03/01



Power Supply Characteristics

Parameters	Description	Test Conditions	s ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
ICC	Quiescent Power Supply Current	$V_{CC} = Max.$	$V_{IN} = GND \text{ or } V_{CC}$			3	μA
ΔI _{CC}	Supply Current per Input HIGH	V _{CC} = Max.	$V_{IN} = 3V^{(3)}$			50	•

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- 2. Typical values are at $V_{CC} = 3.3V, +25^{\circ}C$ ambient.
- 3. Per driven input (control input only); A and B pins do not contribute to ΔI_{CC} .

Switching Characteristics over 3.3V ±0.3V Operating Range

Davamatava	Description	Condition ⁽¹⁾	Co	Units	
Parameters	Description		Min.	Max.	Units
t _{PLH} t _{PHL}	Propagation Delay ^(2,3) Ax to Bx	$C_L = 50 \text{pF},$ $R_L = 500 \text{ Ohms}$		0.25	
t _{PZH} t _{PZL}	Bus Enable Time BE to Ax or Bx	$C_{\rm L} = 50 \rm pF,$ $R_{\rm L} = 500 \rm \ Ohms$	1.5	6.5	ns
t _{PHZ} t _{PLZ}	Bus Disable Time BE to Ax or Bx	$\begin{array}{c} -R_{L} = 500 \text{ Ohms} \\ R = 500 \text{ Ohms} \end{array}$	1.5	5.5	

Switching Characteristics over 2.5V ±0.2V Operating Range

Davamatava	Dogovintion	Condition ⁽¹⁾	Co	TT *4	
Parameters	Description		Min.	Max.	Units
t _{PLH} t _{PHL}	Propagation Delay ^(2,3) Ax to Bx	$C_L = 50 \text{pF},$ $R_L = 500 \text{ Ohms}$		0.25	
t _{PZH} t _{PZL}	Bus Enable Time BE to Ax or Bx	$C_L = 50 \text{pF},$	1.5	9.8	ns
t _{PHZ} t _{PLZ}	Bus Disable Time BE to Ax or Bx	$R_{\rm L} = 500 \text{ Ohms}$ R = 500 Ohms	1.5	8.3	

Notes:

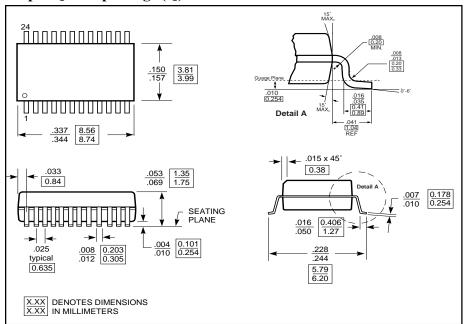
- 1. See test circuit and waveforms.
- 2. This parameter is guaranteed but not tested on Propagation Delays.
- 3. The bus switch contributes no propagational delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

3

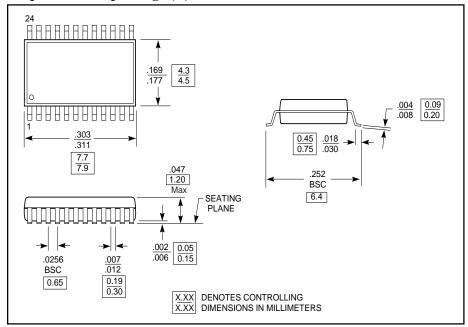
PS8534 10/03/01



24-pin QSOP package (Q)



24-pin TSSOP package (L)



Ordering Information

Part	Pin-Package	Temperature
PI3VT3861L	24-TSSOP (L)	-40°C to +85°C
PI3VT3861Q	24-QSOP (Q)	-40°C to +85°C

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