

Product Features

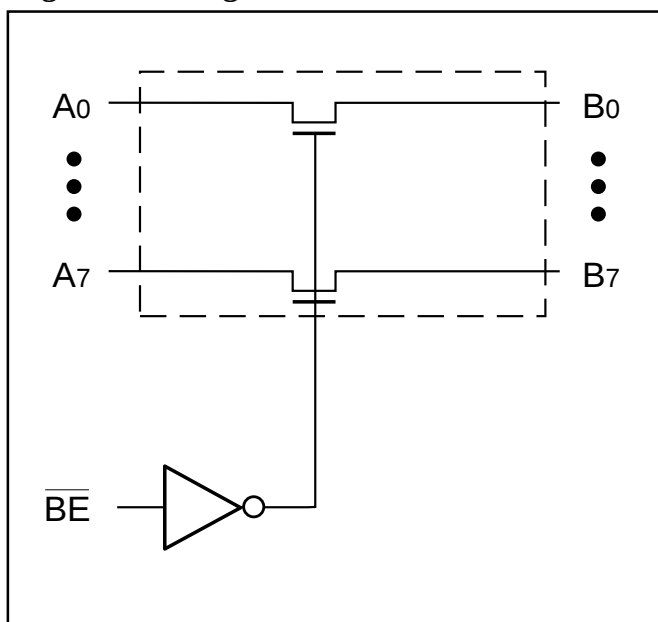
- Near zero propagation delay
- 5Ω switches connect inputs to outputs
- Direct bus connection when switches are on
- Ultra Low Quiescent Power ($0.2\mu\text{A}$ Typical)
 - Ideally suited for notebook applications
- Pin compatible with 74 series 245 logic devices
- Packages available:
 - 20-pin 173 mil wide plastic TSSOP (L)
 - 20-pin 300 mil wide plastic PDIP (P)
 - 20-pin 150 mil wide plastic QSOP (Q)
 - 20-pin 300 mil wide plastic SOIC (S)

Product Description

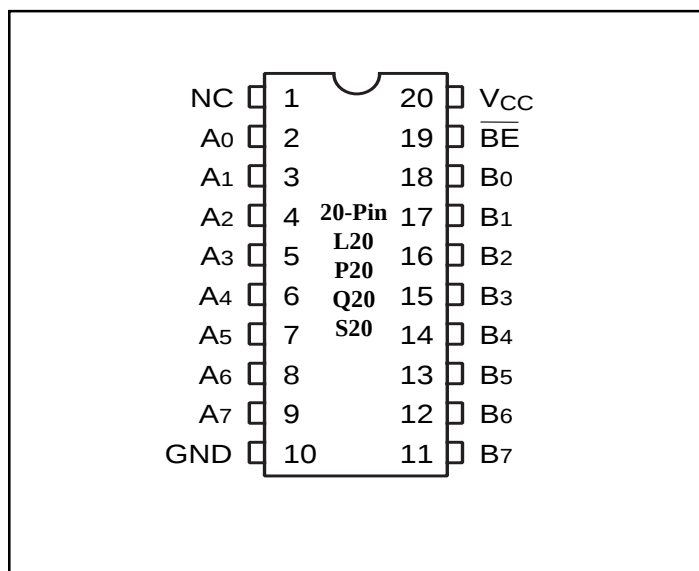
Pericom Semiconductor's PI5C series of logic circuits are produced using the Company's advanced submicron CMOS technology, achieving industry leading performance.

The PI5C3245 is a 8-bit, 2-port bus switch designed with a low ON resistance (5Ω) allowing inputs to be connected directly to outputs. The bus switch creates no additional propagational delay or additional ground bounce noise. The switches are turned on by the Bus Enable ($\overline{\text{BE}}$) input signal. The pinout is compatible with PI74FCT245T (Octal Bidirectional Transceiver).

Logic Block Diagram



Product Pin Configuration



Truth Table⁽¹⁾

Function	$\overline{\text{BE}}$	A0–7
Disconnect	H	Hi-Z
Connect	L	B0–7

Note:

1. H = High Voltage Level
L = Low Voltage Level
Hi-Z = High Impedance

Product Pin Description

Pin Name	Description
$\overline{\text{BE}}$	Bus Enable Input (Active LOW)
A0–7	Bus A
B0–7	Bus B
GND	Ground
VCC	Power

Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	–65°C to +150°C
Ambient Temperature with Power Applied	–40°C to +85°C
Supply Voltage to Ground Potential (Inputs & V _{CC} Only)	–0.5V to +7.0V
Supply Voltage to Ground Potential (Outputs & D/O Only)	–0.5V to +7.0V
DC Input Voltage	–0.5V to +7.0V
DC Output Current	120 mA
Power Dissipation	0.5W

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, T_A = –40°C to +85°C, V_{CC} = 5V ±5%)

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ ⁽²⁾	Max.	Units
V _{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			V
V _{IL}	Input LOW Voltage	Guaranteed Logic LOW Level	–0.5		0.8	V
I _{IH}	Input HIGH Current	V _{CC} = Max., V _{IN} = V _{CC}			±1	μA
I _{IL}	Input LOW Current	V _{CC} = Max., V _{IN} = GND			±1	μA
I _{OZH}	High Impedance Output Current	0 ≤ A, B ≤ V _{CC}			±1	μA
V _{IK}	Clamp Diode Voltage	V _{CC} = Min., I _{IN} = –18mA		–0.7	–1.2	V
I _{OS}	Short Circuit Current ⁽³⁾	A (B) = 0V, B (A) = V _{CC}	100			mA
V _H	Input Hysteresis at Control Pins			150		mV
R _{ON}	Switch On Resistance ⁽⁴⁾	V _{CC} = Min., V _{IN} = 0.0V, I _{ON} = 48mA V _{CC} = Min., V _{IN} = 2.4V, I _{ON} = 15mA		5 10	7 15	Ω

Capacitance (T_A = 25°C, f = 1 MHz)

Parameters ⁽⁵⁾	Description	Test Conditions	Max.	Units
C _{IN}	Input Capacitance	V _{IN} = 0V	6	pF
C _{OFF}	A/B Capacitance, Switch Off	V _{IN} = 0V	6	pF
C _{ON}	A/B Capacitance, Switch On	V _{IN} = 0V	8	pF

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at V_{CC} = 5.0V, T_A = 25°C ambient and maximum loading.
3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
4. Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A,B) pins.
5. This parameter is determined by device characterization but is not production tested.

Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ ⁽²⁾	Max.	Units
I_{CC}	Quiescent Power Supply Current	$V_{CC} = \text{Max.}$	$V_{IN} = \text{GND or } V_{CC}$		0.1	3.0	μA
ΔI_{CC}	Supply Current per Input @ TTL HIGH	$V_{CC} = \text{Max.}$	$V_{IN} = 3.4\text{V}^{(3)}$			2.5	mA
I_{CCD}	Supply Current per Input per MHz ⁽⁴⁾	$V_{CC} = \text{Max.},$ A and B Pins Open $\overline{BE} = \text{GND}$ Control Input Toggling 50% Duty Cycle				0.25	mA/ MHz

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
2. Typical values are at $V_{CC} = 5.0\text{V}$, $+25^\circ\text{C}$ ambient.
3. Per TTL driven input ($V_{IN} = 3.4\text{V}$, control inputs only); A and B pins do not contribute to I_{CC} .
4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

Switching Characteristics over Operating Range

Parameters	Description	Conditions ⁽¹⁾	PI5C3245		Units
			Com.		
			Min.	Max.	
t _{PLH} t _{PHL}	Propagation Delay ^(2,3) Ax to Bx, Bx to Ax	C _L = 50pF R _L = 500Ω		0.25	ns
t _{PZH} t _{PZL}	Bus Enable Time B̄E to Ax or Bx		1.5	6.5	
t _{PHZ} t _{PLZ}	Bus Disable Time B̄E to Ax or Bx		1.5	5.5	

Notes:

1. See test circuit and waveforms.
2. This parameter is guaranteed but not tested on Propagation Delays.
3. The bus switch contributes no propagational delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

Ordering Information

P/N	Package	Width
PI5C3245Q	20 - QSOP (Q20)	150-mil
PI5C3245S	20 - SOIC (Q20)	300-mil