

Product Features

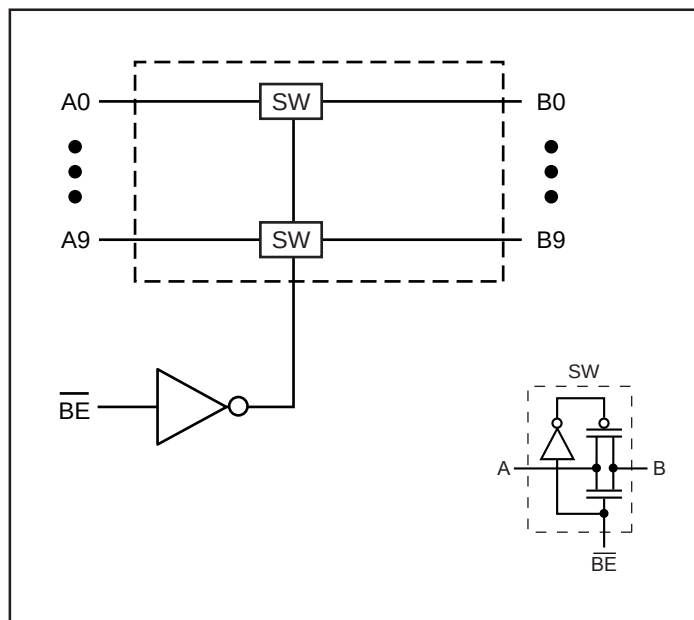
- Near zero propagation delay
- 5-Ohm switches connect inputs to outputs
- Fast Switching Speed - 4.5ns (max.)
- Ultra Low Quiescent Power (0.1µA Typical)
 - Ideally suited for notebook applications
- Packages available:
 - 24-pin 150-mil wide plastic QSOP (Q)
 - 24-pin 173-mil wide plastic TSSOP (L)

Product Description

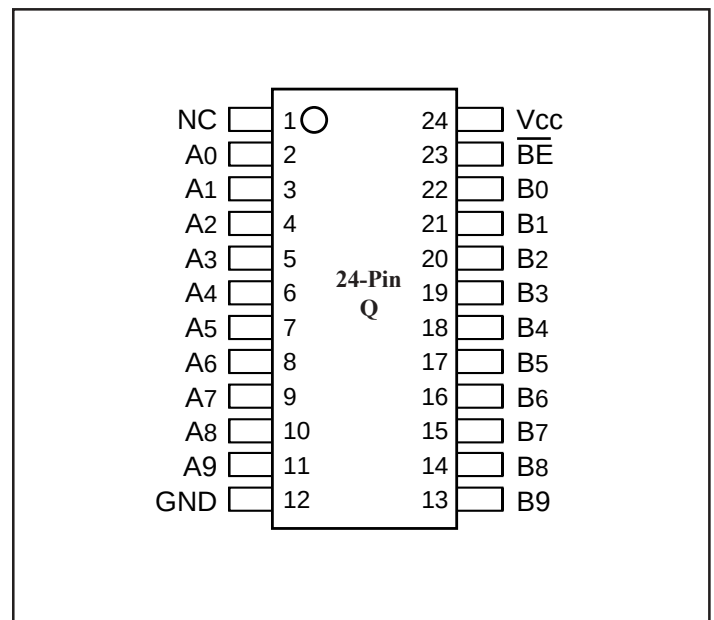
Pericom's Semiconductor's PI3B series of logic circuits are produced using the Company's advanced sub-micron CMOS technology, achieving industry leading performance.

The PI3B3861 is a 10-bit, 3.3 volt, 2-port bus switch designed with a low On-Resistance (5-Ohm) allowing inputs to be connected directly to outputs. The bus switch creates no additional propagational delay or additional ground bounce noise. The switches are turned ON by the Bus Enable (BE) input signal.

Logic Block Diagram



Product Pin Configuration



Truth Table⁽¹⁾

Function	$\overline{BE}$	A0-9
Disconnect	H	Hi-Z
Connect	L	B0-9

Notes: 1. H = High Voltage Level
L = Low Voltage Level
Hi-Z = High Impedance

Product Pin Description

Pin Name	Description
$\overline{BE}$	Bus Enable Input (Active LOW)
A0-9	Bus A
B0-9	Bus B
GND	Ground
VCC	Power

Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	–65°C to +150°C
Ambient Temperature with Power Applied	–40°C to +85°C
Supply Voltage to Ground Potential	–0.5V to +4.6V
DC Input Voltage	–0.5V to +4.6V
DC Output Current	120mA
Power Dissipation	0.5W

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, $T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, $V_{CC} = 3.3\text{V} \pm 10\%$)

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			V
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW Level	–0.5		0.8	V
I_{IH}	Input HIGH Current	$V_{CC} = \text{Max.}, V_{IN} = V_{CC}$			± 1	μA
I_{IL}	Input LOW Current	$V_{CC} = \text{Max.}, V_{IN} = \text{GND}$			± 1	μA
I_{OZH}	High Impedance Output Current	$0 \leq A, B \leq V_{CC}$			± 1	μA
V_{IK}	Clamp Diode Voltage	$V_{CC} = \text{Min.}, I_{IN} = -18\text{mA}$			–1.2	V
R_{ON}	Switch On Resistance ⁽³⁾	$V_{CC} = \text{Min.}, V_{IN} = 0.0\text{V}, I_{ON} = 48\text{mA}$ or 64mA $V_{CC} = \text{Min.}, V_{IN} = 2.4\text{V}, I_{ON} = 15\text{mA}$		5 10	8 17	Ω Ω

Capacitance ($T_A = 25^{\circ}\text{C}$, $f = 1\text{ MHz}$)

Parameters ⁽⁴⁾	Description	Test Conditions	Typ.	Units
C_{IN}	Input Capacitance	$V_{IN} = 0\text{V}$	3.0	pF
C_{OFF}	A/B Capacitance, Switch Off	$V_{IN} = 0\text{V}$	8.0	pF
C_{ON}	A/B Capacitance, Switch On	$V_{IN} = 0\text{V}$	16.0	pF

Notes:

- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at $V_{CC} = 3.3\text{V}$, $T_A = 25^{\circ}\text{C}$ ambient and maximum loading.
- Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A,B) pins.
- This parameter is determined by device characterization but is not production tested.

Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ ⁽²⁾	Max.	Units
I _{CC}	Quiescent Power Supply Current	V _{CC} = Max.	V _{IN} = GND or V _{CC}		0.1	3.0	μA
ΔI _{CC}	Supply Current per Input @ TTL HIGH	V _{CC} = Max.	V _{IN} = 3.0V ⁽³⁾			750	μA

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
2. Typical values are at V_{CC} = 3.3V, +25°C ambient.
3. Per TTL driven input (control input only); A and B pins do not contribute to I_{CC}.

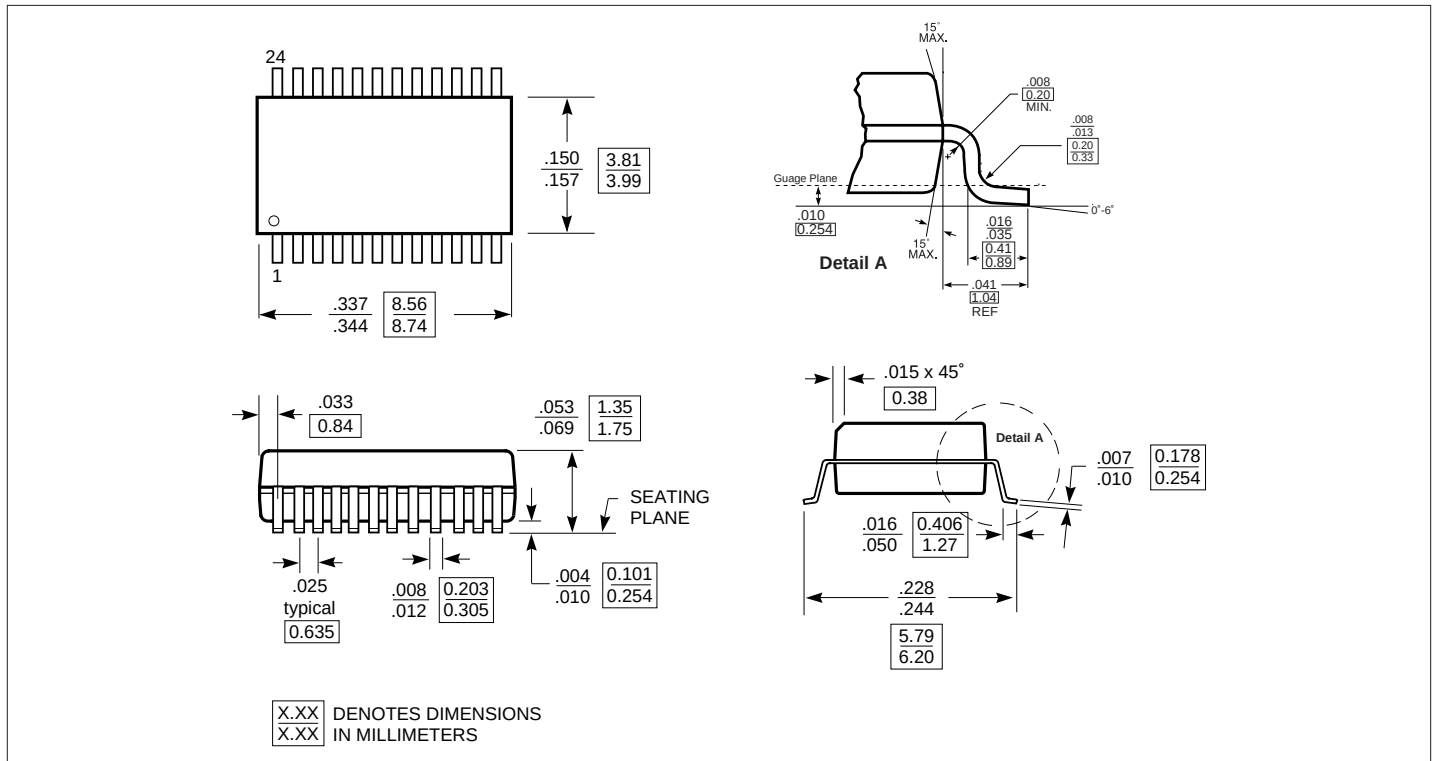
Switching Characteristics over Operating Range

Parameters	Description	Conditions ^(2,3)	PI3B3861		Units
			Com.		
			Min.	Max.	
t _{PH} t _{PL}	Propagation Delay ^(2,3) Ax to Bx, Bx to Ax	C _L = 50pF, R _L = 500Ω		0.25	ns
t _{PZH} t _{PZL}	Bus Enable Time BE to Ax or Bx	C _L = 50pF, R _L = 500Ω	1	4.5	
t _{PHZ} t _{PLZ}	Bus Disable Time BE to Ax or Bx	C _L = 50pF, R _L = 500Ω	1	4.5	

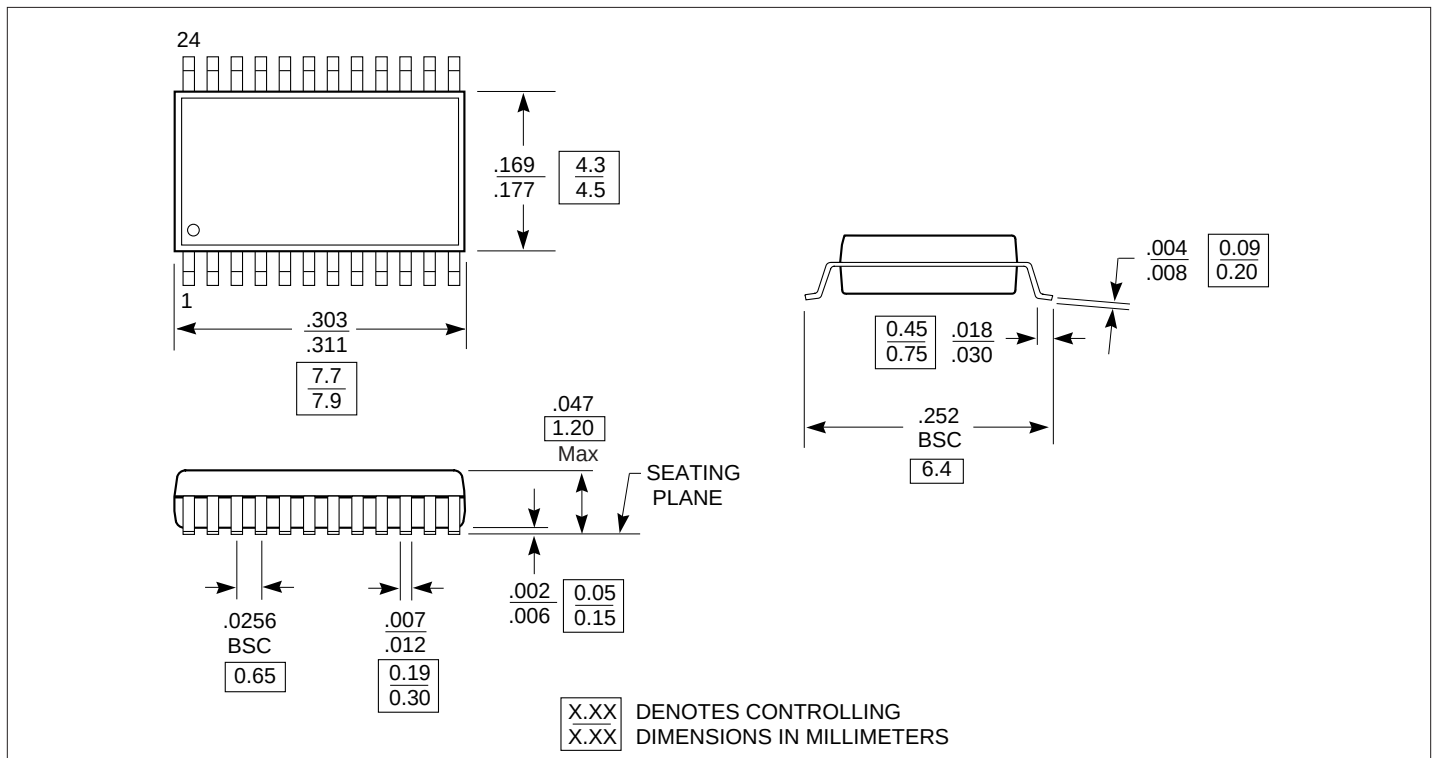
Notes:

1. See test circuit and waveforms.
2. This parameter is guaranteed but not tested on Propagation Delays.
3. The bus switch contributes no propagational delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

Packaging Mechanical: 24-pin QSOP (Q)



Packaging Mechanical: 24-pin TSSOP (L)



Ordering Information

Part Number	Pin / Package	Temperature
PI3B3861Q	24-QSOP (Q)	−40°C to +85°C
PI3B3861L	24-TSSOP (L)	−40°C to +85°C

Applications Information**Logic Inputs**

The logic control inputs can be driven up to +3.6V regardless of the supply voltage. For example, given a +3.3V supply, IN may be driven low to 0V and high to 3.6V. Driving IN Rail-to-Rail[®] minimizes power consumption.

Power-Supply Sequencing and Hot-Plug Information

Proper power-supply sequencing is recommended for all CMOS devices. Always apply V_{CC} and GND before applying signals to input/output or control pins.

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