

VFD Driver/Controller IC**PT6305**

DESCRIPTION

PT6305 is a 48-Bit High Voltage Display Driver utilizing CMOS technology specially designed for VFD display panels. It provides a 48-bit shift register, a 48-bit latch and a high-voltage CMOS driver. The logic circuit operates on a 5 volt power supply (CMOS level input) making it possible for the PT6305 to be used in conjunction with a microcomputer. The driver block consists of a 80 volt, 50 mA (max.) high voltage output buffer. Pin assignments and application circuits are optimized for easy PCB layout and cost saving benefits.

FEATURES

- CMOS technology
- Low power consumption
- 48-bit shift registers
- Data controlled via external transfer clock and latch
- High speed data transfer
- Wide operating temperature range: $T_a = -40$ to $+85^{\circ}\text{C}$
- High voltage output (80V, 50 mA max.)
- Polarities of all drivers may be inverted by using PCB pin
- Available in 100-pins QFP package

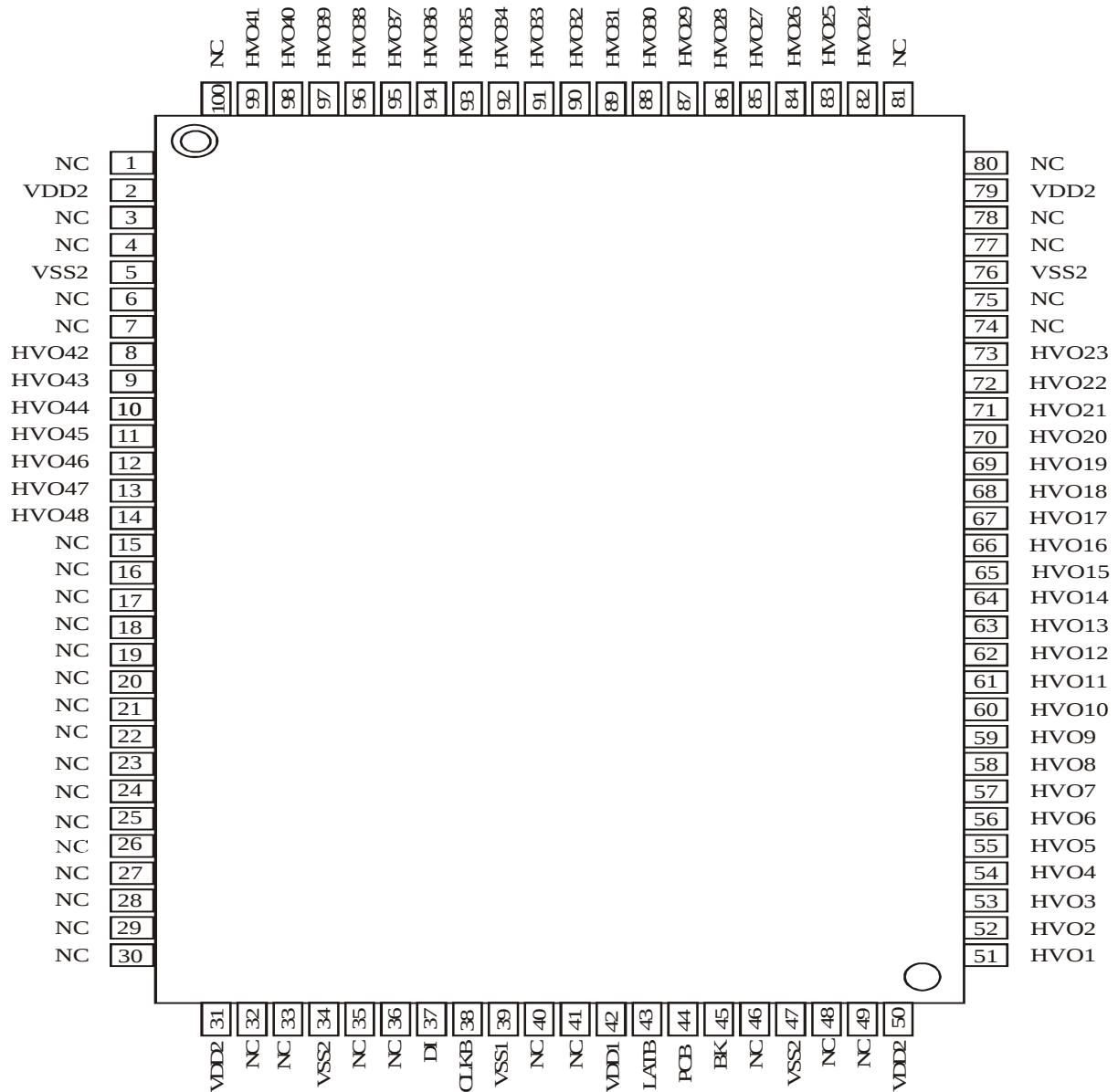
APPLICATION

- Microcomputer peripherals

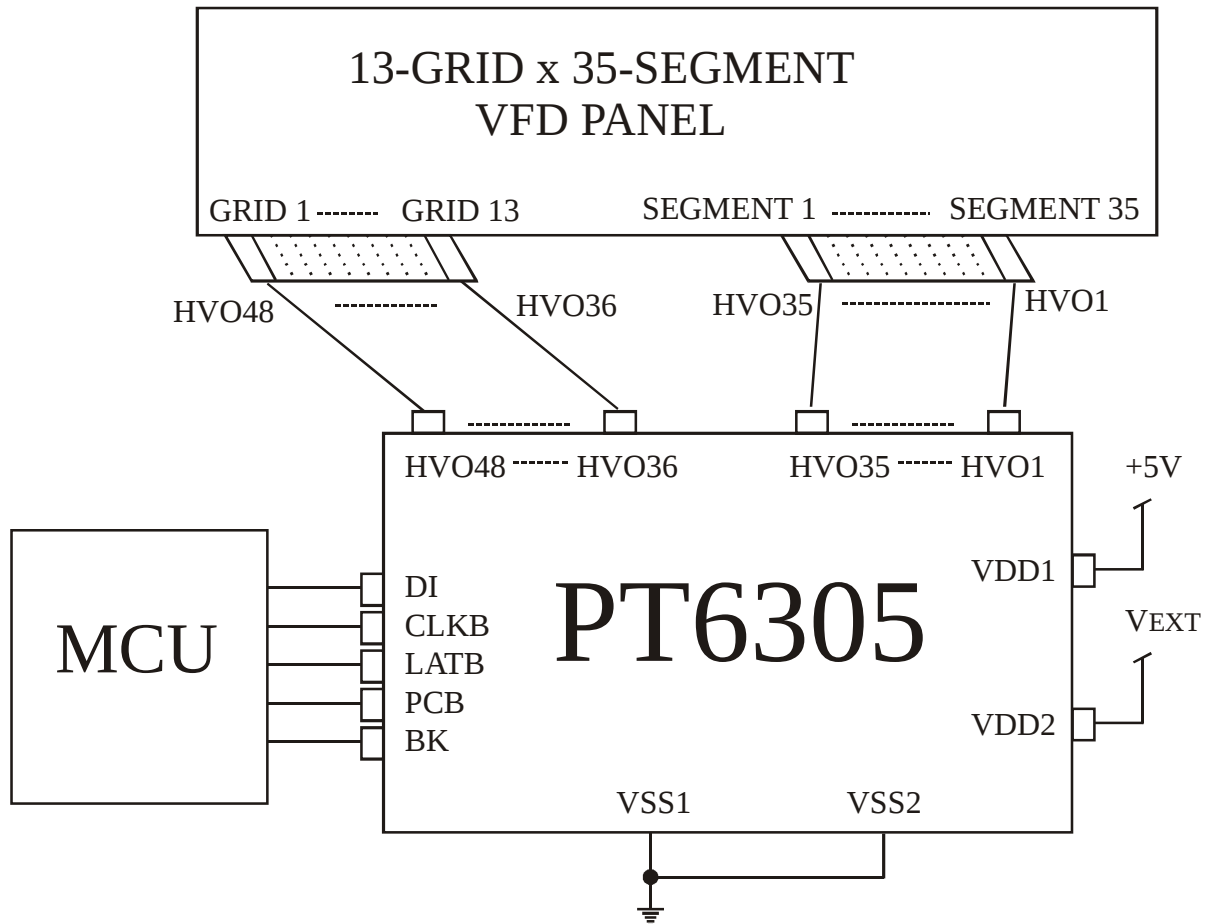
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PIN CONFIGURATION



APPLICATION CIRCUIT



Note: VEXT = External Supply Voltage (Maximum Value = 70V)