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Description

PT6885 is a 80-output OLED column driver utilizing low power CMOS technology that is specially designed for driving large capacity dot-matrix OLED panels. It includes four primary blocks, namely: 80-bit data register address selector, 80-bit data register, 80-bit data latch, 80-bit output current amplifier and a reference current generator. In order to obtain suitable brightness and minimize the drift between drivers, the constant current is adjustable via an external VR.

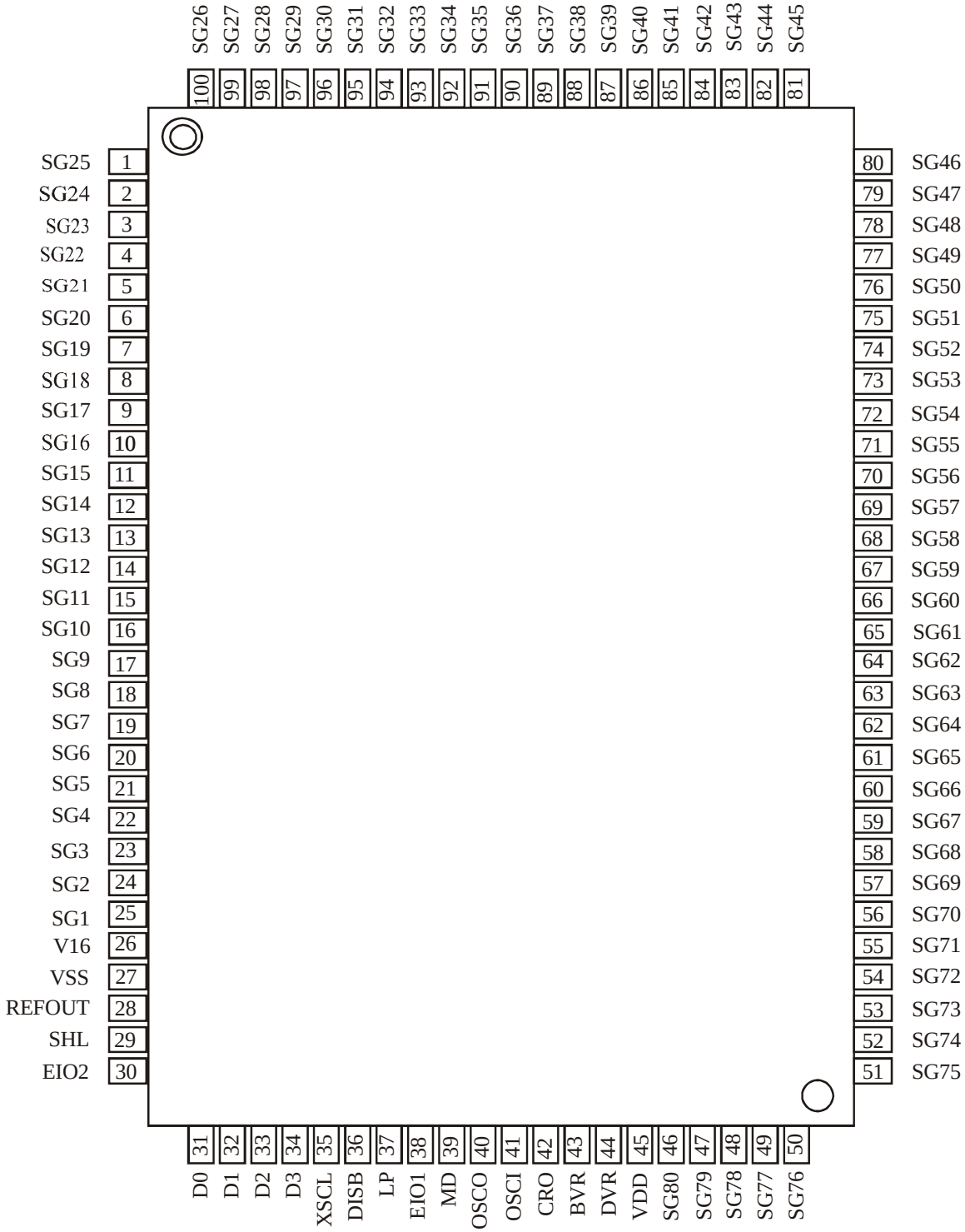
Features

- CMOS Technology
- Low Power Consumption
- 80 Output Channels
- Output Current：300uA (max.) adjusted by External Voltage
- Serial or 4-Bits Parallel Data Interface with Controller
- Shift Clock Frequency: 10MHz (max.)
- A function for cascading more than one device
- Operating Voltage: 16V (max.)
- Bi-directional Data Transfer through the device
- External Brightness Adjustment with VR
- Sleep Mode Control
- Available in C.O.B. or QFP Package

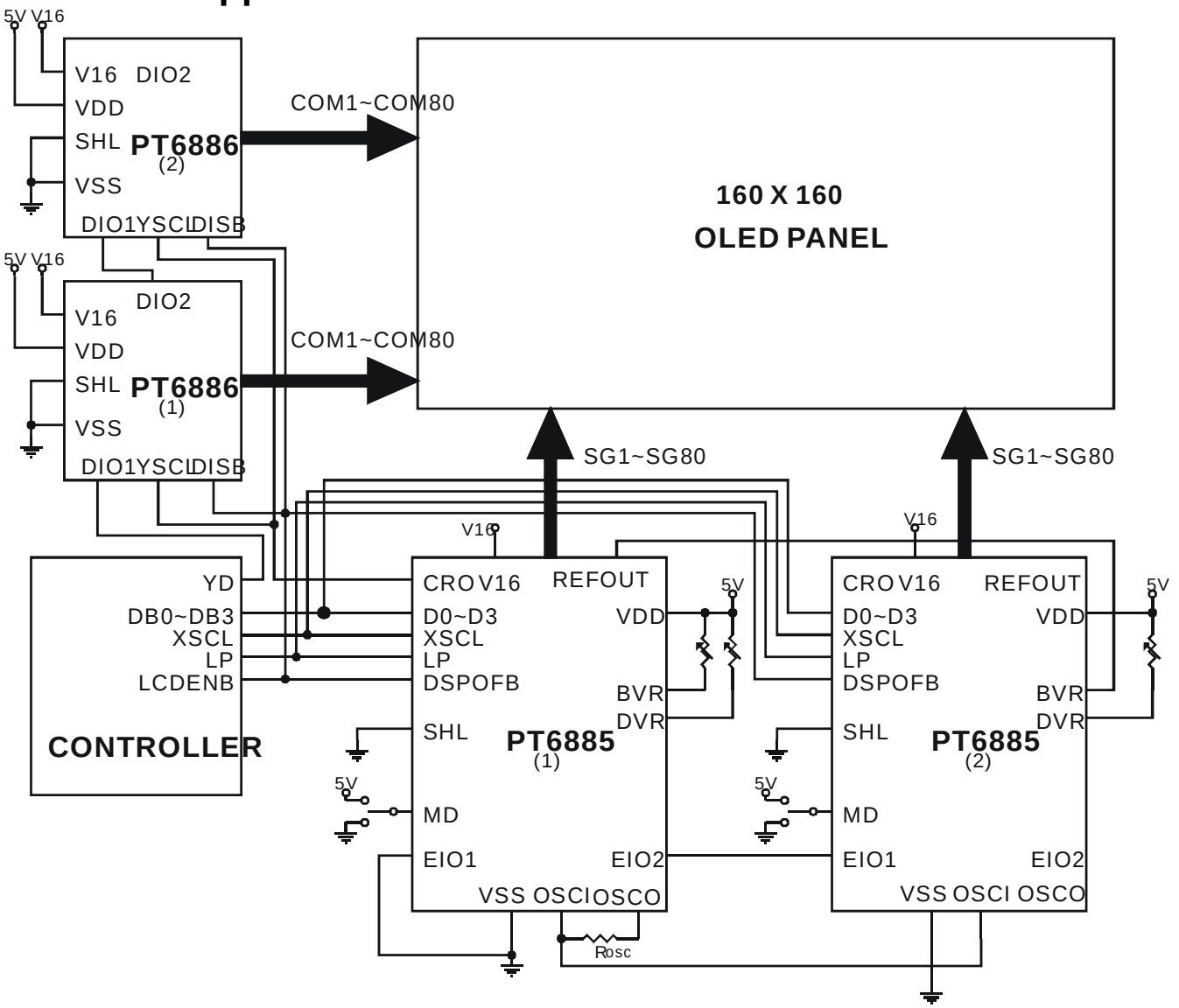
Applications

- OLED Display Panels

Pin Configuration



Reference Application Circuit



Notes:
1. If MD is connected to “HIGH” (5V), then the 4-Bit Parallel Mode is enabled. If MD is connected to “LOW” (GND), then the 1-Bit Serial Mode is enabled.
2. If PT6886’s SHL Pin is connected to GND, then PT6886’s DIO1 Pin is used as an input and DIO2 Pin is used as an output.
3. If PT6885’s SHL Pin is connected to GND, then PT6885’s EIO1 Pin is used as an input and EIO2 Pin is used as an output.
4. Using the above OLED Panel, PTC recommends that external oscillation resistor -- Rosc is given the value of 160 kΩ. However, if the OLED Panel is different from that specified in this data sheet, the high pulse width of PT6885's CRO Pin is dependent on the value of Rosc.