
LCD Driver IC

PT6608

DESCRIPTION

PT6608 is a 64 -channel segment driver for dot matrix liquid crystal graphic display system. It utilizes CMOS Technology and provides display RAM, 64-bit data latch, 64 bit drivers and decoder logics. It features the internal display RAM used as storage for the display data which are transferred from an 8-bit microcontroller. It generates the dot matrix liquid crystal driving signals corresponding to the given stored data. Paired with PT6607 (Common Driver IC), PT6608's pin assignments and application circuit are optimized for easier PCB Layout and cost saving advantages.

FEATURES

- CMOS Technology
- LCD Driving Voltage: 8 V to 17 V (VDD-VEE)
- Power Supply Voltage: +5V $\pm$ 10 %
- LCD Duty: 1/32 to 1/64
- Display Data is stored in Display Data RAM from MPU
- Provides 64-Channel Output
- Available in C.O.B. or 100-Pin, QFP Package
- Output: 64 Channel for LCD Driving
- Input: 8 Bits Parallel Display Data
 - Control Signal from MPU
 - Splitted Bias Voltage (V0R, V0L, V2R, V2L, V3R, V3L, V5R, V5L)
- Interface: Common Driver--PT6607
 - Segment Driver -- PT6608
 - Controller -- MPU

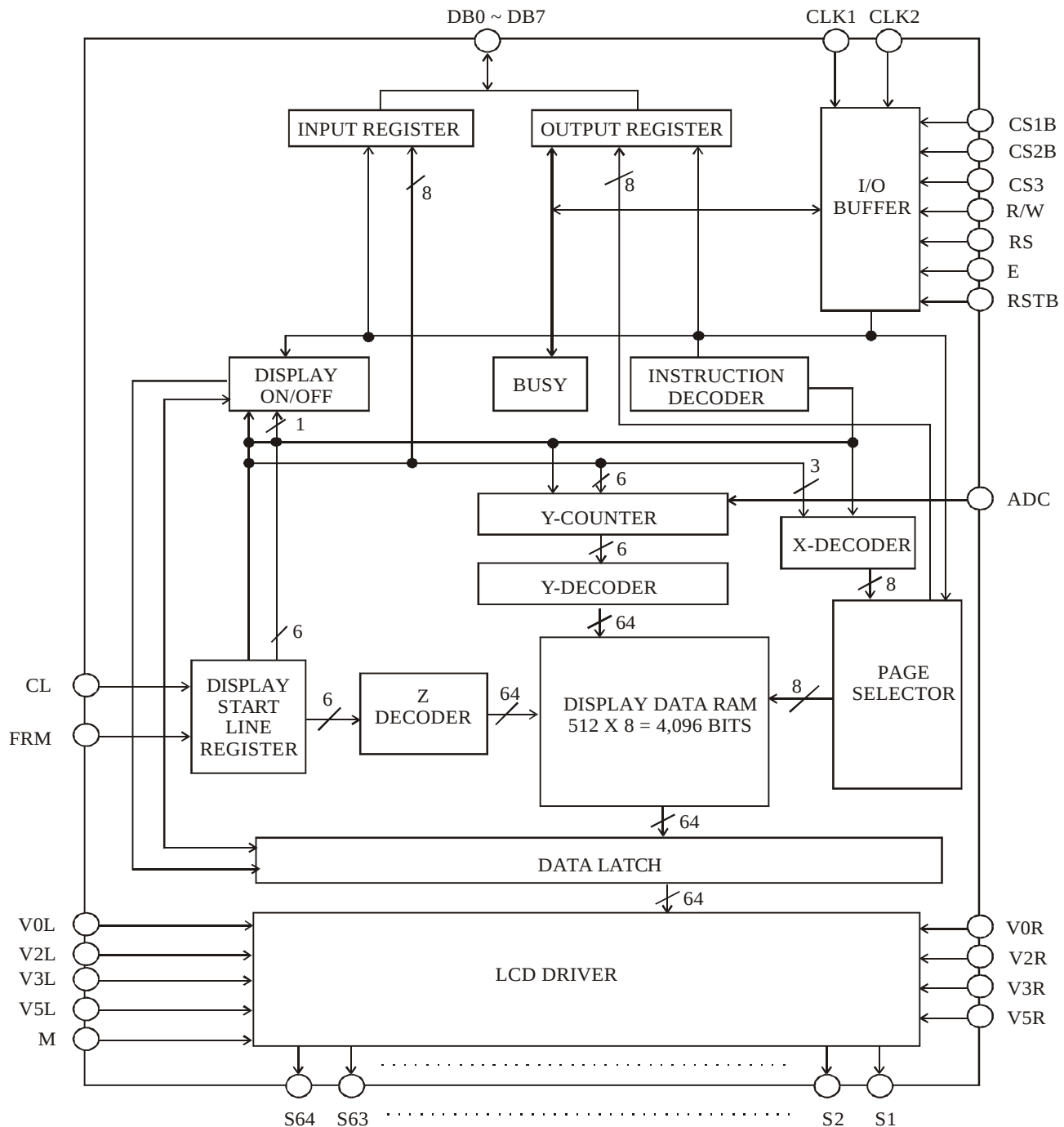
APPLICATION

- Peripheral Device

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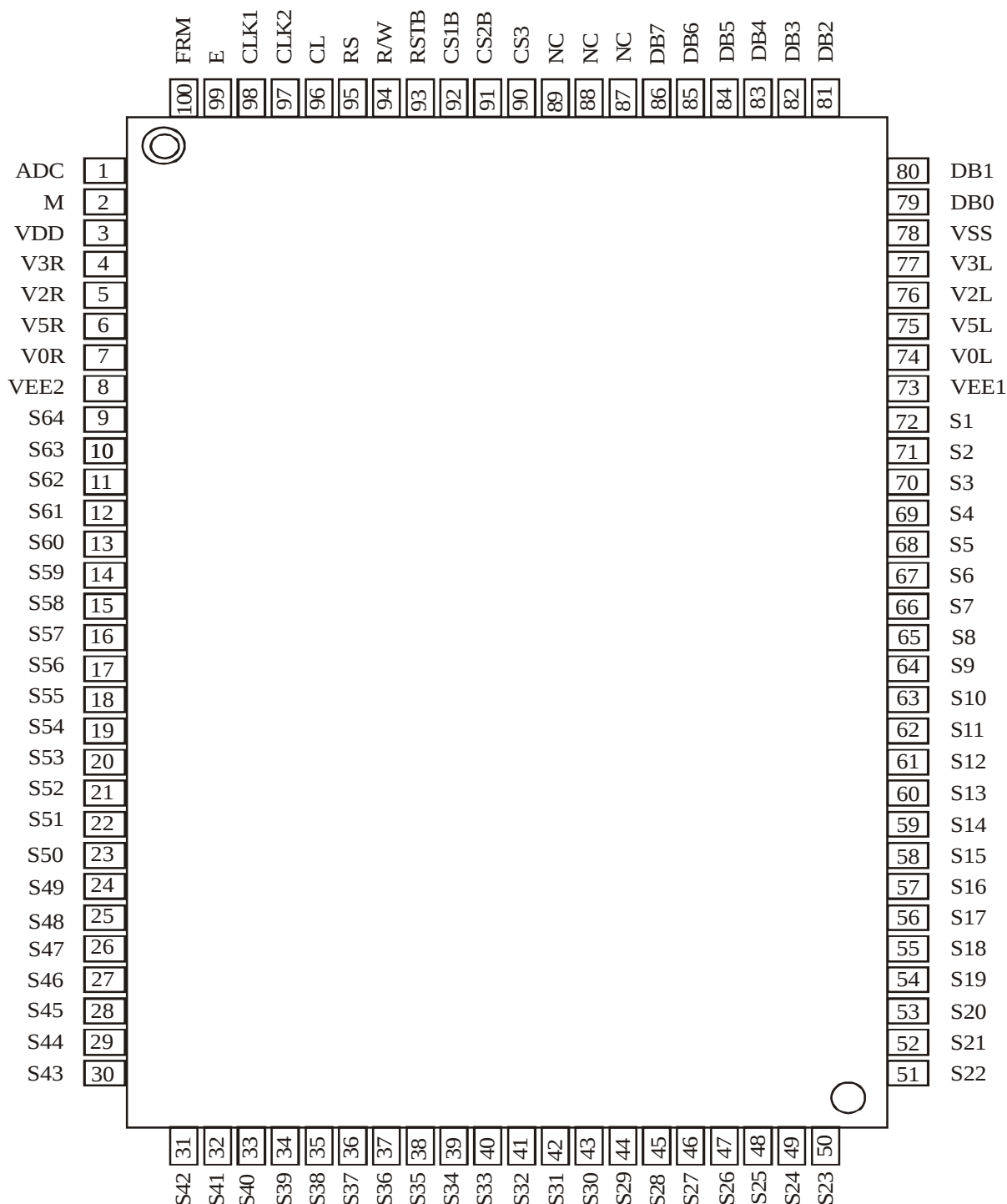
BLOCK DIAGRAM



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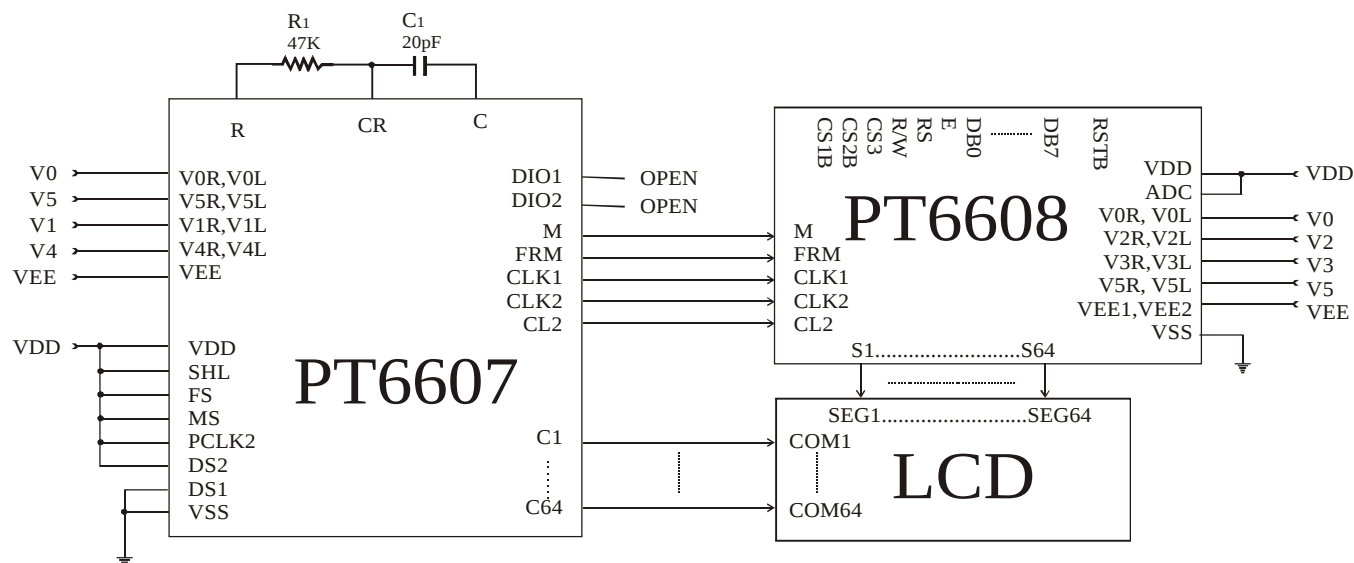
PIN CONFIGURATION



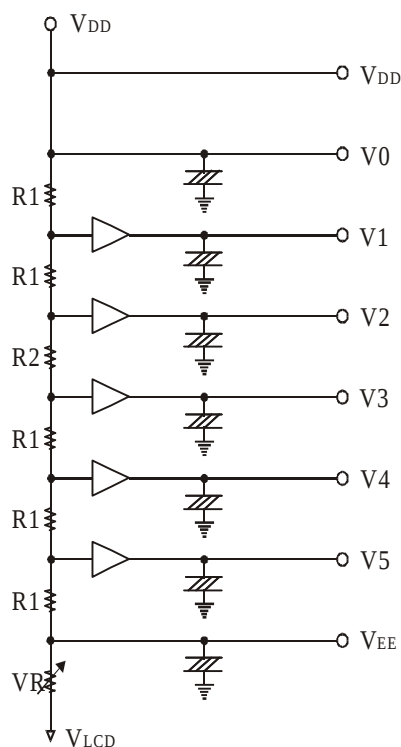
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APPLICATION CIRCUIT



Power Driver Circuit



Duty	Bias	Rdiv
1/48	1/8	R2=4R1
1/64	1/9	R2=5R1
1/96	1/11	R2=7R1
1/128	1/12	R2=8R1

Note: 1. When the duty factor is 1/48, the value of R1 and R2 must satisfy the following:

$$R1/(4R1+R2)=1/8$$

$$R1=3\text{ k}$$

$$R2=12\text{ k}$$

2. Bias means division of voltage between VDD and VEE.

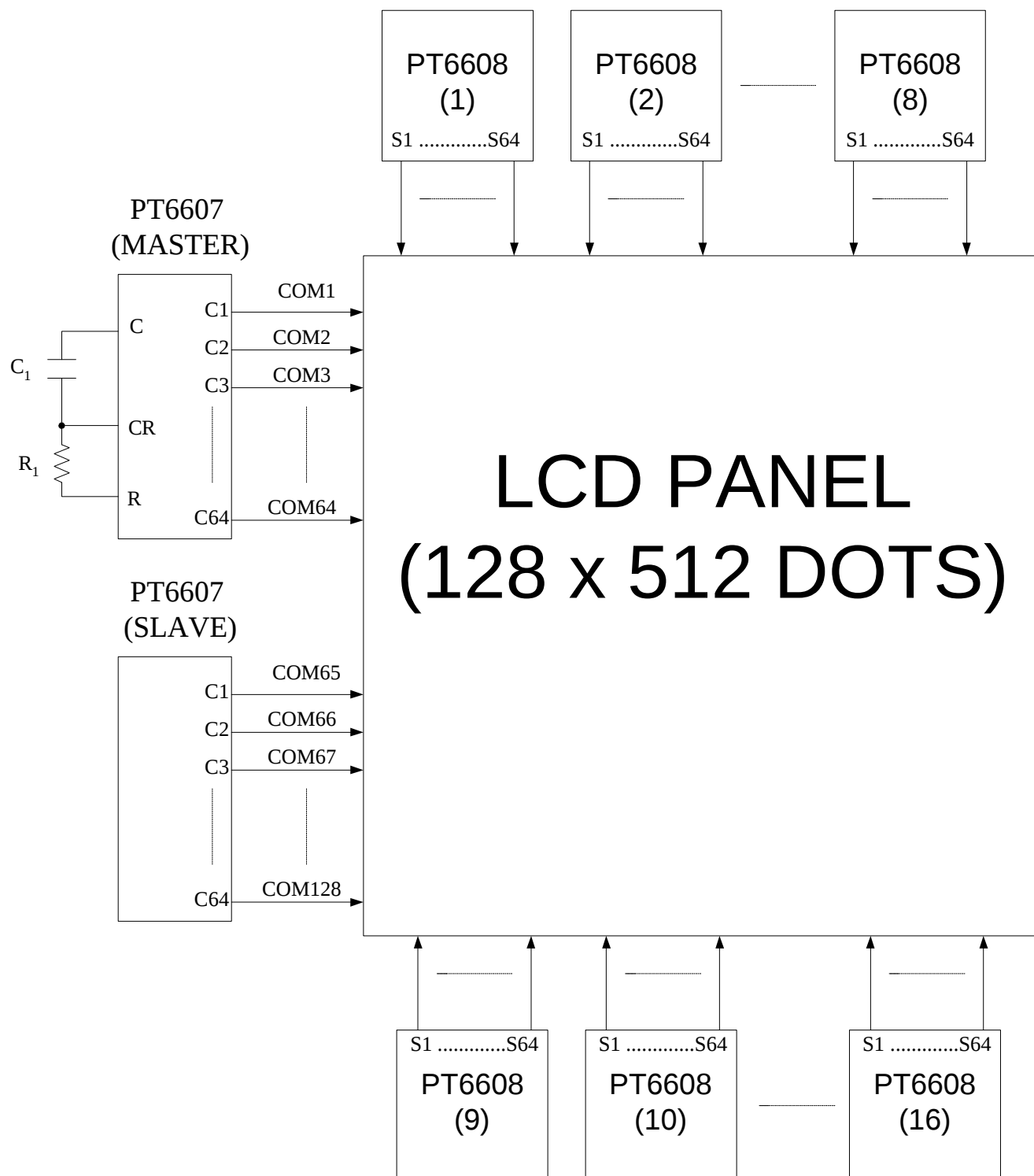
Ex. 1/8 Bias means divide the voltage VDD and VEE to be 8 equalizers.

3. V₁, V₂, V₃, V₄, V₅ and V_{EE} voltage levels can be adjusted by the VR.

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LCD Panel Interface Application Circuit



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ORDER INFORMATION

Valid Part Number	Package Type
PT6608	100 Pins, QFP Package
PT6608-H	C.O.B