

PC456L0NIP Series

High Speed 2Mb/s, High CMR Mini-flat Package *OPIC Photocoupler



■ Description

PC456L0NIP Series contains a LED optically coupled to an OPIC.

Input-output isolation voltage(rms) is 3.75 kV. High speed response (t_{PHL} : MAX. 400 ns, t_{PLH} : MAX. 550 ns) and CMR is MIN. 15 kV/ μ s.

■ Features

- 1. 5 pin Mini-flat package
- 2. Double transfer mold package (Ideal for Flow Soldering)

It is packaged in a 5 pin mini-flat.

- 3. High noise immunity due to high instantaneous common mode rejection voltage (CM_H : MIN. $15kV/\mu s$, CM_L : MIN. $-15kV/\mu s$)
- 4. High speed response (t_{PHL} : MAX. 400 ns, t_{PLH} : MAX. 550 ns)
- 5. High isolation voltage between input and output (V_{iso(rms)}: 3.75 kV)

■ Agency approvals/Compliance

- 1. Recognized by UL1577 (Double protection isolation), file No. E64380 (as model No. **PC456L**)
- Approved by VDE (VDE0884) (as an option) file No. 5911UG (as model No. PC456L)
- 3. Package resin: UL flammability grade (94V-0)

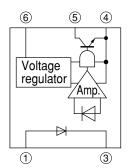
■ Applications

- 1. Programmable controller
- 2. Inverter

^{* &}quot;OPIC"(Optical IC) is a trademark of the SHARP Corporation. An OPIC consists of a light-detecting element and a signal-processing circuit integrated onto a single chip.



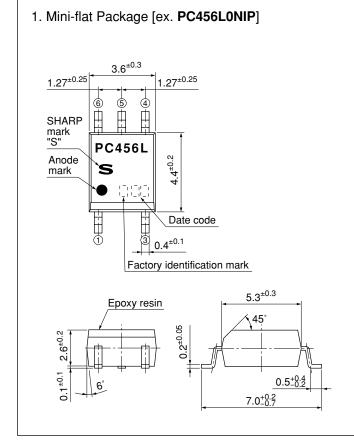
■ Internal Connection Diagram

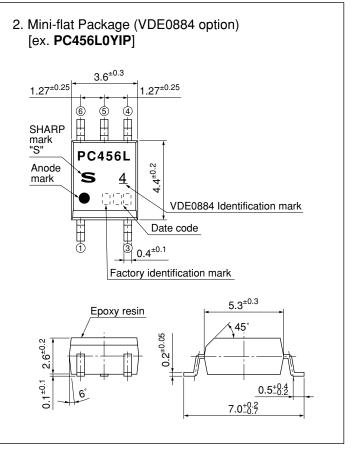


- 1 Anode
- ③ Cathode
- 4 GND
- ⑤ V_O
- ${\small \textcircled{6}}\ V_{CC}$

■ Outline Dimensions

(Unit : mm)





Product mass: approx. 0.1g



Date code (2 digit)

1st digit				2nd digit		
Year of production				Month of production		
A.D.	Mark	A.D	Mark	Month	Mark	
1990	A	2002	P	January	1	
1991	В	2003	R	February	2	
1992	С	2004	S	March	3	
1993	D	2005	T	April	4	
1994	Е	2006	U	May	5	
1995	F	2007	V	June	6	
1996	Н	2008	W	July	7	
1997	J	2009	X	August	8	
1998	K	2010	A	September	9	
1999	L	2011	В	October	0	
2000	M	2012	С	November	N	
2001	N	:	:	December	D	

repeats in a 20 year cycle

Factory identification mark

Factory identification Mark	Country of origin	
no mark	T. n. n.	
	Japan	
	Indonesia	
$\overline{\hspace{1cm}}$	Philippines	
_	China	

^{*} This factory marking is for identification purpose only.

Please contact the local SHARP sales representative to see the actual status of the production.



■ Absolute Maximum Ratings

 $(T_a=25^{\circ}C)$

(1 _a -2				
	Parameter	Symbol	Rating	Unit
	*1 Forward current	I_{F}	25	mA
Input	Reverse voltage	V_R	5	V
	*2 Power dissipation	P	45	mW
	Supply voltage	V _{CC}	-0.5 to +35	V
Output	Output voltage	Vo	-0.5 to +35	V
Output	Output current	Io	15	mA
	*3 Power dissipation	Po	100	mW
Operating temperature		Topr	-40 to +85	°C
Storage temperature		T _{stg}	-55 to +125	°C
*4 Isolation voltage		V _{iso (rms)}	3.75	kV
*5 Soldering temperature		T _{sol}	270	°C

^{*1} When ambient temperature goes above 70°C, the power dissipation goes down at 0.8mA/°C. (Fig.3)

■ Electro-optical Characteristics*6

(unless otherwise specified $T_a=-40$ to 85° C, $V_{CC}=4.5$ to 35V)

		(a					
Parameter		Symbol	Conditions	MIN.	TYP.	MAX.	Unit
	Forward voltage Reverse current		$T_a=25^{\circ}C$, $I_F=10mA$	_	1.6	1.95	V
ndu			$T_a=25^{\circ}C, V_R=5V$	_	-	10	μΑ
_	Terminal capacitance	C_t	$T_a=25$ °C, V=0, f=1MHz	_	60	150	pF
	Operating supply voltage	V_{CC}	-	4.5	_	35	V
Output	Low level output voltage	V _{OL}	$I_F=10mA, I_O=2.4mA$	_	0.3	0.6	V
Out	Low level output current	I_{OL}	$I_F=10mA, V_O=0.6V$	4.4	9	_	mA
	High level output current	I_{OH}	$I_F=0, V_{CC}=V_O$	_	5	50	μΑ
	High level supply current		$I_F=0, V_O=OPEN$	_	0.6	1.3	mA
	Low level supply current		$I_F=10mA$, $V_O=OPEN$	-	0.8	1.3	mA
	"High-Low" input threshold current		V_{O} =0.8V, R_{L} =20k Ω , V_{CC} =15V	_	1.5	5	mA
S	Isolation resistance		T _a =25°C, DC500V, 40 to 60%RH	5×10 ¹⁰	10^{11}	_	Ω
stic	Floating capacitance		T _a =25°C, V=0, f=1MHz	_	0.6	1	pF
characteristics	ല്ല "High→Low" propagation delay time	t_{PHL}	I 10m A (t) I 0 (t)	30	210	400	ns
arac	"High→Low" propagation delay time "Low→High" propagation delay time	t_{PLH}	$I_F=10\text{mA} (t_{PHL}), I_F=0 (t_{PLH}), V_{CC}=15V, R_L=20k\Omega, C_L=100pF$	270	400	550	
	*7 Distortion of pulse width Propagation delay skew	Δtw	$V_{THI,H}=2.0V, V_{THHI}=1.5V$	_	190	450	
sfeı	Propagation delay skew			_	200	450	
Transfer	Instantaneous common mode rejection voltage (High level output)		T_a =25°C, I_F =0, V_{CC} =15V, C_L =100pF, V_{CM} =1.5k $V_{(P-P)}$, R_L =20k Ω , V_O >3.0V	15	30	_	kV/μs
	Instantaneous common mode rejection voltage (Low level output)		$\begin{split} T_a &= 25^{\circ}\text{C}, \ I_F = 10\text{mA}, \ V_{CC} = 15\text{V}, \ C_L = 100\text{pF}, \\ V_{CM} &= 1.5\text{kV}_{(P - P)}, \ R_L = 20\text{k}\Omega, \ V_O < 1.0\text{V} \end{split}$	-15	-30	_	κν/μς

^{*6} It shall connect a by-pass capacitor of $0.01\mu F$ or more between V_{CC} (pin 6) and GND (pin 4) near the device, when it measures the transfer characteristics and the output side characteristics

^{*2} When ambient temperature goes above 70°C, the power dissipation goes down at 1.5mW/°C. (Fig.4)

^{*3} When ambient temperature goes above 70°C, the power dissipation goes down at 1.8mW/°C. (Fig.4)

^{*4 40} to 60%RH, AC for 1minute, f=60Hz

^{*5} For 10s

^{*7} Distortion of pulse width $\Delta tw = |t_{PHL} - t_{PLH}|$



■ Model Line-up

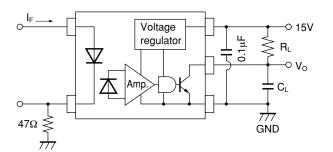
Doolsogo	Taping			
rackage	Package 3 000 pc			
VDE0884		Approved		
Model No.	PC456L0NIP	PC456L0YIP		

Please contact a local SHARP sales representative to inquire about production status and Lead-Free options.

Sheet No.: D2-A04701EN



Fig.1 Test Circuit for Propagation Delay Time



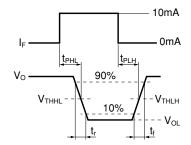
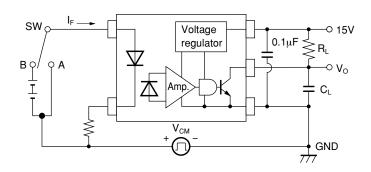


Fig.2 Test Circuit for Instantaneous Common Mode Rejection Voltage



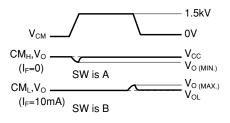


Fig.3 Forward Current vs. Ambient Temperature

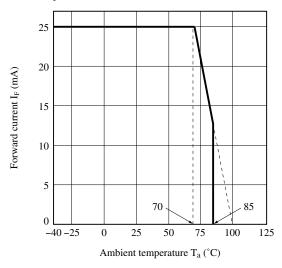


Fig.4 Power Dissipation vs. Ambient Temperature

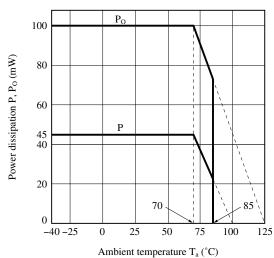




Fig.5 Output Current vs. Forward Current

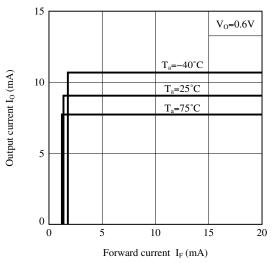


Fig.7 Relative Output Current vs. Ambient Temperature

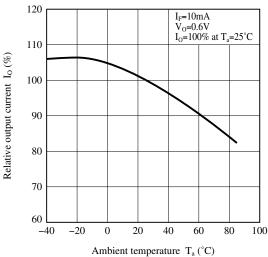


Fig.9 Low Level Output Voltage vs. Ambient Temperature

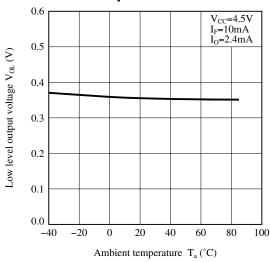


Fig.6 Forward Current vs. Forward Voltage

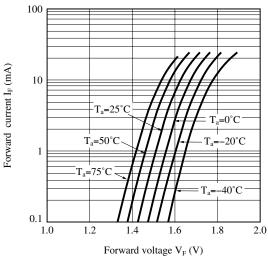


Fig.8 Input Threshold Current vs.
Ambient Temperature

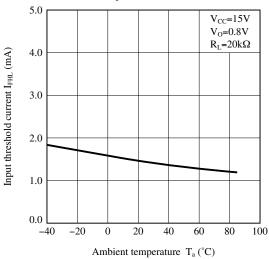


Fig.10 Supply Current vs. Ambient Temperature

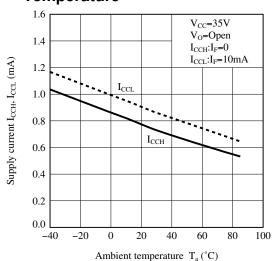




Fig.11 Propagation Delay Time vs.
Ambient Temperature

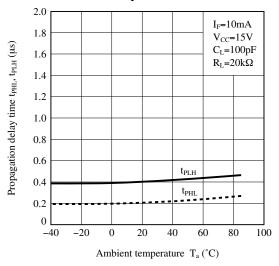


Fig.13 Propagation Delay Time vs. Load Capacitance

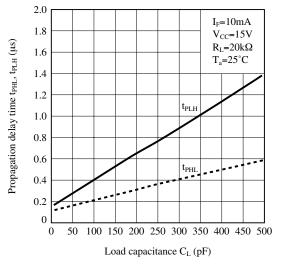


Fig.15 Propagation Delay Time vs. Forward Current

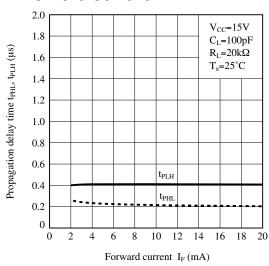


Fig.12 Propagation Delay Time vs. Load Resistance

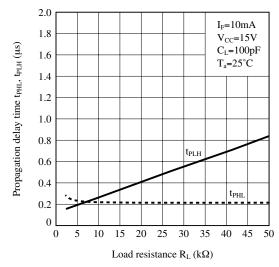
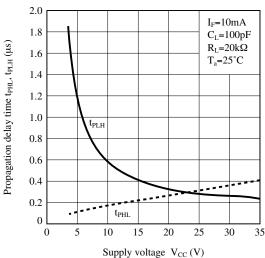


Fig.14 Propagation Delay Time vs. Supply Voltage



Remarks : Please be aware that all data in the graph are just for reference and not for guarantee.



■ Design Considerations

Recommended operating conditions

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Forward current	I_{F}	10	-	20	mA
Supply voltage	V _{CC}	4.5	_	35	V
Output voltage	Vo	0	_	35	V
Operating temperature	Topr	-40	_	70	°C

Notes about static electricity

Transistor of detector side in bipolar configuration may be damaged by static electricity due to its minute design.

When handling these devices, general countermeasure against static electricity should be taken to avoid breakdown of devices or degradation of characteristics.

Design guide

In order to stabilize power supply line, we should certainly recommend to connect a by-pass capacitor of $0.01\mu F$ or more between V_{CC} and GND near the device.

In case that some sudden big noise caused by voltage variation is provided between primary and secondary terminals of photocoupler some current caused by it is floating capacitance may be generated and result in false operation since current may go through LED or current may change.

If the photocoupler may be used under the circumstances where noise will be generated we recommend to use the bypass capacitors at the both ends of LED.

The detector which is used in this device, has parasitic diode between each pins and GND.

There are cases that miss operation or destruction possibly may be occurred if electric potential of any pin becomes below GND level even for instant.

Therefore it shall be recommended to design the circuit that electric potential of any pin does not become below GND level.

This product is not designed against irradiation and incorporates non-coherent LED.

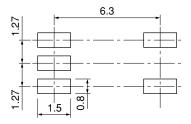
Degradation

In general, the emission of the LED used in photocouplers will degrade over time.

In the case of long term operation, please take the general LED degradation (50% degradation over 5years) into the design consideration.

Please decide the input current which become 2times of MAX. I_{FHL}.

Recommended Foot Print (reference)



(Unit: mm)

[☆] For additional design assistance, please review our corresponding Optoelectronic Application Notes.



■ Manufacturing Guidelines

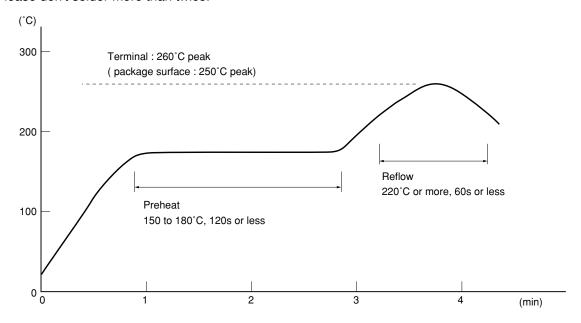
Soldering Method

Reflow Soldering:

Reflow soldering should follow the temperature profile shown below.

Soldering should not exceed the curve of temperature profile and time.

Please don't solder more than twice.



Flow Soldering:

Due to SHARP's double transfer mold construction submersion in flow solder bath is allowed under the below listed guidelines.

Flow soldering should be completed below 260°C and within 10s.

Preheating is within the bounds of 100 to 150°C and 30 to 80s.

Please don't solder more than twice.

Hand soldering

Hand soldering should be completed within 3s when the point of solder iron is below 400°C.

Please don't solder more than twice.

Other notices

Please test the soldering method in actual condition and make sure the soldering works fine, since the impact on the junction between the device and PCB varies depending on the tooling and soldering conditions.



Cleaning instructions

Solvent cleaning:

Solvent temperature should be 45°C or below Immersion time should be 3minutes or less

Ultrasonic cleaning:

The impact on the device varies depending on the size of the cleaning bath, ultrasonic output, cleaning time, size of PCB and mounting method of the device.

Therefore, please make sure the device withstands the ultrasonic cleaning in actual conditions in advance of mass production.

Recommended solvent materials:

Ethyl alcohol, Methyl alcohol and Isopropyl alcohol

In case the other type of solvent materials are intended to be used, please make sure they work fine in actual using conditions since some materials may erode the packaging resin.

Presence of ODC

This product shall not contain the following materials.

And they are not used in the production process for this device.

Regulation substances: CFCs, Halon, Carbon tetrachloride, 1.1.1-Trichloroethane (Methylchloroform)

Specific brominated flame retardants such as the PBBOs and PBBs are not used in this product at all.

Sheet No.: D2-A04701EN



■ Package specification

● Tape and Reel package

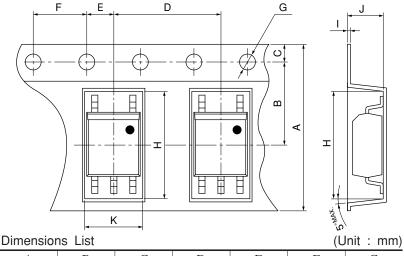
Package materials

Carrier tape: A-PET (with anti-static material)

Cover tape: PET (three layer system)

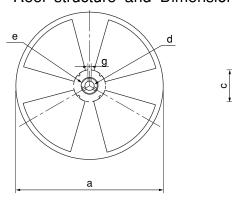
Reel: PS

Carrier tape structure and Dimensions



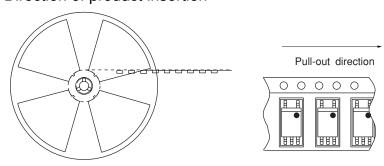
Difficition	io List				()	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,
A	В	С	D	Е	F	G
12.0 ^{±0.3}	5.5 ^{±0.1}	1.75 ^{±0.1}	8.0 ^{±0.1}	2.0 ^{±0.1}	4.0 ^{±0.1}	φ1.5 + 8.1
Н	I	J	K			
$7.4^{\pm0.1}$	0.3 ^{±0.05}	3.1 ^{±0.1}	4.0 ^{±0.1}			

Reel structure and Dimensions



Dimensio	ns List	(Unit: mm)		
a	b	c d		
370	13.5 ^{±1.5}	80±1.0	13 ^{±0.5}	
e	f	g		
21±1.0	2.0±0.5	2.0±0.5		

Direction of product insertion



[Packing: 3 000pcs/reel]



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 - --- Office automation equipment
 - --- Telecommunication equipment [terminal]
 - --- Test and measurement equipment
 - --- Industrial control
 - --- Audio visual equipment
 - --- Consumer electronics
- (ii) Measures such as fail-safe function and redundant design should be taken to ensure reliability and safety when SHARP devices are used for or in connection

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- --- Transportation control and safety equipment (i.e., aircraft, trains, automobiles, etc.)
- --- Traffic signals
- --- Gas leakage sensor breakers
- --- Alarm equipment
- --- Various safety devices, etc.
- (iii) SHARP devices shall not be used for or in connection with equipment that requires an extremely high level of reliability and safety such as:
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