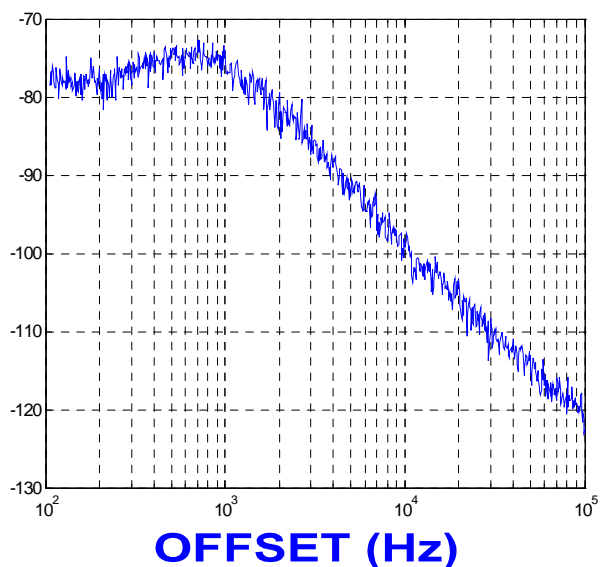




### PHASE NOISE (1 Hz BW, typical)

$L(f)$  (dBc/Hz)



#### FEATURES

- Frequency Range: 420 - 470 MHz
- Step Size: 100 KHz
- cPLL - Style Package

#### APPLICATIONS

- Satellite Modems
- Mobile Radios
- CATV

#### PERFORMANCE SPECIFICATIONS

	VALUE	UNITS
Frequency Range	420 - 470	MHz
RMS Phase Error (100 Hz - 100 KHz)	1.0	°
Harmonic Suppression (2nd, typ.)	-10	dBc
Sideband Spurs (typ.)	-65	dBc
Power Output	4±3	dBm
Load Impedance	50	$\Omega$
Step Size	100	KHz
Charge Pump Output Current	1250	$\mu$ A
Switching Speed (typ., adjacent channel)	3	mSec
Startup Lock Time (typ.)	5	mSec
Operating Temperature Range	-40 to 85	°C
Package Style	cPLL	

#### POWER SUPPLY REQUIREMENTS

Supply Voltage (Vcc, nom.)	5	Vdc
Supply Current (Icc, typ.)	24	mA

All specifications are typical unless otherwise noted and subject to change without notice.

#### APPLICATION NOTES

- AN-107 : How to Solder Z-COMM VCOs / PLLs
- AN-200 : Mounting and Grounding of Z-COMM PLLs
- AN-201 : PLL Fundamentals      AN-202 : PLL Functional Description

#### NOTES:

Reference Oscillator Signal:  $5 \text{ MHz} < f_{\text{osc}} < 100 \text{ MHz}$   
 Frequency Synthesizer IC: Analog Devices - ADF4113

# PLL OUTPUT SPECTRUM

POWER

FREQUENCY OFFSET (KHz)

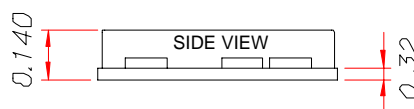
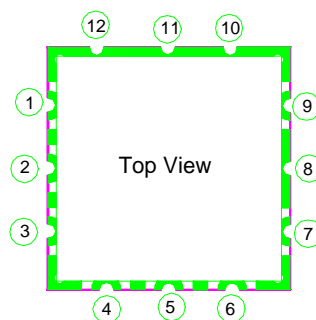
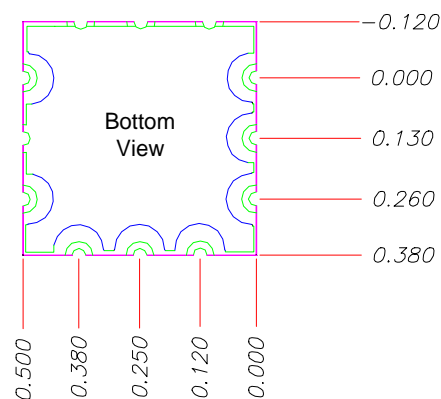
POWER CURVE, typ.

POWER OUTPUT (dBm)

■ 25 °C

FREQUENCY (MHz)

## PHYSICAL DIMENSIONS



1. The inside radius of all 14 half holes at the perimeter of the board are plated to provide a surface for the attachment of the PLL Module to the motherboard. 5 pads are for grounding, 8 pads are for signal interface.
2. The surface of the shield is tin-plated and may be soldered to. The shield's base metal is brass.
3. The ground plane on the bottom side is ground and attaches to a ground track on the top side of the board as well as to the shield.
4. Unless otherwise noted all dimensions are in inches.
5. Unless otherwise noted all tolerances are as follows:  
xxx = ± .010

P1 RF OUTPUT  
P2 REFERENCE OSCILLATOR INPUT  
P3 CLOCK  
P4 DATA  
P5 LOAD ENABLE  
P6 LOCK DETECT  
P7 VCC  
P8 GROUND  
P9 NO CONNECTION  
P10-12 GROUND