

EMI Reduction IC for Digital Video & Imaging

FEATURES

- Provides up to 20 dB of EMI suppression
- FCC approved method of EMI attenuation
- Generates a low EMI spread spectrum clock optimized for 27 MHz input signal.
- Internal loop filter minimizes external components and board space
- 2 selectable spread ranges, 2 selectable modulation rates and Spread spectrum ON/OFF control pin.
- Low cycle-to-cycle jitter
- 3.3 V Operating voltage
- 16 mA output drives
- TTL or CMOS compatible output
- Available in 8 pin SOIC, TSSOP packages.

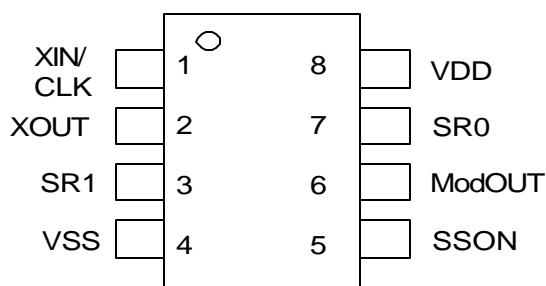
PRODUCT DESCRIPTION

The P2027 is a versatile spread spectrum frequency modulator. P2027 reduces electromagnetic interference (EMI) at the clock source by spreading the energy spectrum of a synthesized clock over a wider band thereby reducing the peak measured emissions of the source. This provides system wide reduction of EMI of all clock dependent signals.

APPLICATIONS

The P2027 is targeted towards the DSC markets as well as other digital imaging & video applications.

Figure 1 – P2027 Pin Diagram



The P2027 allows significant system cost savings by reducing the number of circuit board layers and shielding that are traditionally required to pass EMI regulations.

The P2027 uses the most efficient and optimized modulation profile approved by the FCC and is implemented in a proprietary all-digital method.

Figure 2 – P2027 Block Diagram

