

Low Frequency EMI Reduction IC

FEATURES

- Provides up to 20 dB of EMI suppression
- FCC approved method of EMI attenuation
- Generates a low EMI spread spectrum clock of the input frequency from 10 MHz to 35 MHz
- Internal loop filter minimizes external components and board space
- 4 selectable spread ranges and spread spectrum ON/OFF control pin.
- Low cycle-to-cycle jitter
- Wide operating range (3V to 5V)
- 16 mA output drives
- TTL or CMOS compatible output
- Available in 8 pin SOIC, TSSOP packages.

PRODUCT DESCRIPTION

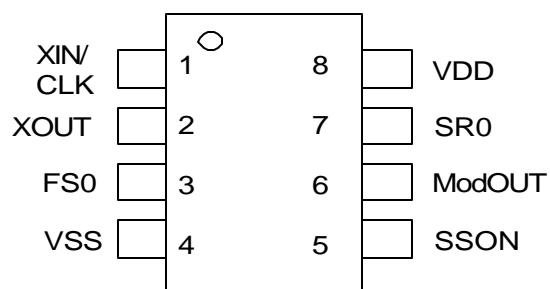
The P2010 is a versatile spread spectrum frequency modulator.

The P2010 reduces electromagnetic interference (EMI) at the clock source by spreading the energy spectrum of a synthesized clock over a wider band thereby reducing the peak measured emissions of the source. This provides system wide reduction of EMI of all clock dependent signals.

APPLICATIONS

The P2010 is targeted towards the embedded control market as well as all scanning devices including Digital Still Cameras.

Figure 1 – P2041 Pin Diagram



The P2010 allows significant system cost savings by reducing the number of circuit board layers and shielding that are traditionally required to pass EMI regulations.

The P2010 uses the most efficient and optimized modulation profile approved by the FCC and is implemented in a proprietary all-digital method.

Figure 2 – P2010 Block Diagram

