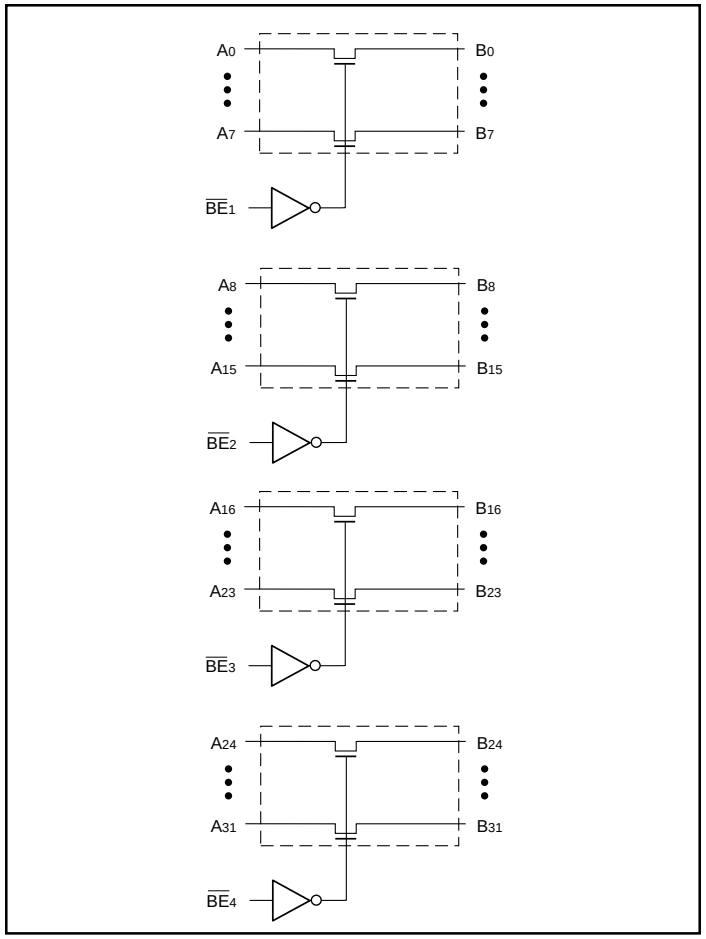
32-Bit, 2-Port Bus Switch

Product Features

- Near-zero propagation delay
- 5Ω switches connect inputs to outputs
- Direct bus connection when switches are ON
- Ultra-low quiescent power (0.2µA typical)
  – Ideally suited for notebook applications
- Pin compatible with QS34X245
- Industrial operating temperature: –40°C to +85°C
- Packages available:
  – 80-pin 150 mil wide plastic BQSOP (B)

Logic Block Diagram



Truth Table[1]

Function	$\overline{BE}$n	A0–31
Disconnect	H	Hi-Z
Connect	L	B0–31

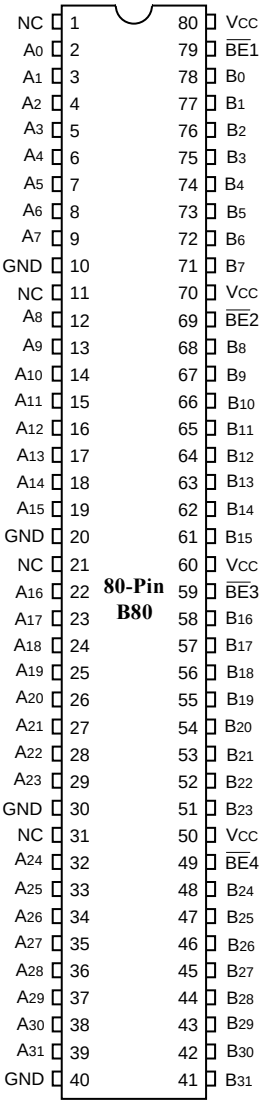
Note:
1. H = High Voltage Level
   L = Low Voltage Level
   Hi-Z = High Impedance

Product Description

Pericom Semiconductor's PI5C series of logic circuits are produced in the Company's advanced 0.6 micron CMOS technology.

The PI5C34X245 is a 32-bit, 2-port bus switch. Four enable signals ($\overline{BE}$n) turn the switches on. The bus switch creates no additional propagational delay or additional ground bounce noise.

Product Pin Configuration



Product Pin Description

Pin Name	I/O	Description
$\overline{BE}$n	I	Bus Enable Input (Active LOW)
A0-A31	I/O	Bus A
B0-B31	I/O	Bus B

Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	–65°C to +150°C
Ambient Temperature with Power Applied	–40°C to +85°C
Supply Voltage to Ground Potential (Inputs & Vcc Only)	–0.5V to +7.0V
Supply Voltage to Ground Potential (Outputs & D/O Only)	–0.5V to +7.0V
DC Input Voltage	–0.5V to +7.0V
DC Output Current	120 mA
Power Dissipation	0.5W

Note:
Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, T_A = –40°C to +85°C, V_{CC} = 5V ±10%)

Parameters	Description	Test Conditions[1]		Min.	Typ[2]	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed Logic HIGH Level		2.0	—	—	V
V_{IL}	Input LOW Voltage	Guaranteed Logic LOW Level		–0.5	—	0.8	V
I_{IH}	Input HIGH Current	V_{CC} = Max., V_{IN} = V_{CC}		—	—	±1	µA
I_{IL}	Input LOW Current	V_{CC} = Max., V_{IN} = GND		—	—	±1	µA
I_{OZH}	High Impedance Output Current	0 - A, B - V_{CC}		—	—	±1	µA
V_{IK}	Clamp Diode Voltage	V_{CC} = Min., I_{IN} = –18 mA		—	–0.7	–1.2	V
I_{OS}	Short Circuit Current[3]	A (B) = 0V, B (A) = V_{CC}		100	—	—	mA
V_H	Input Hysteresis at Control Pins			—	150	—	mV
R_{ON}	Switch On Resistance[4]	V_{CC} = Min., V_{IN} = 0.0V, I_{ON} = 48 mA	34X245	—	5	7	Ω
		V_{CC} = Min., V_{IN} = 2.4V, I_{ON} = 15 mA	34X245	—	10	15	Ω

Capacitance (T_A = 25°C, f = 1 MHz)

Parameters[5]	Description	Test Conditions	Typ	Units
C_{IN}	Input Capacitance	V_{IN} = 0V	6	pF
C_{OFF}	A/B Capacitance, Switch Off	V_{IN} = 0V	6	pF
C_{ON}	A/B Capacitance, Switch On	V_{IN} = 0V	12	pF

Notes:
1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at Vcc = 5.0V, T_A = 25°C ambient and maximum loading.
3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
4. Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A,B) pins.
5. This parameter is determined by device characterization but is not production tested.

Power Supply Characteristics

Parameters	Description	Test Conditions[1]		Min.	Typ[2]	Max.	Units
I_{CC}	Quiescent Power Supply Current	V_{CC} = Max.	V_{IN} = GND or V_{CC}	—	0.1	3.0	µA
ΔI_{CC}	Supply Current per Input @ TTL HIGH	V_{CC} = Max.	V_{IN} = 3.4V[3]	—	—	1.5	mA
I_{CCD}	Supply Current per Input per MHz[4]	V_{CC} = Max., A and B Pins Open $\overline{BE}n$ = GND Control Input Toggling 50% Duty Cycle		—	—	0.25	mA/ MHz

Notes:
1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
2. Typical values are at Vcc = 5.0V, +25°C ambient.
3. Per TTL driven input (V_{IN} = 3.4V, control inputs only); A and B pins do not contribute to Icc.
4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

PI5C34X245 Switching Characteristics over Operating Range

Parameters	Description	Conditions[1]	PI5C34X245 Com. Min.	PI5C34X245 Com. Max.	Units
t_{PLH} t_{PHL}	Propagation Delay[2,3] Ax to Bx, Bx to Ax	C_L = 50 pF R_L = 500Ω	—	0.25	ns
t_{PZH} t_{PZL}	Bus Enable Time $\overline{BE}$x to Ax or Bx		1.5	5.6	
t_{PHZ} t_{PLZ}	Bus Disable Time BEx to Ax or Bx		1.5	5.2	

Notes:
1. See test circuit and waveforms.
2. This parameter is guaranteed but not tested on Propagation Delays.
3. The bus switch contributes no propagational delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25 ns for 50 pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.