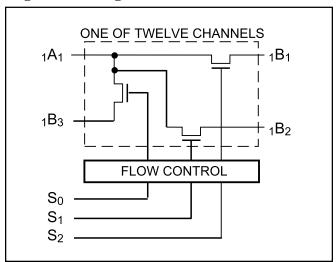


# 12-Bit, 3-To-1 Bus-Select Switch

#### **Features**

- Near-zero propagation delay
- $5\Omega$  switches connect inputs to outputs
- Direct bus connection when switches are ON
- Ultra-low quiescent power (0.2µA typical)
  - ideally suited for notebook applications
- Industrial operating temperature: -40°C to +85°C
- Packages available:
  - 56-pin 240-mil wide plastic TSSOP (A)
  - 56-pin 300-mil wide plastic SSOP (V)

# **Logic Block Diagram**



#### Truth Table(1)

Function	S2	S1	S0	A1
Disconnect	L	L	L	Z
A1 to B1	L	L	Н	B1
A1 to B2	L	Н	L	B2
Disconnect	L	Н	Н	Z
Disconnect	Н	L	L	Z
A1 to B3	Н	L	Н	В3
A1 to B1	Н	Н	L	B1
A1 to B2	Н	Н	Н	B2

#### Note:

1. H = High Voltage Level

L = Low Voltage Level

Z = High Impedance

# **Product Description**

Pericom Semiconductor's PI5C series of BusSwitch circuits are produced in the Company's advanced 0.8 micron CMOS technology, achieving industry leading speed.

The PI5C16214 and PI5C162214 are 3-to-1 bus-select switches designed with a low ON resistance (5 $\Omega$ ) allowing inputs to be connected directly to outputs. These devices operate as 12-bit bus switches via the data select pins (S0-S2).

The PI5C162214 device has a built-in 25-ohm series resistor to reduce noise resulting from reflections, thus eliminating the need for an external terminating resistor.

# **Product Pin Configuration**

Toduct I in Comigura	шип	
	( )	
S <sub>0</sub> ☐ 1	$\bigcirc$	56 🛚 S <sub>1</sub>
1A1 🛭 2		55 🗎 S <sub>2</sub>
1B3 ☐ 3		54 🗎 1B1
2 <b>A</b> 1 ☐ 4		53 🛘 1B2
2B3 ☐ 5		52 🗎 2B1
3 <b>A</b> 1 ☐ 6		51 2B <sub>2</sub>
3B3 ☐ 7		50 🗎 3B1
GND ☐ 8		49 GND
4A1 📮 9		48 🗎 3B2
4B3 ☐ 10	- o - D-	47 🗎 4B1
5A₁ ☐ 11	56-Pin A, V	46 🛘 4B2
5B3 ☐ 12	11, 1	45 🛘 5B1
6A1 🛘 13		44 🗎 5B2
6B3 ☐ 14		43 🗎 6B1
7A1 ☐ 15		42 🗎 6B2
7B3 ☐ 16		41 🗎 7B1
Vcc ☐ 17		40 🛘 7B2
8A1 🛘 18		39 🗎 8B1
GND ☐ 19		38 GND
8B3 ☐ 20		37 🛘 8B2
9A1 🛘 21		36 🗎 9B1
9B3 ☐ 22		35 🗎 9B2
10 <b>A</b> 1 ☐ 23		34 🗎 10B1
10B3 ☐ 24		33 🛘 10B2
11A1 ☐ 25		32 🗎 11B1
11B3 ☐ 26		31 🗎 <sub>11</sub> B <sub>2</sub>
12 <b>A</b> 1 ☐ 27		30 🛘 12B1
12B3 ☐ 28		29 🛘 12B2

# **Product Pin Description**

1

Pin Name	I/O	Description	
SO-S2	I	Select Inputs	
xAx	I/O	Bus A	
xBx	I/O	Bus B	

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### **Maximum Ratings**

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature
Ambient Temperature with Power Applied $-40^{\circ}C$ to $+85^{\circ}C$
Supply Voltage to Ground Potential (Inputs & Vcc Only) $-0.5V$ to $+7.0V$
Supply Voltage to Ground Potential (Outputs & D/O Only) ${-}0.5V$ to ${+}7.0V$
DC Input Voltage0.5V to +7.0V
DC Output Current
Power Dissipation

#### Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

### **DC Electrical Characteristics** (Over the Operating Range, $TA = -40^{\circ}C$ to $+85^{\circ}C$ , VCC = 4 to 5V)

Parameters	Description	Test Conditions <sup>(1)</sup>		Min.	<b>Typ</b> <sup>(2)</sup>	Max.	Units
VIH	Input HIGH Voltage	Guaranteed Logic HIGH Level		2.0	_	_	V
VIL	Input LOW Voltage	Guaranteed Logic LOW Level		-0.5	_	0.8	V
Iı	Input Current	$V_{CC} = 5.5V., V_{IN} = 5.5V \text{ or } GI$	ND	_	_	±1	μΑ
		$V_{CC} = 0V$ , $V_{IN} = V_{CC}$		_	_	±1	μA
Іохн	High Impedance Output Current	0 ≤A, B ≤Vcc		_	_	10	μA
Vik	Clamp Diode Voltage	$V_{CC} = 4.5V$ , $I_{IN} = -18mA$		_	-0.7	-1.2	V
Ios	Short Circuit Current <sup>(3)</sup>	A(B) = 0V, B(A) = VCC		100	_	_	mA
VH	Input Hysteresis at Control Pins			_	150	_	mV
Ron	Switch On Resistance <sup>(4)</sup>	$V_{CC} = 4.5V, V_{IN} = 0.0V,$	16214	_	5	7	Ω
		Ion = 30mA, 64mA	162214	20	28	40	
		$V_{CC} = 4.5V, V_{IN} = 2.4V,$	16214	_	_	15	Ω
		Ion = 15mA	162214	20	35	48	
		$V_{CC} = 4 \text{ V}, V_{IN} = 2.4 \text{V},$ $I_{ON} = 15 \text{mA}$	16214	_	14	20	Ω

### Capacitance ( $TA = 25^{\circ}C$ , f = 1 MHz)

Parameters <sup>(5)</sup>	Description	Test Conditions	Тур	Units
Cin	Input Capacitance	$V_{IN} = 0V \text{ or } 3V$	4.5	pF
Coff	A/B Capacitance, Switch Off	$V_{IN} = 0V \text{ or } 3V$	5.5	pF

#### Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. Typical values are at Vcc = 5.0V,  $TA = 25^{\circ}C$  ambient and maximum loading.
- Not more than one output should be shorted at one time. Duration of the test should not exceed one second.
- 4. Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A,B) pins.
- 5. This parameter is determined by device characterization but is not production tested.



# **Power Supply Characteristics**

Parameters	Description	Test Condition	$\mathbf{s}^{(1)}$	Min.	Typ <sup>(2)</sup>	Max.	Units
Icc	Quiescent Power Supply Current	$V_{CC} = Max.$	$V_{IN} = GND \text{ or } V_{CC}$		0.1	10	μΑ
ΔIcc	Supply Current per Input @ TTL HIGH	$V_{CC} = Max.$	$V_{IN} = 3.4V^{(3)}$			2.5	mA
Іссь	Supply Current per Input per MHz <sup>(4)</sup>	Vcc = Max., A and B Pins Open Control Input Toggling 50% Duty Cycle				0.25	mA/ MHz

#### **Notes:**

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
- 2. Typical values are at Vcc = 5.0V,  $+25^{\circ}C$  ambient.
- 3. Per TTL driven input (VIN = 3.4V, control inputs only); A and B pins do not contribute to Icc.
- 4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design.

### PI5C16214 Switching Characteristics over Operating Range

			PI5C16214		
			$Vcc = 5V \pm 0.5V$		
Parameters	Description	Conditions <sup>(1)</sup>	Min.	Max.	Units
<b>t</b> PLH	Propagation Delay <sup>(2,3)</sup>	CL = 50pF		0.25	
tphl	Ax to Bx, Bx to Ax	$R_L = 500\Omega$			
tpzh	Bus Enable Time		1.5	6.5	ns
tpzl	$x\overline{OE}$ to Ax or Bx				
tphz	Bus Disable Time		1.5	5.5	
tplz	xOE to Ax or Bx				

### PI5C162214 Switching Characteristics over Operating Range

			PI5C162214		
			$Vcc = 5V \pm 0.5V$		
Parameters	Description	Conditions <sup>(1)</sup>	Min.	Max.	Units
tplh	Propagation Delay <sup>(2,3)</sup>	$C_L = 50pF$		1.25	
<b>t</b> PHL	Ax to Bx, Bx to Ax	$R_L = 500\Omega$			
tpzh	Bus Enable Time		1.5	6.5	ns
tpzl	xOE to Ax or Bx				
tphz	Bus Disable Time		1.5	5.5	
tplz	xOE to Ax or Bx				

#### **Notes:**

- 1. See test circuit and waveforms.
- 2. This parameter is guaranteed but not tested on Propagation Delays.
- 3. The bus switch contributes no propagational delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.

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