

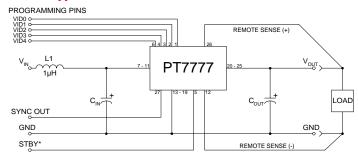
The PT7777 is a new series of high-performance, 32 Amp Integrated Switching Regulators (ISRs) housed in a 27-pin SIP package. The 32A capability allows easy integration of the latest high-speed, low-voltage µPs, ASICs, DSPs, and bus drivers into existing 5V systems.

The output voltage of the PT7777

can be easily programmed from 1.3V to 3.5V with a 5 bit input compatible with Intel's Pentium® Pro Processor. A differential remote sense is also provided which automatically compensates for any voltage drop from the ISR to the load.

680μF of output capacitance is required for proper operation.

Standard Application



Cin = Required 2400µF electrolytic Cout = Required 680μF electrolytic L1 = Optional 1μH input choke

Pin-Out Information

Pin	Function
1	VID0
2	VID1
3	VID2
4	VID3
5	STBY*- Stand-by
6	VID4
7	V_{in}
8	V_{in}
9	\mathbf{V}_{in}
10	$V_{_{\mathrm{in}}}$
11	$V_{_{\mathrm{in}}}$
12	Remote Sense Gnd
13	GND

Pin	Function
14	GND
15	GND
16	GND
17	GND
18	GND
19	GND
20	V _{out}
2 1	V _{out}
22	V _{out}
23	V _{out}
24	V _{out}
25	V _{out}
26	Remote Sense Vout
27	Sync Out

For STBY* pin; open = output enabled; ground = output disabled.

Specifications

Characteristics			PT7777 SI	PT7777 SERIES		
(T _a = 25°C unless noted)	Symbols	Conditions	Min	Тур	Max	Units
Output Current	I_{o}	$T_a = +60$ °C, 200 LFM, pkg N $T_a = +25$ °C, natural convection	0.1(1) 0.1(1)	=	32 26	A A
Input Voltage Range	Vin	$0.1A \le I_o \le 32A$	4.5 (2)	_	5.5	V
Output Voltage Tolerance	$\Delta V_{\rm o}$	$V_{\text{in}} = +5V, I_{\text{o}} = 32A$ 0°C \le T_a \le +55°C	Vo-0.03	-	Vo+0.03	V
Line Regulation	Reg _{line}	$4.5V \le V_{in} \le 5.5V$, $I_{o} = 32A$	_	±10	_	mV
Load Regulation	Reg_{load}	$V_{in} = +5V, 0.1 \le I_o \le 32A$	_	±10	_	mV
Vo Ripple/Noise pk-pk	V_n	$V_{in} = +5V, I_o = 32A$	_	50	_	mV
Transient Response with C _{out} = 680μF	${ m t_{tr} \over V_{os}}$	I _o step between 16A and 32A V _o over/undershoot	_	100 200	_	μSec mV
Efficiency	η	$V_{in} = +5V, I_o = 20A, V_o = 3.3V$	_	90	_	%
Switching Frequency	$f_{\rm o}$	$4.5V \le V_{in} \le 5.5V$ $0.1A \le I_o \le 32A$	650	700	750	kHz
Absolute Maximum Operating Temperature Range	T_a	_	0	_	+85	°C
Recommended Operating Temperature Range	Ta	Forced Air Flow = 200 LFM Over V _{in and} I _o Ranges	0	_	+65	°C
Storage Temperature	T_s	_	-40		+125	°C
Weight	_	Vertical/Horizontal		53/66	_	grams

- (1) ISR-will operate down to no load with reduced specifications. Please note that this product is not short-circuit protected.
- (2) The minimum input voltage is $4.5\mathrm{V}$ or $\mathrm{V}_{\mathrm{out}}$ + $1.2\mathrm{V}$, whichever is greater.

Output Capacitors: The PT7777 series requires a minimum output capacitance of 680µF for proper operation. Do not use Oscon type capacitors. The maximum allowable output capacitance is 30,000µF.

Input Filter: An input filter is optional for most applications. The input inductor must be sized to handle 32ADC with a typical value of 1µH. The input capacitance must be rated for a minimum of 2.6Arms of ripple current. For transient or dynamic load applications, additiona capacitance may be required.

Features

- +5V input
- 5-bit Programmable: 1.3V to 3.5V@32A
- High Efficiency
- Input Voltage Range: 4.5V to 5.5V
- Differential Remote Sense
- 27-pin SIP Package

Programming Information

VID3	VID2	VID1	VIDO	VID4=1 Vout	VID4=0 Vout	
1	1	1	1	2.0V	1.30V	
1	1	1	0	2.1V	1.35V	
1	1	0	1	2.2V	1.40V	
1	1	0	0	2.3V	1.45V	
1	0	1	1	2.4V	1.50V	
1	0	1	0	2.5V	1.55V	
1	0	0	1	2.6V	1.60V	
1	0	0	0	2.7V	1.65V	
0	1	1	1	2.8V	1.70V	
0	1	1	0	2.9V	1.75V	
0	1	0	1	3.0V	1.80V	
0	1	0	0	3.1V	1.85V	
0	0	1	1	3.2V	1.90V	
0	0	1	0	3.3V	1.95V	
0	0	0	1	3.4V	2.00V	
0	0	0	0	3.5V	2.05V	

Logic 0 = Pin 12 potential (remote sense gnd) Logic 1 = Open circuit (no pull-up resistors) VID3 and VID4 may not be changed while the unit

Ordering Information

PT7777 = 1.3 to 3.5 VoltsFor dimensions and PC board layout, see Package Style 1020 and 1030

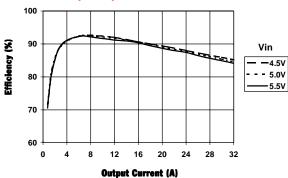
PT Series Suffix (PT1234X)

Case/Pin	
Configuration	

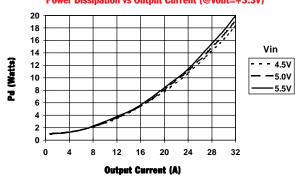
Comparation	
Vertical Through-Hole	N
Horizontal Through-Hole	Α
Horizontal Surface Mount	C

CHARACTERISTIC DATA

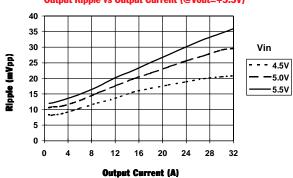




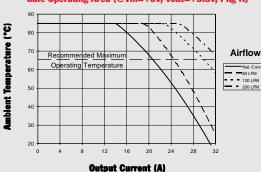
Power Dissipation vs Output Current (@Vout=+3.3V)



Output Ripple vs Output Current (@Vout=+3.3V)



Safe Operating Area (@Vin=+5V, Vout=+3.3V, Pkg N)



Note: SOA curves represent operating conditions at which internal components are at or below manufacturer's maximum rated operating temperatures.

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