



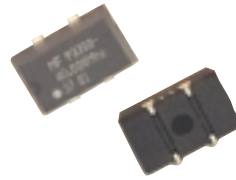
CRYSTAL OSCILLATORS HCMOS 3.3V

SURFACE MOUNT
P package
P3390, P3392

J-Lead Surface Mount

Commercial: -20° to 70°C

TRISTATE, 1 MHz to 66.6667 MHz



SPECIFICATIONS

P3390/P3392		
Frequency Range	1 MHz – 26 MHz	26.001 MHz – 66.6667 MHz
Frequency Stability	P3390: ±100 ppm P3392: ±50 ppm	
Operating Temperature Range	-20°C to +70°C	
Storage Temperature Range	-55°C to +125°C	
Supply Voltage	V _{DD} : 3.3 ± 0.3V	
Supply Current	10 mA MAX	20 mA MAX
Duty Cycle	TTL level CMOS level	45% – 55% 40% – 60%
Output Voltage	V _{OH} : V _{DD} — 0.4V MIN V _{OL} : 0.4V MAX	V _{OH} : V _{DD} — 0.4V MIN V _{OL} : 0.4V MAX
Jitter, Positive Edge to Positive Edge	100 ps, peak to peak, MAX	100 ps, peak to peak, MAX
Output Load	TTL CMOS	10TTL MAX 50pF MAX
Rise & Fall Time	t _r : 5 nsec MAX t _f : 5 nsec. MAX	t _r : 5 nsec MAX t _f : 5 nsec MAX
Start up Time	4 msec MAX	10 msec MAX
Input (OE) Voltage	V _{IH} : 2.0V MIN V _{IL} : 0.8V MAX	V _{IH} : 2.0V MIN V _{IL} : 0.8V MAX
Disable Current	6 mA MAX	6 mA MAX
Aging	±5ppm/year MAX	
Shock Resistance	±20 ppm MAX	

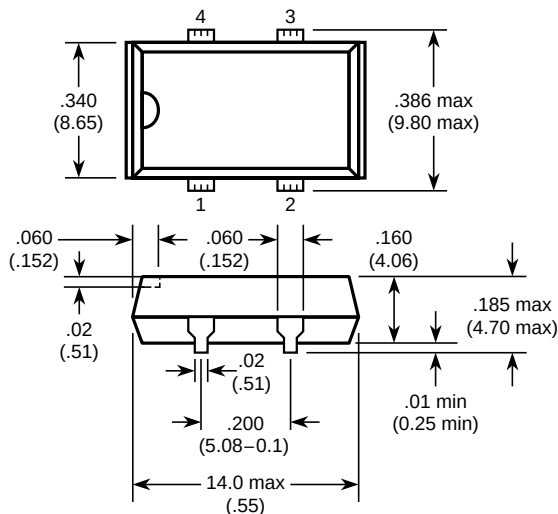
Description

The P3390/P3392 are replacements for the standard SG-615 plastic J-lead fixed frequency clock oscillators. The MF Electronics part has all the features of the 615, and also includes specifications for jitter.

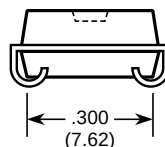
FEATURES

- Pin-for-pin replacement for SG615P, 3.3V
- No frequency multiplication techniques are used, which limits jitter to 100 ps pk-pk max.
- Frequency stability of ±100ppm or ±50ppm
- 1,000 units per reel

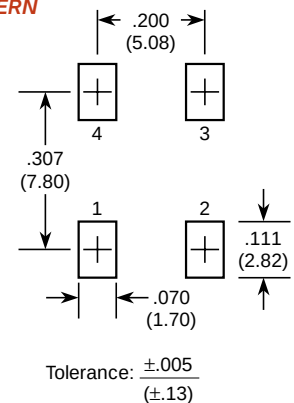
DIMENSIONS



Pin No.	Pin Function
1	OE
2	GND
3	OUT
4	V _{DD}



PCB PATTERN

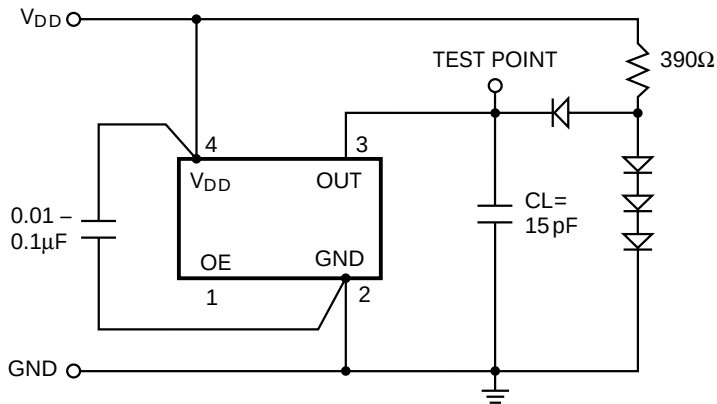




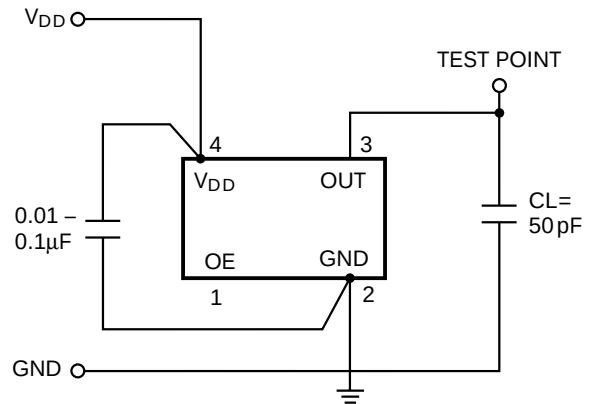
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SURFACE MOUNT
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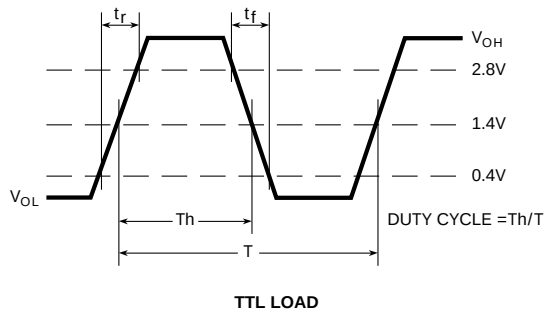
P3390/P3392 (TTL LOAD)



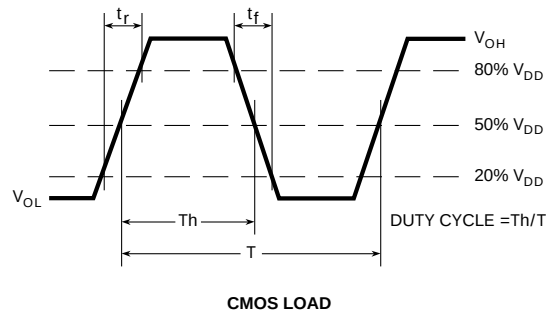
P3390/P3392 (CMOS LOAD)



OUTPUT WAVEFORMS



OUTPUT WAVEFORMS



HOW TO ORDER

For Part Number
Add Frequency to the Model
For Example...

P3390 - 3.6864M

Model is
P3390: 100 ppm
P3392: 50 ppm

Frequency is
3.6864 MHz

Please order in multiples of 1,000

SS#	Rev.
P3390	A

MF ELECTRONICS

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