

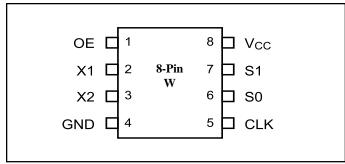


# 3.3V Precision **Clock Generator**

### **Features**

- No external capacitors (919 only)
- Excellent signal quality
- · Very low jitter
- Precise frequencies
- Minimal undershoot, ringback or overshoot
- Nearly perfect 50% duty cycle
- Four selectable clock frequencies
- Operates at  $V_{CC} = 3.3V$
- Output may be disabled to save power
- Small 150-mil wide SOIC-8 (W) package saves board area

# Pin Configuration



#### **Output CLK Frequency Selection**

Selection S1, S0	N/M <sup>(2)</sup>	Clock (using10 MHz Xtal)	Clock (using 16.6 MHz Xtal)	Clock (using 20 MHz Xtal)	Clock (using 22.1 MHz Xtal)	
00	2	_	33.3 <sup>(1)</sup>	40	44.2	
01	4	40 <sup>(1)</sup>	66.6 <sup>(1)</sup>	80 <sup>(1)</sup>	88.4	
10	5	50 <sup>(1)</sup>	83	100 <sup>(1)</sup>	110.5	
11	7	70 <sup>(1)</sup>	116	-	_	

#### Notes:

- 1. Indicates popular target frequencies.
- 2. The ratio of N/M may be changed by mask option.

# **Description**

The PI6C918 and PI6C919 are high-precision, low-voltage general-purpose clock generators that reach a maximum output frequency of 120 MHz. These devices use an external low-cost crystal to generate very accurate and stable system clocks.

These frequency synthesizers include a crystal oscillator, a programmable PLL and an output buffer. Any one of four different output frequencies can be selected via two select pins (S0, S1). The frequency multipliers are: 2, 4, 5, and 7.

The PI6C919 has on-chip capacitors, eliminating the need for external capacitors at the X1, X2 pins. This reduces overall parts count and board area, and increases reliability.

The OE (output enable) pin may be pulled down to disable the output buffer and save system power.

The small surface-mount SOIC-8 package is ideal for compact portable equipment applications.

## **Pin Description**

Pin Name	Pin#	I/O	Description
S0	6	I	Select 0, internal pull-up
S1	7	I	Select 1, internal pull-up
X1	2	С	Crystal or clock input, 8 to 23 MHz
X2	3	С	Crystal connection or no connect
CLK	5	О	Clock output
OE	1	I	OE = 1 Enable CLK output
			OE = 0 Disable CLK output

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1.I = Input, O = Output, C = Crystal

## **Product Speed Grades**

Part No.	Max CLK Output Freq.	Max VCO Frequency
PI6C918/919	80 MHz	160 MHz
PI6C918A/919A	100 MHz	200 MHz
PI6C918B/919B	120 MHz	240 MHz

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# **Maximum Ratings**

Storage Temperature65°C to+150°C
Ambient Temperature with Power Applied
Supply Voltage to Ground Potential (Inputs & Vcc Only)0.5Vto+7.0V
Supply Voltage to Ground Potential (Outputs & D/O Only)0.5V to +7.0V
DC Input Voltage0.5V to +7.0V
DC Output Current
Power Dissipation

#### Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

## **DC Electrical Characteristics**

Operating Range,  $V_{CC} = 3.3V \pm 10\%$  (918/919/918A/919A).  $V_{CC} = 3.3V \pm 5\%$  (918B/919B), Temperature 0°C to +70°C

Symbol	Description	Test Conditions		Min.	Тур.	Max.	Units
V <sub>OH</sub>	Output HIGH Voltage	$V_{CC} = Min.,$	$V_{OH} = -8 \text{mA}$	$V_{CC} - 0.4$	-	-	
V <sub>OL</sub>	Output LOW Voltage	$V_{\rm IN} = V_{\rm IH} \text{ or } V_{\rm IL}$	$V_{\rm OL} = 8 \text{mA}$	-	-	0.4	$\left  \begin{array}{c} \mathbf{v} \end{array} \right $
V <sub>IH</sub>	Select Input HIGH Voltage	$V_{CC} = 5V$		2.0	-	-	v
V <sub>IL</sub>	Select Input LOW Voltage			-	-	0.8	
V <sub>IH</sub>	Select Input HIGH Current	$V_{CC} = Max., V_{IN} = V_{CC}$	-	-	5		
$V_{\mathrm{IL}}$	Select Input LOW Current	$V_{CC} = Max., V_{IN} = 0V$		-	-	-10	μΑ
I <sub>CC</sub>	Supply Current	V <sub>CC</sub> = Max., CLK frequency =	-	10	20	mA	
RL	X1, X2 Negative Resistance		-1,000	-2,000	-	Ω	
R <sub>FB</sub>	On-chip X1, X2 Internal Feedback Resistance			227	-	-	kΩ

Note: There is no guarantee that on-chip capacitance eliminates the need for external capacitors.

## **AC Electrical Characteristics**

Operating Range,  $V_{CC} = 3.3V \pm 10\%$  (918/919/918A/919A).  $V_{CC} = 3.3V \pm 5\%$  (918B/919B), Temperature 0°C to +70°C

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Symbol	Description	Test Conditions <sup>(1)</sup>	Min.	Тур.	Max.	Units
dт	Duty Cycle,	VTH = 1.4V	45	50	55	%
$T_R, T_F$	Rise/Fall Time	20% and 80% Vcc	_	_	3	ns
tPU	Power-up Time <sup>(2)</sup>		_		10	ms
FXTAL	Crystal Input Frequency	918/918A/919/919A	10	_	20	MHz
		918B/919B	10	_	24	
TJLS	One Sigma Jitter	f <sub>VCO</sub> ≥60 MHz	_		60	ps

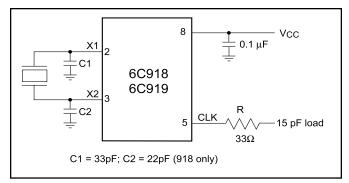
## Notes:

- 1. Test load of 15pF, 2-inch trace
- 2. Time from when the power supply reaches 90% until locked clock output waveform is reached.

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## **External Circuit**



## **Notes:**

- 1. Locate crystal as close to X1, X2 pins as possible.
- 2. Connect a 0.1µF bypass capacitor at Vcc (Pin 8).

# **Ordering Information**

Part No.	Max. Freq.	Package
PI6C918W	80 MHz	
PI6C918AW	100 MHz	
PI6C918BW	120 MHz	Names COIC 9
PI6C919W	80 MHz	Narrow SOIC-8
PI6C919AW	100 MHz	
PI6C919BW	120 MHz	

## **Crystal Specifications**

It is the customer's responsibility to procure the prototype and production crystals directly from crystal vendors.

Typical Characteristic(1)	Value
Frequency, F	18.43200 MHz nominal
Frequency Tolerance at 25°C	±50 ppm
Frequency Stability vs. Temperature vs. Aging	±35 ppm (0°C to 70°C) ±15 ppm/4 years
Oscillation Mode	Fundamental
Calibration Mode	Parallel resonant
Load Capacitance, C <sub>L</sub>	20 pF, Fundamental
Shunt Capacitance, C <sub>o</sub>	7 pF max., Fundamental
Series Resistance, R1	50Ω Typical
Drive Level	1mΩ Typical
Operating Temperature	0° to 70°C
Storage Temperature	-40° to 85°C

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