100-W 8.5-A Programmable Isolated DC/DC Converter



(Revised 9/4/2001)



Features

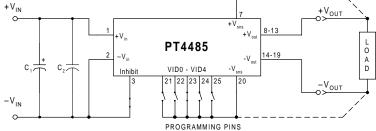
- 36V to 75V Input Range
- Temp Range: -40° to +100°C
- 1500 VDC Isolation
- 89% Efficiency
- Programmable Output Voltage 6.5V to 17.5V
- Remote On/Off
- Differential Remote Sense
- N+1 Current Sharing
- Over-Current Protection
- Over-Temperature Protection
- Over-Voltage Protection
- Solderable Copper Case

Description

The PT4485 Excalibur™ module combines state-of-the-art power conversion technology with unparalleled flexibility. Operating off a standard 48V telecom input, the PT4485 provides a full 100W output at load currents up to 8.5A, and over the programmable output voltage range of 6.5V to 17.5V.

The PT4485 features high efficiency, ultra-fast transient response, and the capability for true N+1 current sharing. This product also includes output short circuit and over-temperature protection.

Standard Application



- C₁ = Optional 33μF, 100V electrolytic capacitor
- C₂ = Optional 1µF, 100V ceramic capacitor
- Programming pins, VID0–VID4, are shown configured for Vo =12.0V $\,$
- For normal operation, pin 3 (Inhibit) must be connected to -Vin.
- For operation in N+1 configuration, consult the related application note.
- Pins 6 & 26 are used for N+1 configurations only.

Specifications

Characteristics	Symbols	Conditions	PT4485			
(T _a =25°C unless noted)		(V _{in} =48V, V _o =12.0V unless noted)	Min	Тур	Max	Units
Output Current	I_o	Over V _{in} range (100W max)	0.1	_	8.5 (1)	A
Current Limit	I_{cl}	$V_{in} = 36V$	_	10	_	A
Current Sharing		Single line reference to -V _{sense}	_	_	±10	%
Input Voltage Range	V_{in}	$I_o = 0$ to $I_o max$	36	48	75	V
Output Voltage Tolerance	ΔV_{o}	Over V_{in} range, $I_o = I_o max$ $T_a = -40$ to $+100$ °C case	_	±1.0	±2.0	$%V_{o}$
Line Regulation	Reg _{line}	Over V _{in} range @ max I _o	_	±0.1	±1.0	$%V_{o}$
Load Regulation	Regload	0 to 100% of I _o max	_	±0.5	±1.0	$%V_{o}$
Vo Ripple/Noise	V_n	$I_o = I_o max$	_	120	150	mV_{pp}
Transient Response	t _{tr}	50% to 75% I_{o} max @ 0.1A/µs V_{o} over/undershoot (no ext caps)	_	N/A 1.0		μSec %V _o
		50% to 100% I_{o} max @1.0A/ μ s V_{o} over/undershoot (no ext. caps)		200 4	_	μSec %V _o
Vo Rise Time	$V_{ m otr}$	At turn-on	_	_	10	mSec
Efficiency	η	$I_o = 5 A$	_	89	_	%
Switching Frequency	f_{o}	_	_	300	_	kHz
Remote On/Off	Off On	Referenced to -V _{in}	+2.5 -0.5	=	+75.0 +0.8	VDC
Over-Voltage Protection	OVP	Shutdown and latch off	_	125	_	$%V_{o}$
Isolation	_	_	1500		_	VDC
Operating Temperature Range	T_{c}	Measured at center of case	-40		+100	°C
Over-Temperature Shutdown	OTP	Case temperature - Auto reset	_	+110	_	°C
Storage Temperature	T_s	_	-40	_	+125	°C
Reliability	MTBF	Per Bellcore TR-332 50% stress, t =40°C, ground benign	1.7	_	_	10 ⁶ H
Mechanical Shock	_	Per Mil-STD-883D, Method 2002.3, 1mS, Half-sine, mounted to a fixture	_	500	_	G's
Mechanical Vibration (Mil-STD-883D, 20-2000Hz)	_	Vertical (Suffix N) Horizontal (Suffixes A, C)	_	10 (2) 20 (2)	_	G's
Weight	_	_	_	90	_	grams

Notes: (1) The maximum output current is limite to 8.5A or 100/V_{OUT}, whichever is less.
(2) Only the case pins on the through-hole package types (suffixes N & A) must be soldered. For more information see the applicable package outline drawing.



100-W 8.5-AProgrammable Isolated DC/DC Converter

Pin-Out Information

Pin	Function	Pin	Function
1	$+V_{in}$	14	$-V_{ m out}$
2	$-V_{ m in}$	15	$-V_{ m out}$
3	Inhibit	16	$-V_{ m out}$
4	Do not connect	17	$-V_{ m out}$
5	Do not connect	18	$-V_{out}$
6	Sync	19	$-V_{ m out}$
7	$+V_{sense}$	20	$-V_{ m sense}$
8	$+V_{out}$	21	VID0
9	$+V_{ m out}$	22	VID1
10	$+V_{out}$	23	VID2
11	$+V_{out}$	24	VID3
12	$+V_{out}$	25	VID4
13	$+V_{out}$	26	Share

Programming Information

				VID4=1	VID4=0
VID3	VID2	VID1	VIDO	Vout	Vout
1	1	1	1	10.0	6.5
1	1	1	0	10.5	6.75
1	1	0	1	11.0	7.0
1	1	0	0	11.5	7.25
1	0	1	1	12.0	7.5
1	0	1	0	12.5	7.75
1	0	0	1	13.0	8.0
1	0	0	0	13.5	8.25
0	1	1	1	14.0	8.5
0	1	1	0	14.5	8.75
0	1	0	1	15.0	9.0
0	1	0	0	15.5	9.25
0	0	1	1	16.0	9.5
0	0	1	0	16.5	9.75
0	0	0	1	17.0	10.0
0	0	0	0	17.5	10.25

Logic 0 = Pin 20 potential (remote sense gnd)
Logic 1 = Open circuit (no pull-up resistors)
VID4 may not be changed while the unit is operating.

Ordering Information

PT4485 \blacksquare =+6.5V to 17.5V

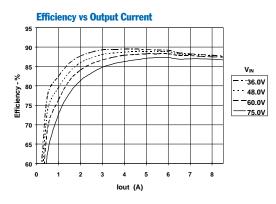
PT Series Suffix (PT1234x)

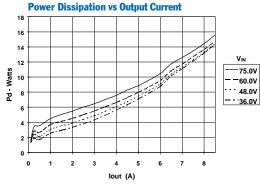
Case/Pin Configuration	Order Suffix	Package Code
Vertical	N	(EKD)
Horizontal	Α	(EKA)
SMD	C	(EKC)

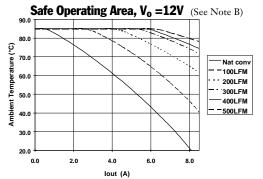
(Reference the applicable package code drawing for the dimensions and PC board layout)

TYPICAL CHARACTERISTICS

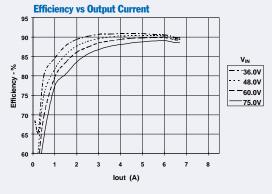
PT4485, V_o =12.0V (See Note A)

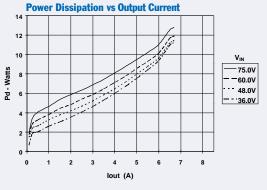


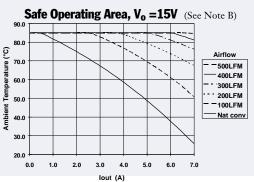




PT4485, V₀ =15.0V (See Note A)







Note A: All data listed in the above graphs has been developed from actual products tested at 25°C. This data is considered typical data for the DC/DC Converter.

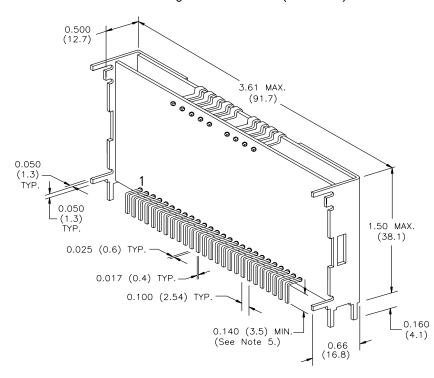
Note B: SOA curves represent operating conditions at which the temperature of the metal case is at or below the maximum specified 100°C

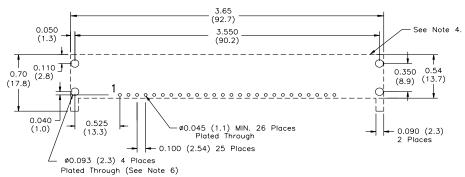
Suffix N

(Revised 6/30/2000)

PACKAGE INFORMATION AND DIMENSIONS

Vertical Through-Hole Mount (Suffix N)





PC Layout

Notes: (Rev. E)

- 1: All dimensions are in inches (mm). 2: 2 place decimals are ±.030 (±0.8mm). 3: 3 place decimals are ±.010 (±0.3mm).
- 4: Recommended mechanical keep out area (dotted line).
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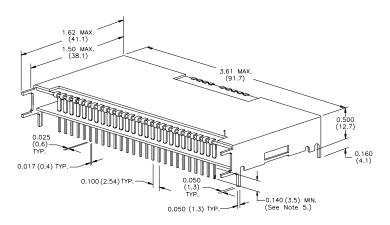


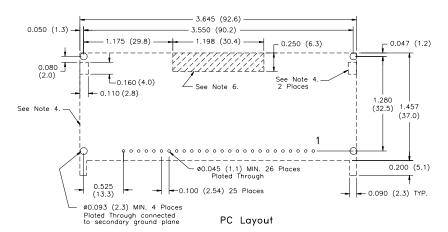
Suffix A

(Revised 1/17/2001)

PACKAGE INFORMATION AND DIMENSIONS

Horizontal Through-Hole Mount (Suffix A)





Notes: (Rev. G)

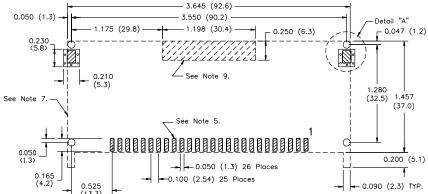
- 1: All dimensions are in inches (mm).
- 2: 2 place decimals are $\pm .030$ (± 0.8 mm). 3: 3 place decimals are $\pm .010$ (± 0.3 mm).
- 4: Recommended mechanical keep out area (dotted lines).
- 5: Electrical pin length mounted on printed circuit board seating plane to pin end.
 6: No copper, power or signal traces in this area.

Suffix C

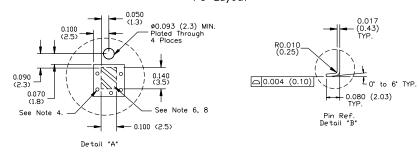
(Revised 1/17/2001)

PACKAGE INFORMATION AND DIMENSIONS

Horizontal Surface Mount (Suffix C) 1.62 MAX. (41.1) 1.50 MAX. (38.1) 0.0025 (0.6) TYP. (91.7) 0.160 (4.1) 0.100 (2.54) TYP. 0.050 (1.3) TYP.



PC Layout



Notes: (Rev. E)

- 1: All dimensions are in inches (mm).
- 2: 2 place decimals are ±.030 (±0.8mm).
- 3: 3 place decimals are $\pm .010$ (± 0.3 mm).
- 4: Vias are recommended to improve copper adhesion.
- 5: Power pin connections should utilize two or more vias per input, ground and output pin.
- 6: Solder mask openings to copper island for solder joints to mechanical pins.
- 7: Recommended mechanical keep out area (dotted lines).
- 8: Electrically connected case to secondary ground plane.
- 9: No copper, power or signal traces in this are.



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