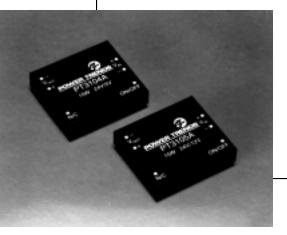
## PT3100 Series

24V

15 WATT 24V TO 5V/12V/15V ISOLATED DC-DC CONVERTER

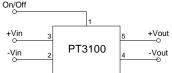
**Revised 5/15/98** 



- Power Density 15 Watts/in<sup>3</sup>
- Wide Input Voltage Range 18V to 40V
- 81% Efficiency
- 500 VDC Isolation
- Small Footprint
- No External Components Required

Power Trends' PT3104A (5V), PT3105A (12V) and PT3106A (15V) Isolated DC-DC Converters advance the state-of-the-art for board-mounted converters by employing high switching frequencies greater than 650 KHz and planar magnetics and surface-mount construction. They feature the industry's smallest footprint, a power density of 15 Watts/in³, and operate at 80% efficiency. They are designed for Telecom, Industrial, Computer, Medical, and other distributed power applications requiring input-to-output isolation.

### **Standard Application**



#### **Specifications**

Characteristics		Conditions	PT3100 SERIES			
(T <sub>a</sub> =25°C unless noted)	Symbols		Min	Тур	Max	Units
Output Current	Io	Over $V_{in}$ range, $V_{o} = 5V$	0	_	3.0	A
		$V_o = 12V$ $V_o = 15V$	0		1.25 1.0	A A
Current Limit	$I_{cl}$	$V_{in} = 18V$ , $V_{o} = 5V$	_	4.0	_	A
		$V_o = 12V$ $V_o = 15V$	_	1.75 1.4	_	A A
On/Off Standby Current	I <sub>in standby</sub>	V <sub>in</sub> = 24V, Pin 1 = -V <sub>in</sub>	_	7	10	mA
Short Circuit Current	$I_{sc}$	$V_{in} = 24V$ , $V_{o} = 5V$	_	6.25	_	A
		$V_o = 12V$ $V_o = 15V$	_	2.5 2.0	_	A A
Inrush Current	$I_{ir}$	$V_{\rm in}$ = 24V @ max $I_{\rm o}$	_	1.0	2.0	A
	t <sub>ir</sub>	On start-up		1.0	5.0	mSec
Input Voltage Range	V <sub>in</sub>	$I_o = 0.1$ to max $I_o$	18.0	24.0	40.0	V
Output Voltage Tolerance	$\Delta  m V_o$	Over V <sub>in</sub> Range T <sub>A</sub> = -20°C to +70°C	_	±1.0	±2.0	$%V_{o}$
Ripple Rejection	RR	Over V <sub>in</sub> range @ 120 Hz	_	60	_	dB
Line Regulation	Reg <sub>line</sub>	Over V <sub>in</sub> range @ max I <sub>o</sub>		±0.2	±1.0	$%V_{o}$
Load Regulation	Reg <sub>load</sub>	10% to 100% of I <sub>o</sub> max	_	±0.4	±1.0	$%V_{o}$
V <sub>o</sub> Ripple/Noise	$V_n$	$V_{in}$ =24V, $I_o$ =3.0A, $V_o$ =5V $V_{in}$ =24V, $I_o$ =1.25A, $V_o$ =12V	_	75 75	100 150	${}^{\mathrm{mV}_{\mathrm{pp}}}_{\mathrm{mV}_{\mathrm{pp}}}$
		$V_{in}$ =24V, $I_{o}$ =1.25A, $V_{o}$ =15V	_	100	200	$\mathrm{mV_{pp}}$
Transient Response	t <sub>tr</sub>	$50\%$ load change $V_{\rm o}$ over/undershoot	_	125 3.0	200 5.0	μSec %V <sub>o</sub>
Efficiency	η	$V_{in}$ =24V, $I_o$ =3.0A, $V_o$ =5V $V_{in}$ =24V, $I_o$ =1.25A, $V_o$ =12V	_	80 80	_	%
		$V_{\text{in}}$ =24V, $I_{\text{o}}$ =1A, $V_{\text{o}}$ =15V		81	=	%
Switching Frequency	$f_{0}$	Over $V_{in}$ and $I_o$ , $V_o=5V$ $V_o=12V/15V$	800 600	850 650	900 700	kHz kHz
Recommended Operating	Ta	V <sub>in</sub> = 24V @ max I <sub>o</sub>	-20		+70*	°C
Temperature Range	^	Free air convection, (40-60LFM)			.,,	
Thermal Resistance	$\theta_{ja}$	Free Air Convection, (40-60LFM)		14		°C/W
Case Temperature	T <sub>c</sub>	@ Thermal shutdown			100	°C
Storage Temperature	T <sub>s</sub>		-40		110	°C
Mechanical Shock	_	Per Mil-STD-202F, Method 213B, 6mS, Half-sine, mounted to a PCB	_	50	_	G's
Mechanical Vibration	_	Per Mil-STD-202F, Method 204D, 10-500Hz, Soldered in a PCB	_	10	_	G's
Weight	_	_	_	28	_	grams
Isolation	_	_	500	_	_	V
Capacitance Resistance			 10	1100	_	pF MΩ
Flammability	_	Materials meet UL 94V-0	10			17132
Remote On/Off	On Off	Open or 2.5 to 7.0 VDC above -V <sub>in</sub> Short or 0 to 0.8 VDC above -V <sub>in</sub>				
* See Thermal Derating Cu		onort of 0 to 0.0 v DC above -vin				

#### **Pin-Out Information**

Pin Function	
1	Remote ON/OFF
2	-V <sub>in</sub>
3	+ $ m V_{in}$
4	$-V_{ m out}$
5	+ $ m V_{out}$
6	Do not connect

#### **Ordering Information**

Through-Hole

PT3104A = 5 Volts

PT3105A = 12 Volts

PT3106A = 15 Volts

Surface Mount

**PT3104C** = 5 Volts **PT3105C** = 12 Volts **PT3106C** = 15 Volts

(For dimensions and PC board layout, see Package Style 700.)

24V Bus Products

# PT3100 Series

2 4 V

#### CHARACTERISTIC DATA

#### PT3104, 5.0 VDC PT3105, 12.0 VDC PT3106, 15.0 VDC (See Note 1) (See Note 1) (See Note 1) **Efficiency vs Output Current Efficiency vs Output Current Efficiency vs Output Current** - - 18V — — - 18V 80 Efficiency ---- 24V --- 24V ---- 24V 70 70 - - - 32V - - — 32V - - - 32V 60 60 60 - 40V 40V 50 50 50 40 0.5 1.5 lout-(Amps) lout-(Amps) lout-(Amps) **Ripple vs Output Current Ripple vs Output Current Ripple vs Output Current** 110 110 110 - - - 40V - - - 40V Ripple-(mV) Ripple-(mV) Ripple-(mV) 70 70 70 --- 32V --- 32V - - - 24V - - - 24V - 24\ 50 50 18V - 18V 30 10 10 0.5 1.5 0.25 0.5 0.75 0.25 0.5 0.75 1.25 lout-(Amps) lout-(Amps) lout-(Amps) Thermal Derating (T<sub>a</sub>) Thermal Derating (Ta) Thermal Derating (Ta) (See Note 2) (See Note 2) (See Note 2) 1.25 2.5 0.8 lout-(Amps) lout-(Amps) 2 0.75 0.6 0.2 0.25 0.5 18 20 22 24 26 28 30 32 34 36 38 40 18 20 22 24 26 28 30 32 34 36 38 40 18 20 22 24 26 28 30 32 34 36 38 40 Vin-(Volts) Vin-(Volts) Vin-(Volts) **Power Dissipation vs Output Current Power Dissipation vs Output Current Power Dissipation vs Output Current** Vin 40V - - 40V PD-(Watts) PD-(Watts) PD-(Watts) --- 32V - - - 24V --- 24V - - - 24\ — - 18V - - 18V 0.5 1.25 0.5 0.75 0.5 1.5 2 2.5 0.25 0.75 0.25 lout-(Amps) lout-(Amps) lout-(Amps)

Note 1: All data listed in the above graphs, except for derating data, has been developed from actual products tested at 25°C. This data is considered typical data for the DC-DC Converters.

Note 2: Thermal derating graphs are developed in free air convection cooling of 40-60 LFM.

#### **IMPORTANT NOTICE**

Texas Instruments and its subsidiaries (TI) reserve the right to make changes to their products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgement, including those pertaining to warranty, patent infringement, and limitation of liability.

TI warrants performance of its semiconductor products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

CERTAIN APPLICATIONS USING SEMICONDUCTOR PRODUCTS MAY INVOLVE POTENTIAL RISKS OF DEATH, PERSONAL INJURY, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE ("CRITICAL APPLICATIONS"). TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS. INCLUSION OF TI PRODUCTS IN SUCH APPLICATIONS IS UNDERSTOOD TO BE FULLY AT THE CUSTOMER'S RISK.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance or customer product design. TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used. TI's publication of information regarding any third party's products or services does not constitute TI's approval, warranty or endorsement thereof.

Copyright © 1999, Texas Instruments Incorporated