

PCT388



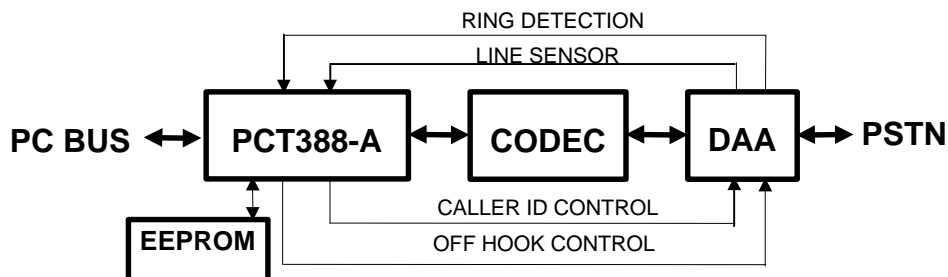
ASIC Features PCT388

- ◆ V.90 56K/14.4K Fax/ADPCM Voice/V.80
- ◆ Optional DSVD/HSP Speakerphone™ as a software upgrade through full-duplex audio chip (or with external low-cost PC-TEL ASIC/Voice Codec)
- ◆ Supports Windows™ and Windows NT™ operating systems
- ◆ Received Win 95 logo
- ◆ On chip PnP logic. Supports 16-bit PnP address. Needs 256 bytes external serial EEPROM
- ◆ PCT388 comes in a 100-pin PQFP package or 100-pin TQFP package
- ◆ Pin-compatible with the PCT288I in non-PnP mode
- ◆ Auto selection of COM ports/IRQs
- ◆ Supports Ties AT commands
- ◆ High throughput virtual (software) UART allows DTE rate over 115,200 bps
- ◆ Low power consumption (150mW operating @5V) and auto power management
- ◆ Excellent choice for low power applications

The PCT388 is a high speed Host Signal Processing Modem (HSP Modem™) chip implementing ITU-T V.34 protocols and 56K technology. Several patents are pending on the PC_TEL HSP Modem software and hardware architecture. The PCT388 supports baseline data/fax/voice communication features without the expensive DSP data pump, controller, UART and memory components found in conventional DSP modems. Luxury features such as simultaneous voice and data (DSVD) and HSP Speakerphone™ can be supported through any full-duplex audio chip. In the absence of full-duplex audio chip, external low-cost PC-TEL ASIC/Voice Codec is needed for DSVD/HSP Speakerphone.

The PCT388 has been designed to operate in a Windows environment ensuring product compatibility over the life of the product. The PCT family of products are software upgradable for speed, features, and enhancements thus eliminating the need to ever have to "swap out" product.

The HSP Modem reference design, evaluation units, user's manual, schematics and Gerber files will be provided on request to qualified customers.



Specifications

- **Data Modulation Standards**
V.90/K56 Flex
ITU-T V.34, V.32bis, V.32, V.23,
V.22bis, V.22A/B, V.21
Bell 212A and 103

Data Compression V.42bis, MNP Class 5
Error Correction V.42 LAPM and MNP 2-4
- **Fax Modulation or Protocol Standards**
V.17, V.29, V.27ter, V.21 Channel 2
Group 3 and EIA Class 1
- **System Requirements**
Pentium class CPU (100MHz+), 133 MHz+ Cyrix 6x86MX Processor
8MB RAM/256K L2 cache with ISA bus expansion slot

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