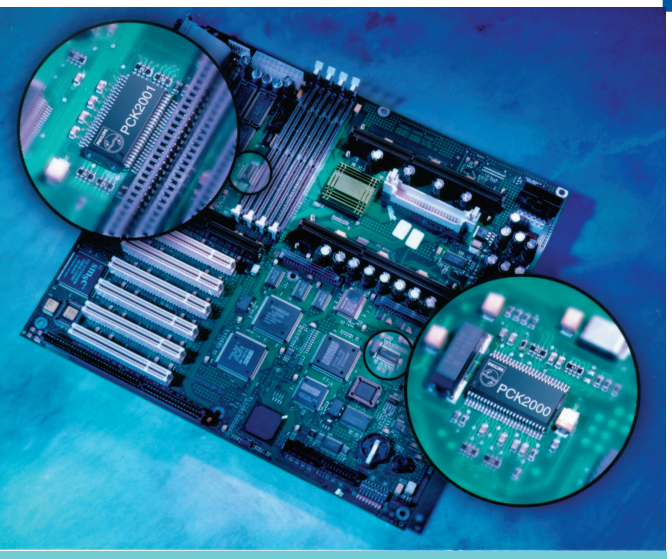
# PCK2000
CK97 System Clock Generator

# PCK2001
14.318-150 MHz 1:18 Clock Buffer




## Description

The PCK2000 is a CK97 system clock generator that synthesizes 66/100 MHz single-ended clock frequencies. The PCK2001 is a 1:18 clock buffer that fans out these clock frequencies to the system's dual in-line memory modules (DIMMs). Together the PCK2000 and PCK2001 provide a complete solution for SDR enabled systems.

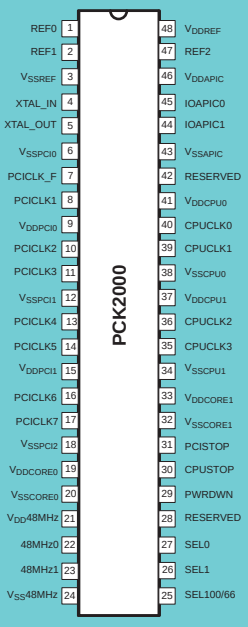
## PCK2000 Advantages

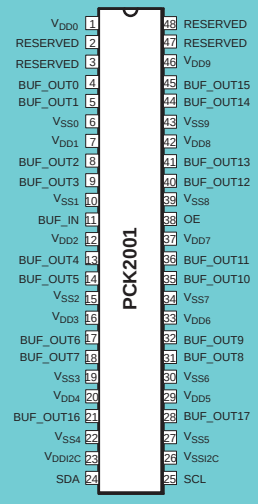
- Supports CK97 66/100 MHz frequencies
- Integrates system PLL and USB PLL
- Mixed 2.5V and 3.3V operation
- 4 CPU clocks with pin-to-pin skew < 175 ps
- 4 CPU clocks with cycle-to-cycle jitter < 250 ps
- 8 PCI clocks with pin-to-pin skew < 500 ps
- 8 PCI clocks with cycle-to-cycle jitter < 500 ps
- 3 IOAPIC clocks with pin-to-pin skew < 250 ps
- 3 IOAPIC clocks with cycle-to-cycle jitter < 500 ps
- 2 USB 48-MHz clocks with PLL disable
- Efficient power management mode
- 48-pin plastic SSOP package
- CPU to PCI offset between 1.5 ns and 4.0 ns
- Optimized for use with PCK2001 or PCK2001M

## PCK2001 Advantages

- Buffers frequencies up to 150 MHz
- Individual buffered clock enable/disable via I2C
- 3.3V operation
- 18 buffered clocks with pin-to-pin skew < 250 ps
- 18 buffered clocks with part-to-part skew < 250 ps
- 18 buffered clocks with cycle-to-cycle jitter < 60 ps
- Propagation delay < 2.5 ns
- 48-pin plastic SSOP package
- Spread spectrum compliant
- Optimized for use with PCK2000 or PCK2000M

## Pin Configurations



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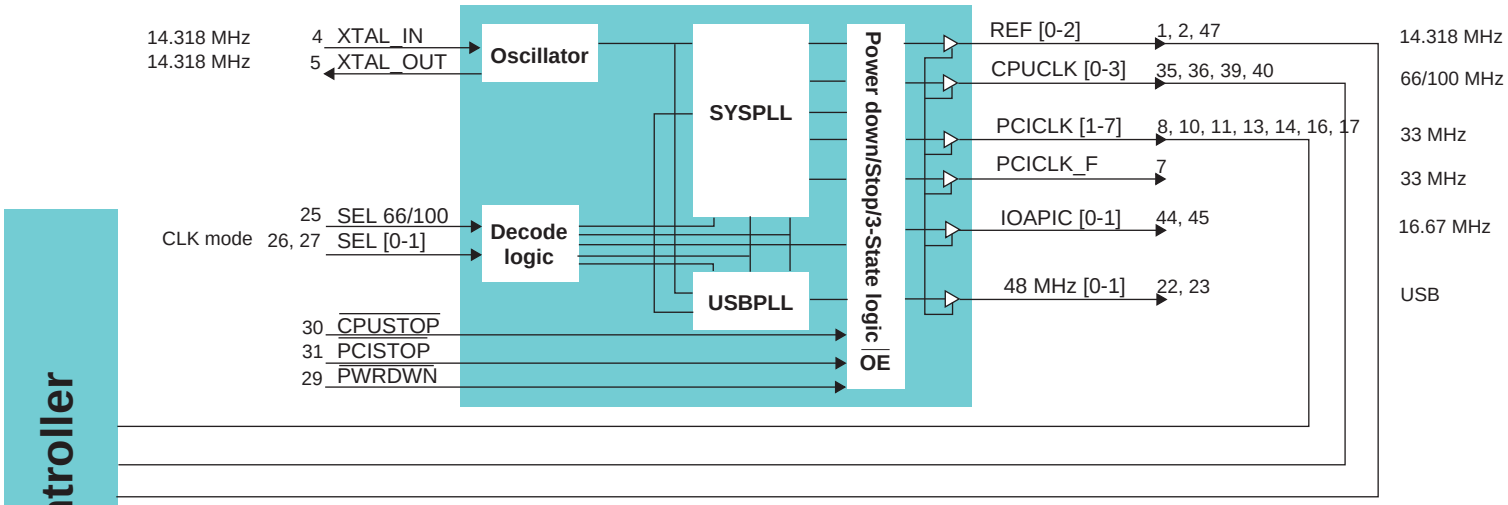
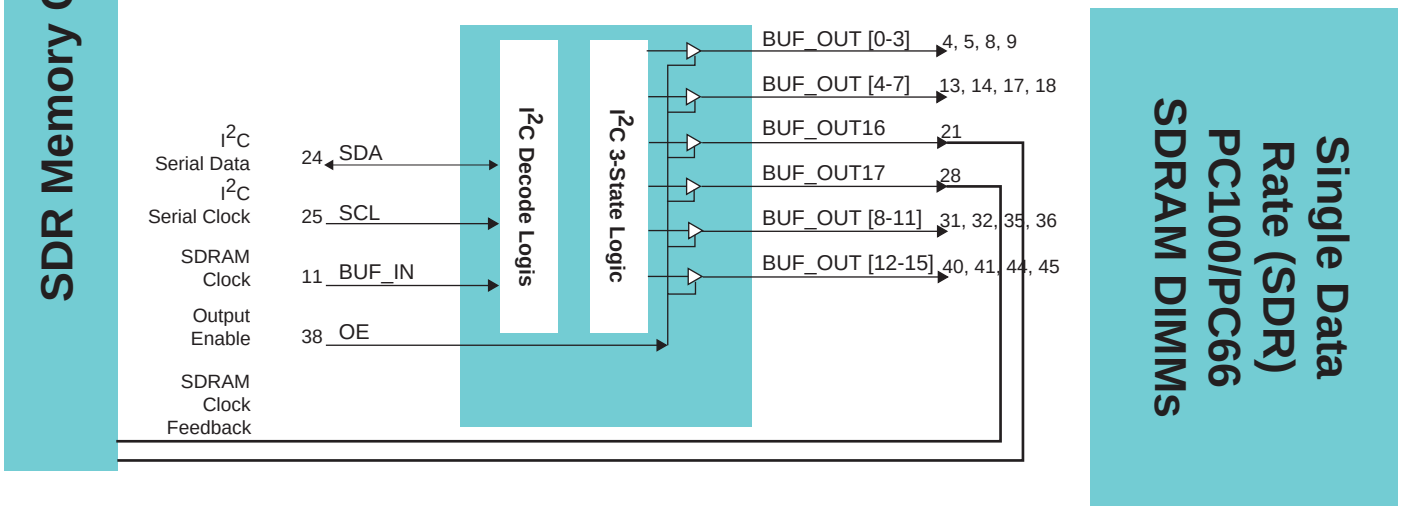
## PCK2000 Block Diagram
## CK97 System Clock Generator




## PCK2001 Block Diagram
## 14.318 - 150 MHz 1:18 Clock Buffer

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