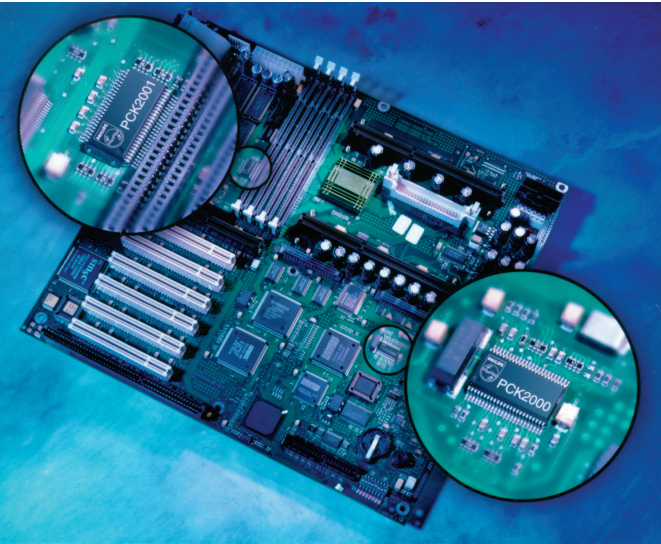


PCK2000

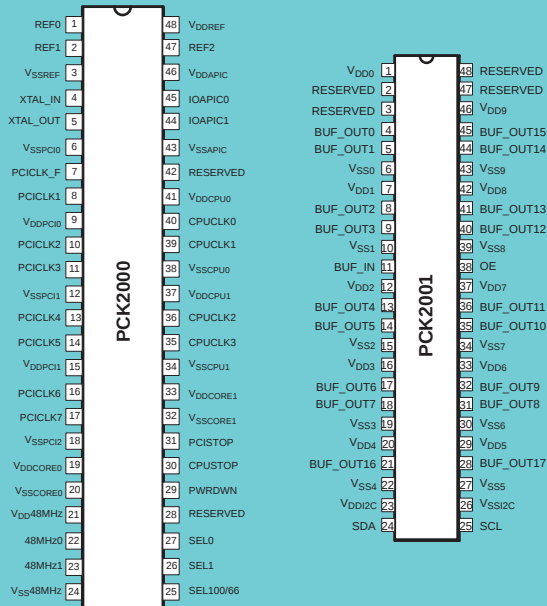
CK97 System Clock Generator

PCK2001

14.318-150 MHz 1:18 Clock Buffer



Pin Configurations



Description

The PCK2000 is a CK97 system clock generator that synthesizes 66/100 MHz single-ended clock frequencies. The PCK2001 is a 1:18 clock buffer that fans out these clock frequencies to the system's dual in-line memory modules (DIMMs). Together the PCK2000 and PCK2001 provide a complete solution for SDR enabled systems.

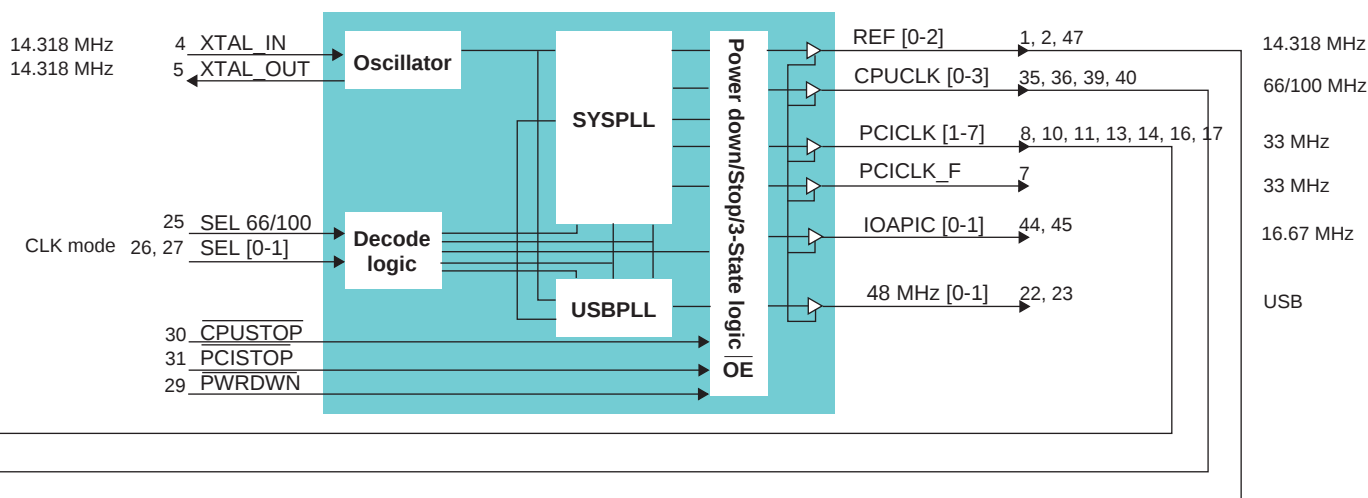
PCK2000 Advantages

- Supports CK97 66/100 MHz frequencies
- Integrates system PLL and USB PLL
- Mixed 2.5V and 3.3V operation
- 4 CPU clocks with pin-to-pin skew < 175 ps
- 4 CPU clocks with cycle-to-cycle jitter < 250 ps
- 8 PCI clocks with pin-to-pin skew < 500 ps
- 8 PCI clocks with cycle-to-cycle jitter < 500 ps
- 3 IOAPIC clocks with pin-to-pin skew < 250 ps
- 3 IOAPIC clocks with cycle-to-cycle jitter < 500 ps
- 2 USB 48-MHz clocks with PLL disable
- Efficient power management mode
- 48-pin plastic SSOP package
- CPU to PCI offset between 1.5 ns and 4.0 ns
- Optimized for use with PCK2001 or PCK2001M

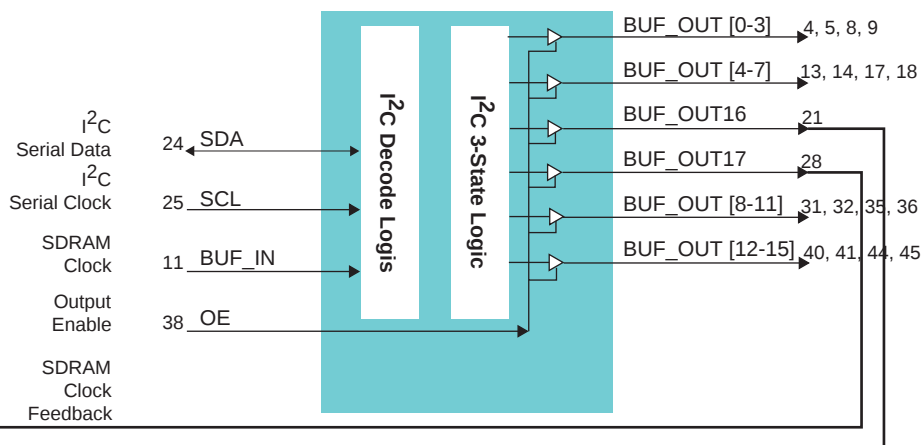
PCK2001 Advantages

- Buffers frequencies up to 150 MHz
- Individual buffered clock enable/disable via I2C
- 3.3V operation
- 18 buffered clocks with pin-to-pin skew < 250 ps
- 18 buffered clocks with part-to-part skew < 250 ps
- 18 buffered clocks with cycle-to-cycle jitter < 60 ps
- Propagation delay < 2.5 ns
- 48-pin plastic SSOP package
- Spread spectrum compliant
- Optimized for use with PCK2000 or PCK2000M

PCK2000 Block Diagram CK97 System Clock Generator



PCK2001 Block Diagram 14.318 - 150 MHz 1:18 Clock Buffer



Single Data
Rate (SDR)
PC100/PC66
SDRAM DIMMS

For more information, contact your local Philips office:

North America
Tel: 1 800 234-7381

Internet (in English):
www.philipslogic.com/products/pc

Europe
Fax: +31 10 2843181

Asia
Fax: 886 2 2134-2941

Japan / Korea
Fax: +81-3-3740-5057
Internet (in Japanese):
www.philipsco.jp/semicon

© Philips Electronics N.V. 1999

All rights reserved. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice. No liability will be accepted by the publisher for any consequence of its use. Publication thereof does not convey nor imply any license under patent - or industrial or intellectual property rights.

Printed in the USA

241609/7K/FP/2pp/0799 9397-750-06192