



- ” Consist exclusively of passive element.
- ” Handle all POTS loop currents from 0mA to 100mA.
- ” Designed for implementation of ADSL CPE application.
- ” Provides excellent isolation between DSL and POTS.
- ” Designed to meet G.Lite requirement.

Electrical Characteristic@25°C

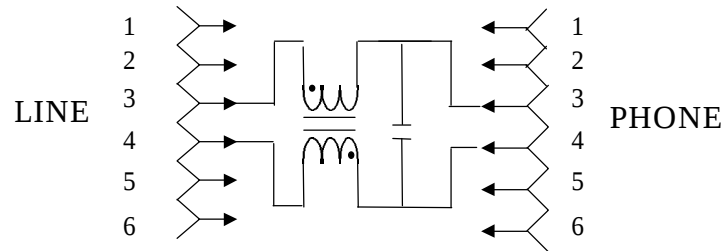
Part NO: POTS-CP1661A-71					
Parameter	Condition	Min.	Typ.	Max	Unit
			3		
Impedance	@300-3.4KHz		Note1		
Attenuation	@25KHz	-15			dB
	@50KHz	-25			dB
DCR	Each line			5.0	Ω
Group delay	@0.6-3.2KHz			100	μ s
DC Current				100	mA
Longitudinal balance	@0.2-3KHz	-53			dB
Insertion loss	@0.2-4KHz		Note2	-1.0	dB
	@0.2-4KHz		Note3	-2.0	dB
Return loss	@ERL	11.0	Note2		dB
	@SRLl	13.0	Note2		dB
	@SRLH	7.0	Note2		dB
	@ERL	9.0	Note3		dB
	@SRLl	13.0	Note3		dB
	@SRLH	3.0	Note3		dB

Note 1 : 1330 ohm in parallel with the series connection of a 348 ohm resistor and a 100nF capacitor .

2 : Test condition : single filter

3 : Test condition : with four added parallel filter.

Schematic:



Dimension :

