



- ◆ Designed to meet IEC-950 isolation.
- ◆ Meet ANSI T1.413 Issue 2, Category1 &2, and ITU G.Lite standards.
- ◆ Designed for Texas Instruments DMT Chipset.

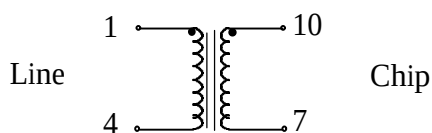
Specification :

Parts No	Turn Ratio (Line:Chip)	L _p (μH)	L _L (μH)max	C _{ww} (pF)max	DCR (Chip) (Ω)max	DCR (Line) (Ω)max
PS106730	2 : 1	75±5%	12.0	10.0	0.55	2.0
PS106731	1.423 : 1	1500±5%	18.0	20.0	0.70	4.0
PS106732	1.429 : 1	71.25-78.75	1.5	25.0	0.22	1.5

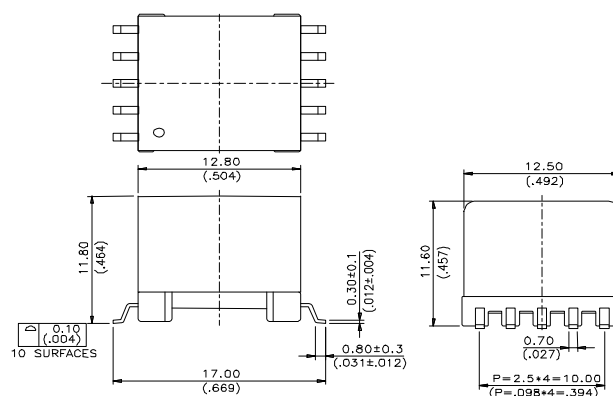
Parts No	Impedance	Longitudinal Balance min	Harmonic Distortion dB max (@100KHz, 15Vp-p)
PS106730	Line:100Ω	50.0	60.0
PS106732	Line:135Ω	60.0	----

Note : PS106730 Longitudinal Balance test condition : 30KHz-1.1MHz

Schematic :



Dimension :



TOLERANCES $\pm 0.25\text{mm}$ ($\pm .010\text{inch}$)