



- ◆ Excellent Longitudinal Balance.
- ◆ Designed for use with ADSL interface IC.
- ◆ Superior distortion characteristic.

Specification :

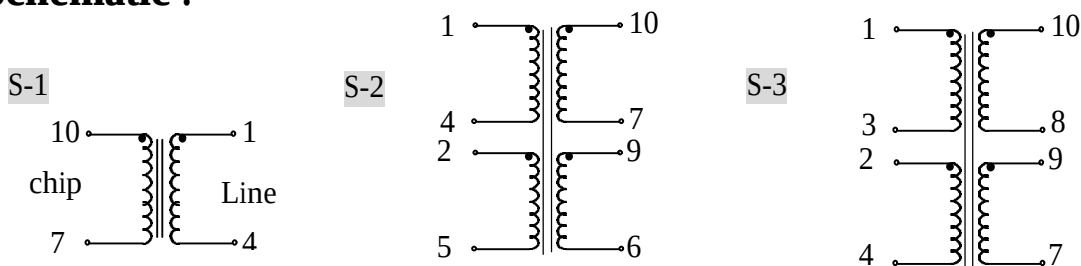
Part No	Turn Ratio (chip:line)	L _p (mH)min	L _L (μH)max	C _{ww} (pF)max	DCR Line (Ω)max	DCR Chip (Ω)max	Schematic
PS102720	1 :1.421	1.425-1.575	18.0	35.0	0.70	4.0	S-1
PS102721	1 :1	1.27±5%	5.0	----	0.53	0.53	S-2
PS102048	1 : 1	5.0mH	10.0	50.0	3.2	3.2	S-3

Part No	Impedance	Longitudinal Balance min	Harmonic Distortion dB max	Insertion loss max	Return loss min
PS102720	Pin(1-4)100Ω Pin(10-7)48.9Ω	60.0	(@100KHz, 15Vp-p) 80.0	----	----
PS102721	100Ω	50.0	80.0	----	----
PS102048	100Ω	50.0	(@30KHz, 4Vp-p) 80.0	0.5	16.0

Note : PS102720,PS102721 Longitudinal Balance test condition : 20KHz-1.1MHz

PS102048 Longitudinal Balance test condition : 1KHz-1MHz

Schematic :



Dimension :

