

YCL

Quad 10Base-T/100Base-TX Transformer Design for 1 : 1 turn ratio on TX side



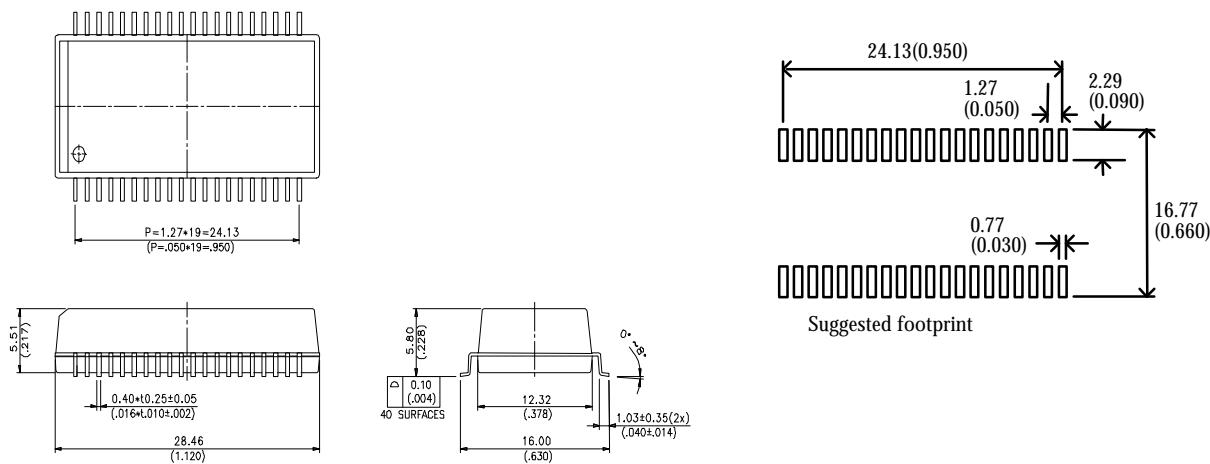
- ◆ Designed to meet IEEE802.3u requirement.
- ◆ Primary inductance 350 μ H min. with 8mA DC Bias
- ◆ Designed to meet IR 235°C peak requirement.
- ◆ 1500Vrms Isolation.

Electrical Characteristic @25°C

Model No.	Turn Ratio TX/RX	Insertion Loss dB Typ 0.1-100MHz	Return Loss dB 30/60/80MHz	Cross Talk dB Typ 60/100MHz	Schematic
PH406003	1:1/1:1	1.0	-16/note/-10	-40/-38	S-1
PH406012	1:1/1:1	1.0	-16/note/-10	-40/-38	S-2
PH406030	1:1/1:1	1.0	-16/note/-10	-40/-38	S-3
PH406064	1:1/1:1	1.0	-16/note/-10	-40/-38	S-4
PH406080	1:1/1:1	1.0	-16/note/-10	-40/-38	S-5
PH406082	1:1/1:1	1.0	-16/note/-10	-40/-38	S-6

Note : Greater than $16 - 20 \log(f/30\text{MHz}) \text{dB}$

Dimension:

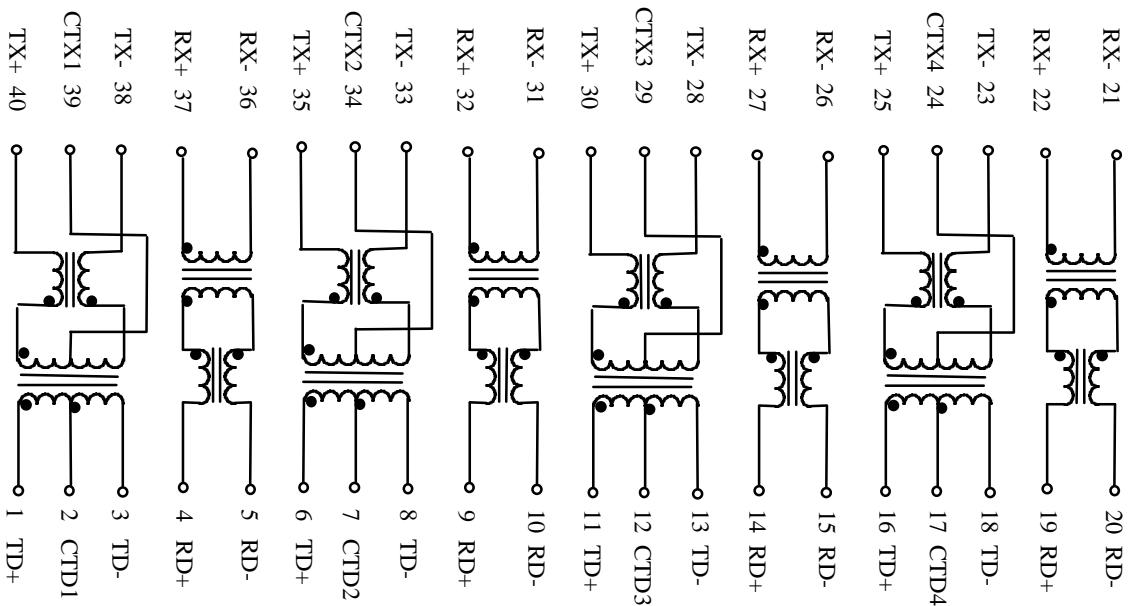


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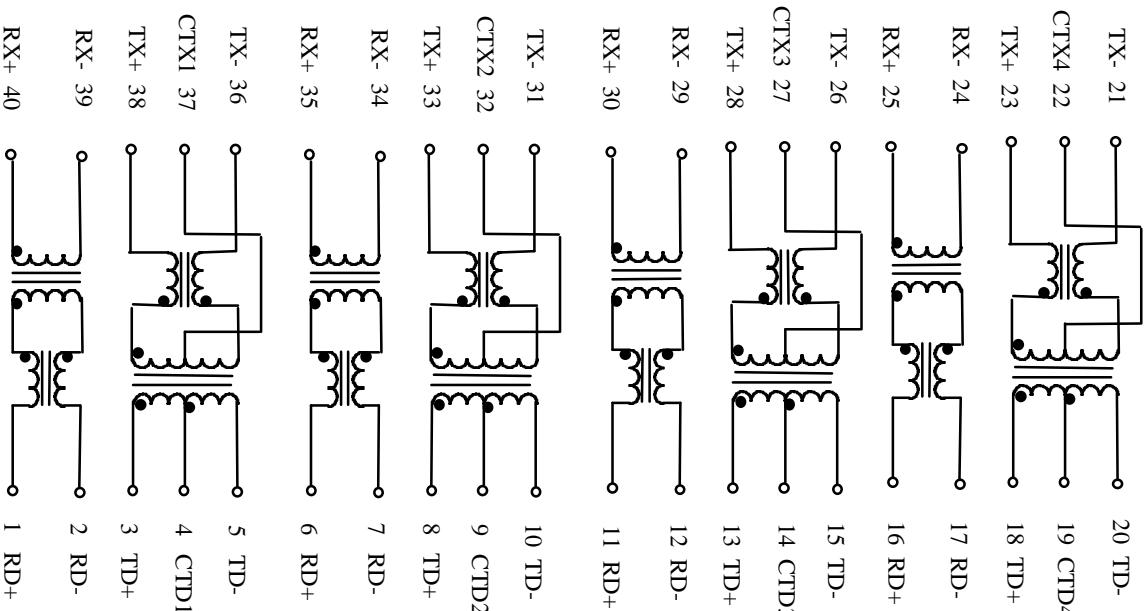
Quad 10Base-T/100Base-TX Transformer Design for 1:1 turn ratio on Tx side

Schematic

S-1



S-2



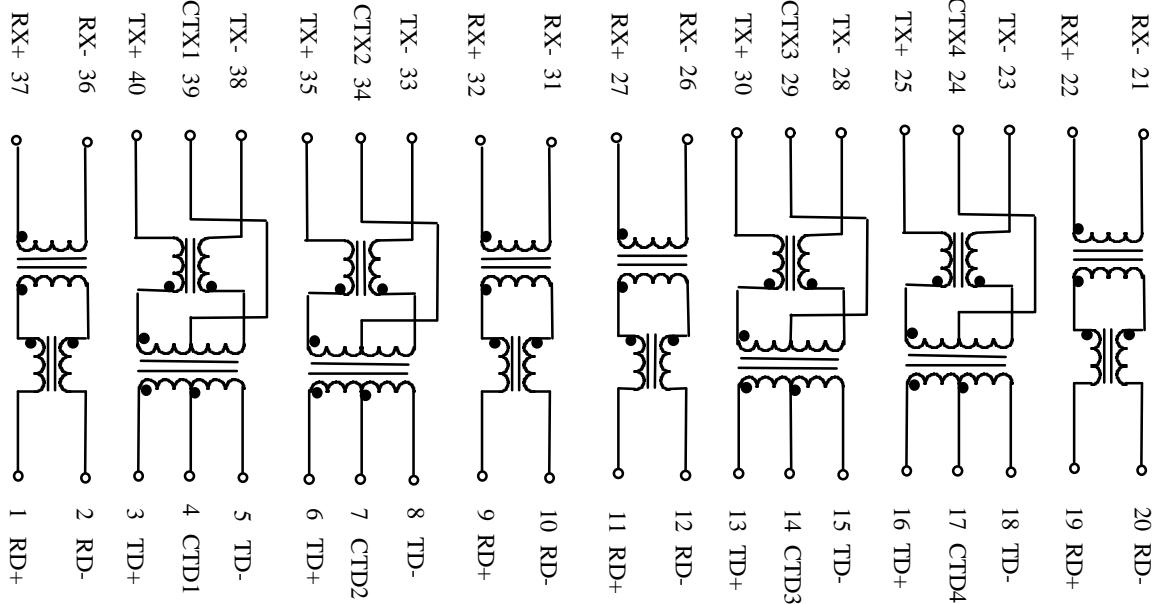
Date : Jan.03,2000

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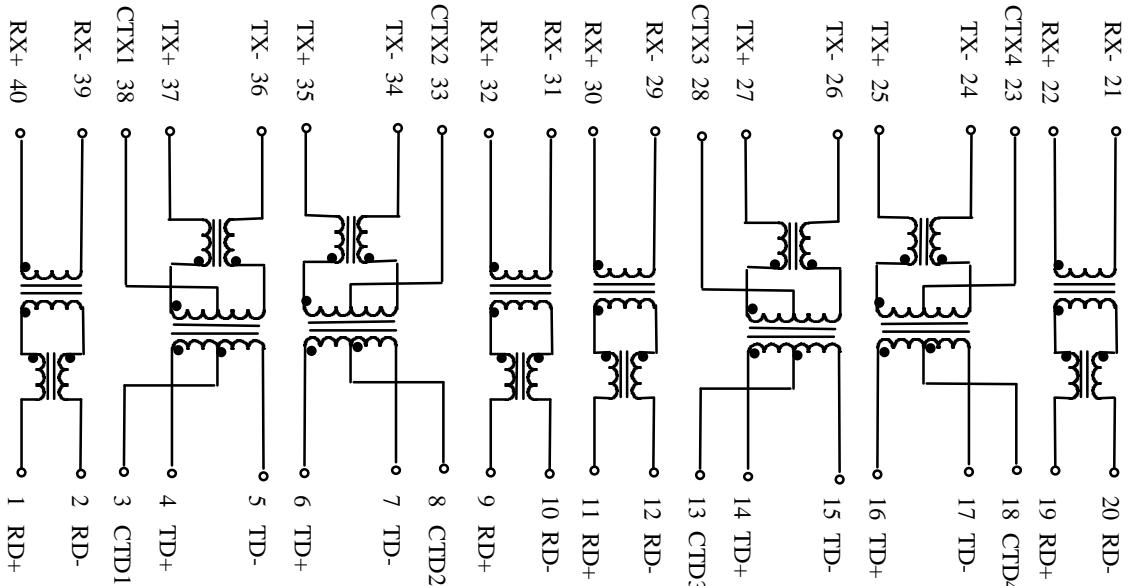
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Schematic

S-3



S-4

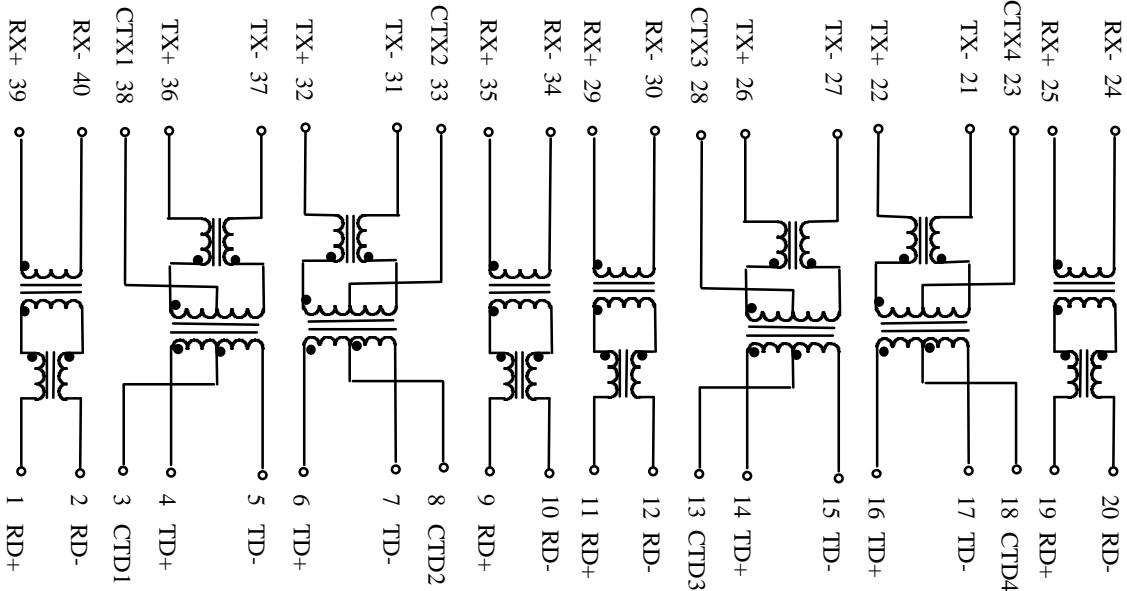


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Quad 10Base-T/100Base-TX Transformer Design for 1:1 turn ratio on Tx side

Schematic

S-5



S-6

