

June, 1998 Preliminary

AMI 0.6 micron CMOS
C6L Double Poly

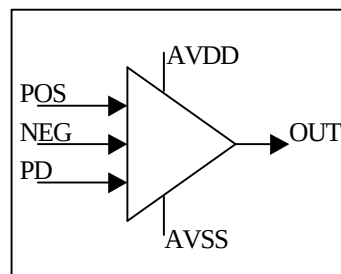
Internal Opamp

Features

- 86-91dB gain
- gain bandwidth >1MHz
- input CMR 0.5-3.5V

Description

2 stage opamp with p channel differential input pair for driving loads of 100K Ω and 20pf



Schematic Symbol

PIN DESCRIPTION

NAME	TYPE	DESCRIPTION
POS	Analog input	Positive opamp input
NEG	Analog input	Negative opamp input
PD	Analog input	Power down input (Active High)
AVDD	Analog supply	+5V supply
AVSS	Analog supply	Analog ground
OUT	Analog output	Opamp output

AC ELECTRICAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIO N	MIN	TYP	MAX	UNITS
Vdd	Power Supply Voltage		4.5	5	5.5	V
Idc	Supply Current			500		μ A
T	Ambient Temperature		-40		125	$^{\circ}$ C
G	Open Loop Gain		86		91	dB
BW	Bandwidth		1		4.4	MHz
SRHL	Slew Rate high to low		3			V/ μ sec
SRLH	Slew Rate low to high		3			V/ μ sec
PM	Phase Margin	C _L = 20pf	68		78	degrees
ICMR	Input Common Mode Range		VSS+0.5		Vdd-2	V