

June, 1998 Preliminary

AMI 0.8 micron CMOS
CWM Double Poly

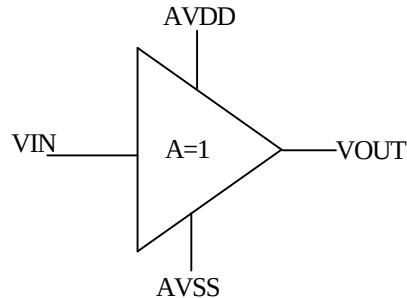
Voltage Follower Output Buffer

Features

- Low power AB-class operation
- 2mA sink / source current
- Includes pad cell

Description

Low power class AB output buffer capable of sourcing or sinking currents up to 2 mA. Includes pad and protection circuitry.



PIN DESCRIPTION

NAME	TYPE	DESCRIPTION
AVDD	Positive Supply	+3.5V to +5.5V supply
AVSS	Negative Supply	
VIN	Analog Input	Input Voltage
VOUT	Analog Output	Output Voltage

ELECTRICAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITION	MIN	MAX	UNITS
T	Temperature		-40	95	°C
V	Supply Voltage		3.5	5.5	V
IDD	Quiescent Current		-	70	uA
Gerr	Gain Error	Systematic only	-	0.05	%
Voff	Offset Voltage	Systematic only	-	0.6	mV
PSRRdc	PSRR @ DC		71	-	dB
ISS	Source/Sink Current	@Vout=1.25V	2.4	-	mA
CMIR	CMIR		-0.58	1.79	V
Cin	Input Capacitance		-	3	pF