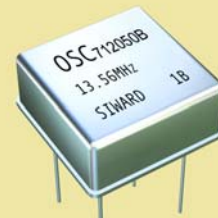


# OSC72, OSC71

CLOCK OSCILLATOR

DIP



## FEATURES

Short Lead Time  
Can Be Programmed Twice

## DIMENSION

OSC72: 13.2 x 13.2 x 5.08[mm]  
OSC71: 20.8 x 13.2 x 5.08[mm]

## TYPICAL SPECIFICATIONS

|                            |                                                                          |                           |
|----------------------------|--------------------------------------------------------------------------|---------------------------|
| Nominal frequency range    | 1 ~ 133MHz                                                               | 1 ~ 110MHz                |
| Frequency stability        | $\pm 50 \times 10^{-6}$ , $\pm 100 \times 10^{-6}$                       |                           |
| Temperature range(°C)      | Operating: 0 ~ 70°C, -20 ~ + 70°C, -40 ~ + 85°C<br>Storage: -55 ~ +125°C |                           |
| Power Supply Voltage (Vdd) | 5V                                                                       | 3.3V                      |
| Current consumption        | 45mA Max. : Freq < 133 MHz                                               | 25mA Max. : Freq < 100MHz |
| Output                     | TTL-CMOS (See Table A)                                                   |                           |

## TABLE A

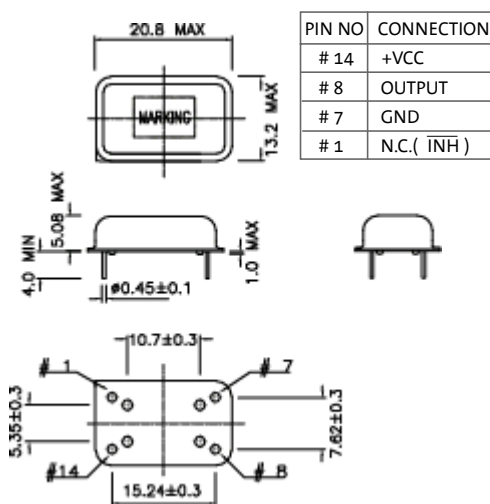
|                  |                                                          |                  |                 |
|------------------|----------------------------------------------------------|------------------|-----------------|
| Output level     | CMOS                                                     |                  | TTL             |
| Load             | 15 pF / 50 pF Max.                                       |                  | 25 / 50 pF Max. |
| Output voltage   | "0" level                                                | VOL: 0.1Vdd Max. | VOL : 0.4V Max. |
|                  | "1" level                                                | VOH: 0.9Vdd Min. | VOH : 2.4V Min. |
| Rise & fall time | 0.8V ~ 2 V, 4.5-5.5Vdd, CL = 50 : 1.8ns Max.             |                  |                 |
|                  | 0.8V ~ 2 V, 4.5-5.5Vdd, CL = 25 : 1.2ns Max.             |                  |                 |
|                  | 0.8V ~ 2 V, 4.5-5.5Vdd, CL = 15 : 0.9ns Max.             |                  |                 |
|                  | 0.2V ~ 0.8 Vdd, 4.5-5.5Vdd, CL = 50 : 3.4ns Max.         |                  |                 |
|                  | 0.2V ~ 0.8 Vdd, 3.0-3.6Vdd, CL = 30 : 4.0ns Max.         |                  |                 |
|                  | 0.2V ~ 0.8 Vdd, 3.0-3.6Vdd, CL = 15 : 2.4ns Max.         |                  |                 |
| Symmetry         | 45~55% at 50% Vdd F≤69.9MHz<br>40~60% at 50% Vdd F>70MHz |                  | 40~60% at +1.4V |
| Aging            | ± 5 ppm / Year                                           |                  | ± 5 ppm / Year  |

\* Please consult with us for other specification.

## OUTLINE DIMENSIONS

Unit:mm

### OSC-71



### OSC-72

